ABSTRACT

APPARATUS AND METHOD FOR ERASING DATA IN A NON-VOLATILE MEMORY DEVICE

The present disclosure relates to an apparatus and method for erasing data in a non-volatile memory device. A memory device includes plural non-volatile memory cells and a control circuit. The plural non-volatile memory cells can store data and are arranged in series between a bit line and a source line. The control circuit synchronizes discharge of charges, which are accumulated in a channel formed by the plural non-volatile memory cells, through the bit line and the source line during an erase operation for erasing the data stored in the plural non-volatile memory cells.

FIG. 1