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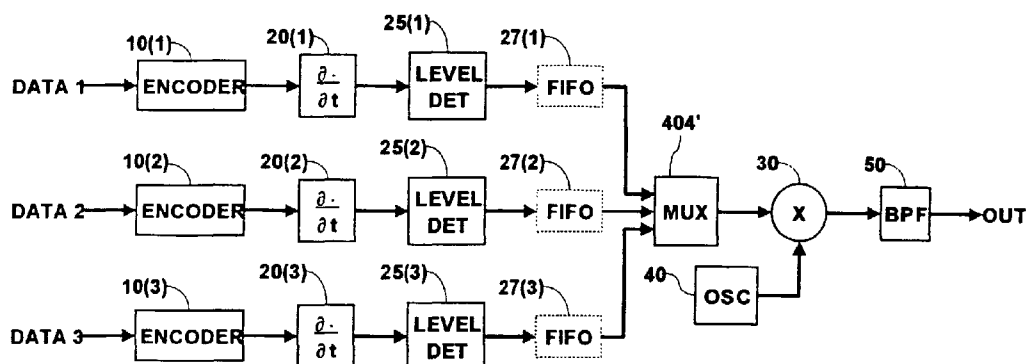
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(54) Title: DATA TRANSMISSION USING PULSE WIDTH MODULATION



(57) Abstract: A digital modulator (fig 11) includes a source of a plurality of digital data signals which have a common data bit period. A plurality of encoders each encodes a corresponding one of the plurality of digital data signals using a variable pulse width code. A plurality of pulse signal generators each generates respective pulses, which represent the edges of the corresponding digital data signals. A digital data demodulator (fig 12) includes a source of a modulated signal which includes successive bit periods, representing variable pulse width encoded digital signals. A demodulator demodulates the modulated signal to generate pulses corresponding to received carrier pulses.



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