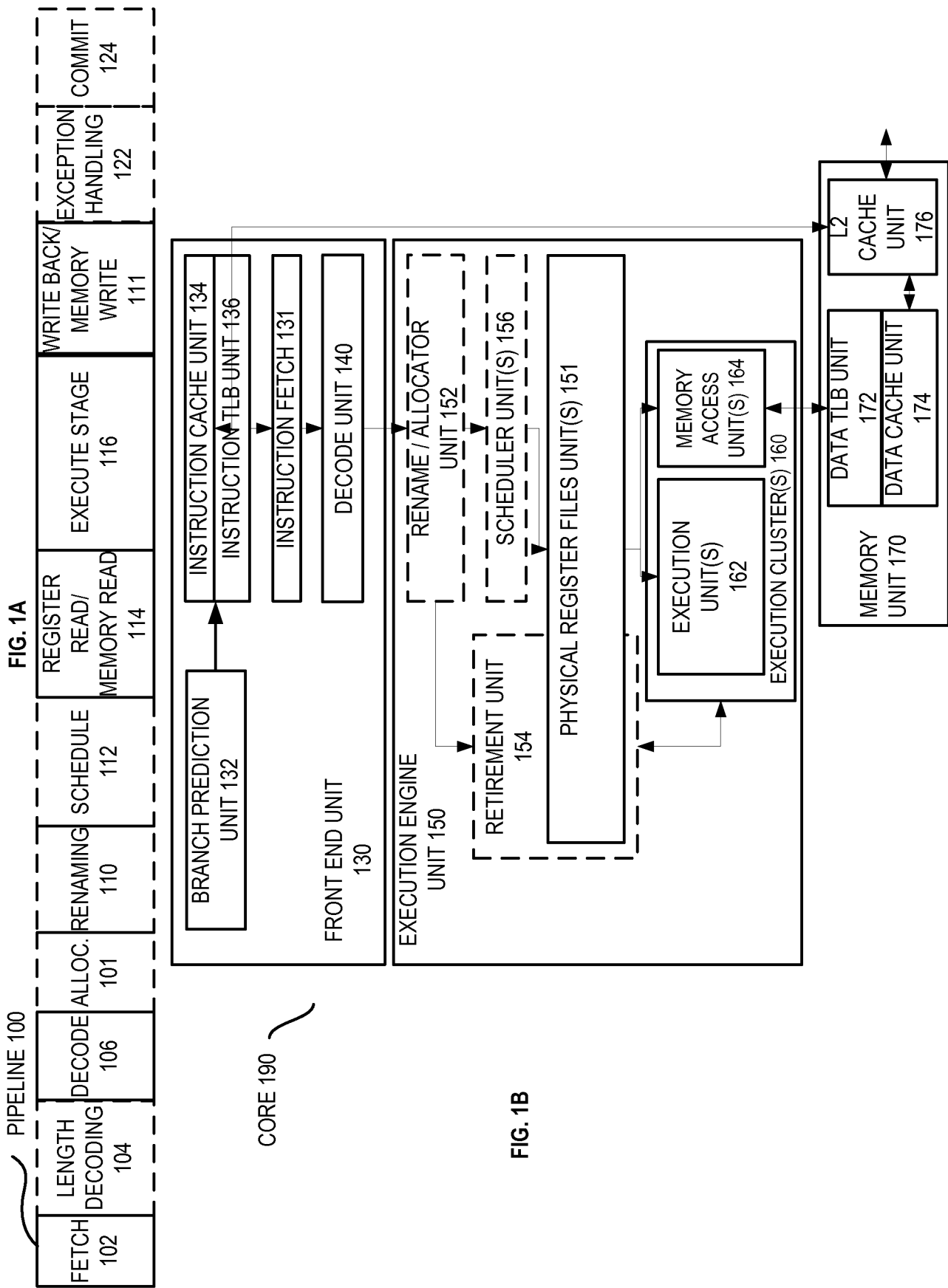




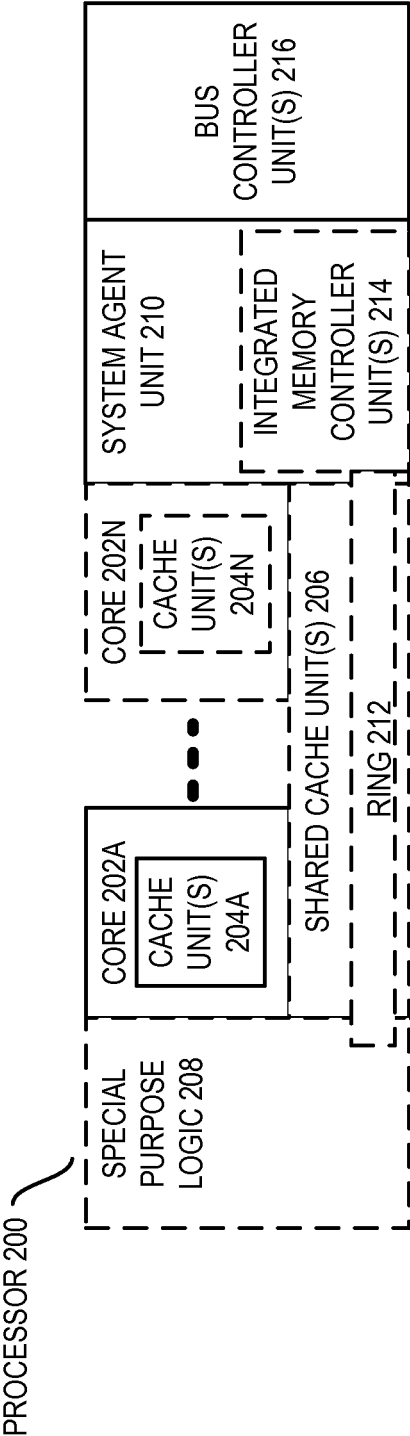


FIG. 2

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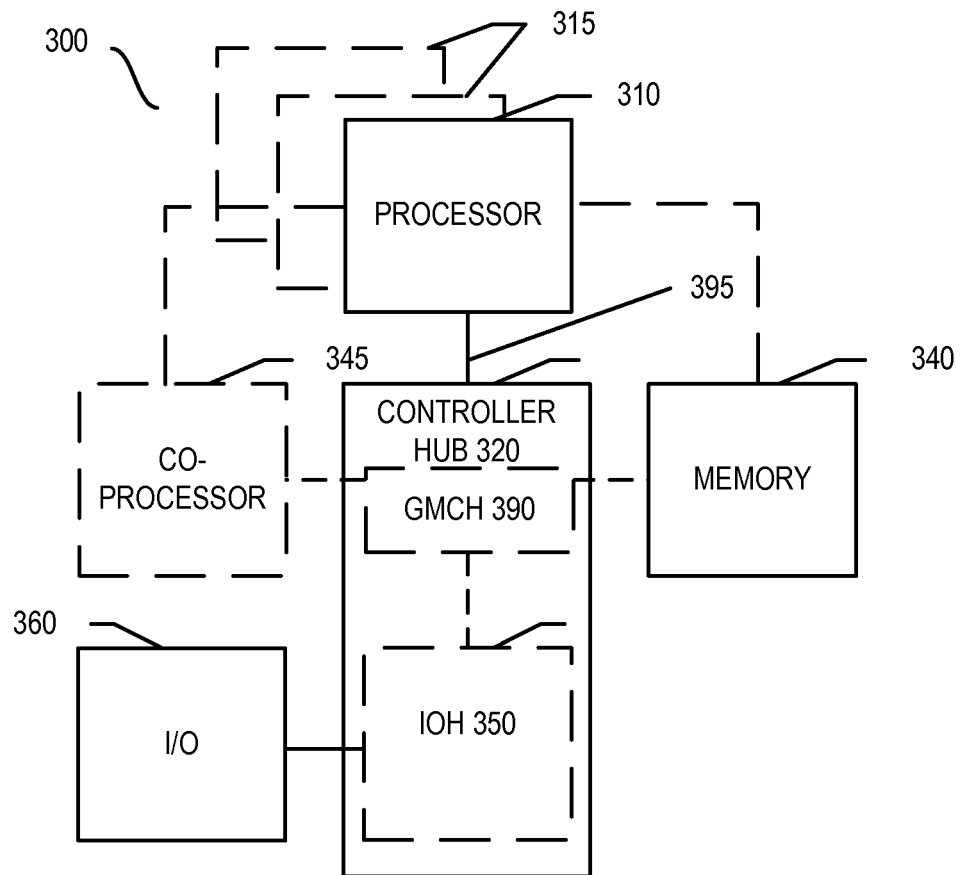


FIG. 3

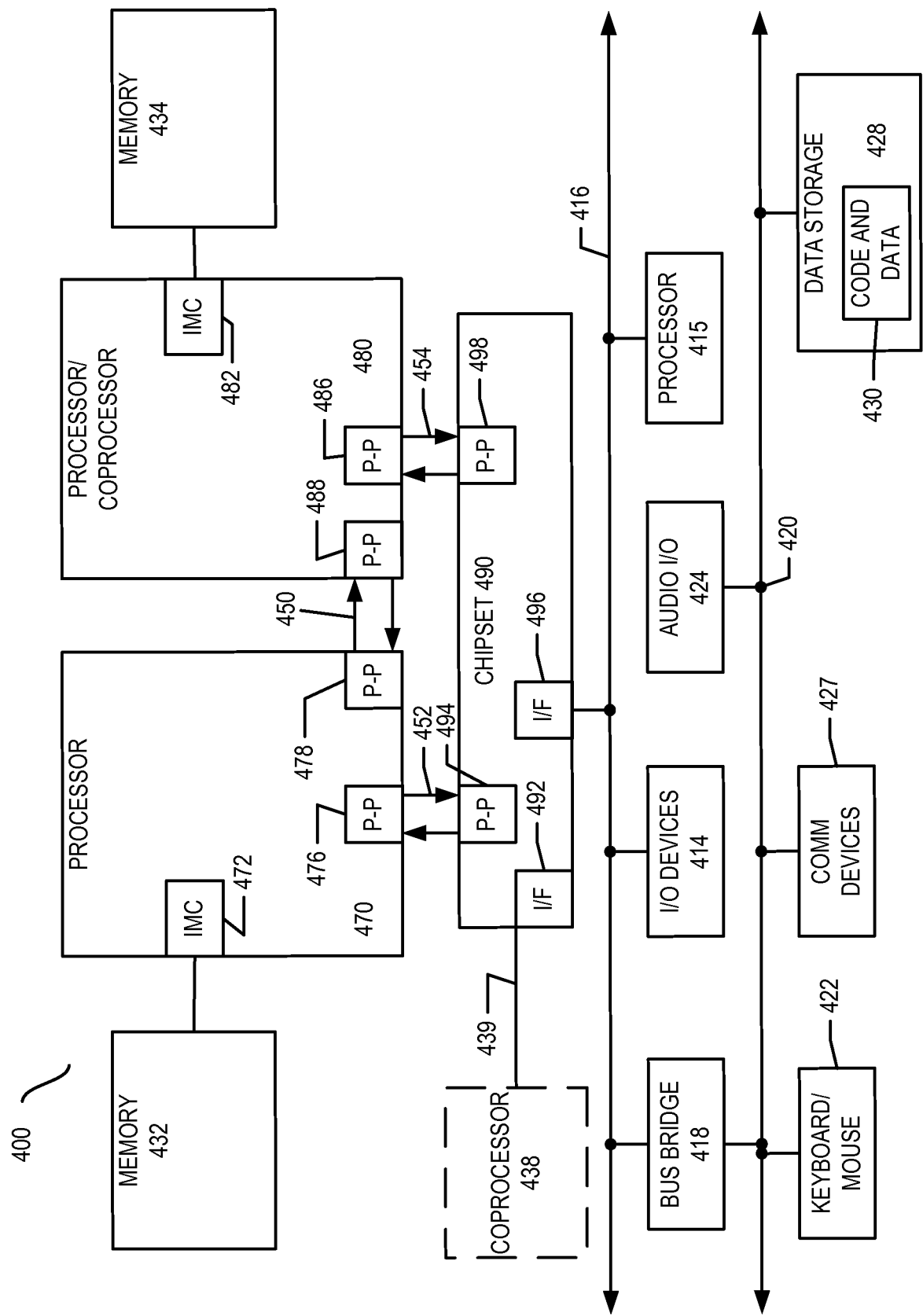


FIG. 4

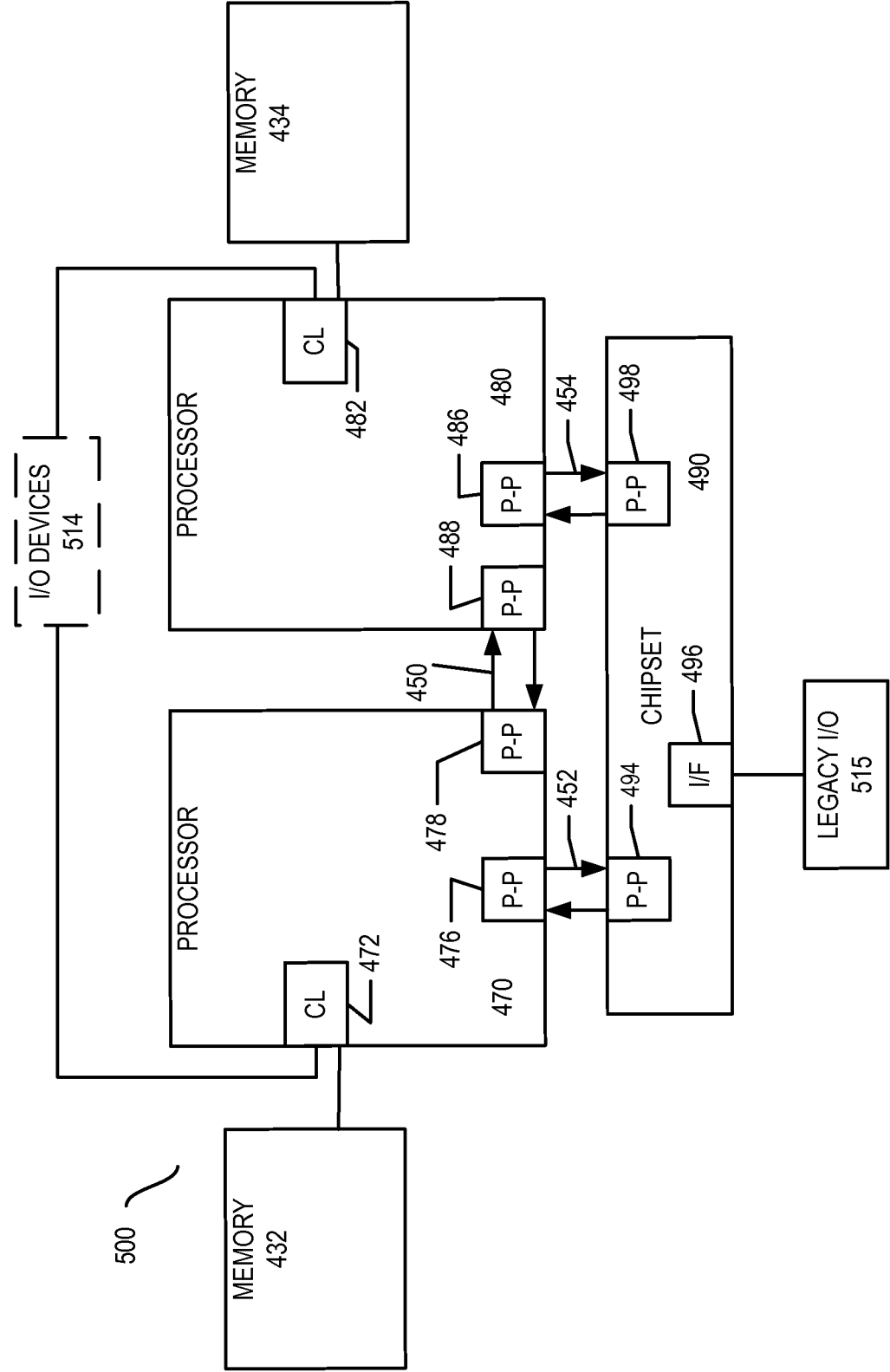


FIG. 5

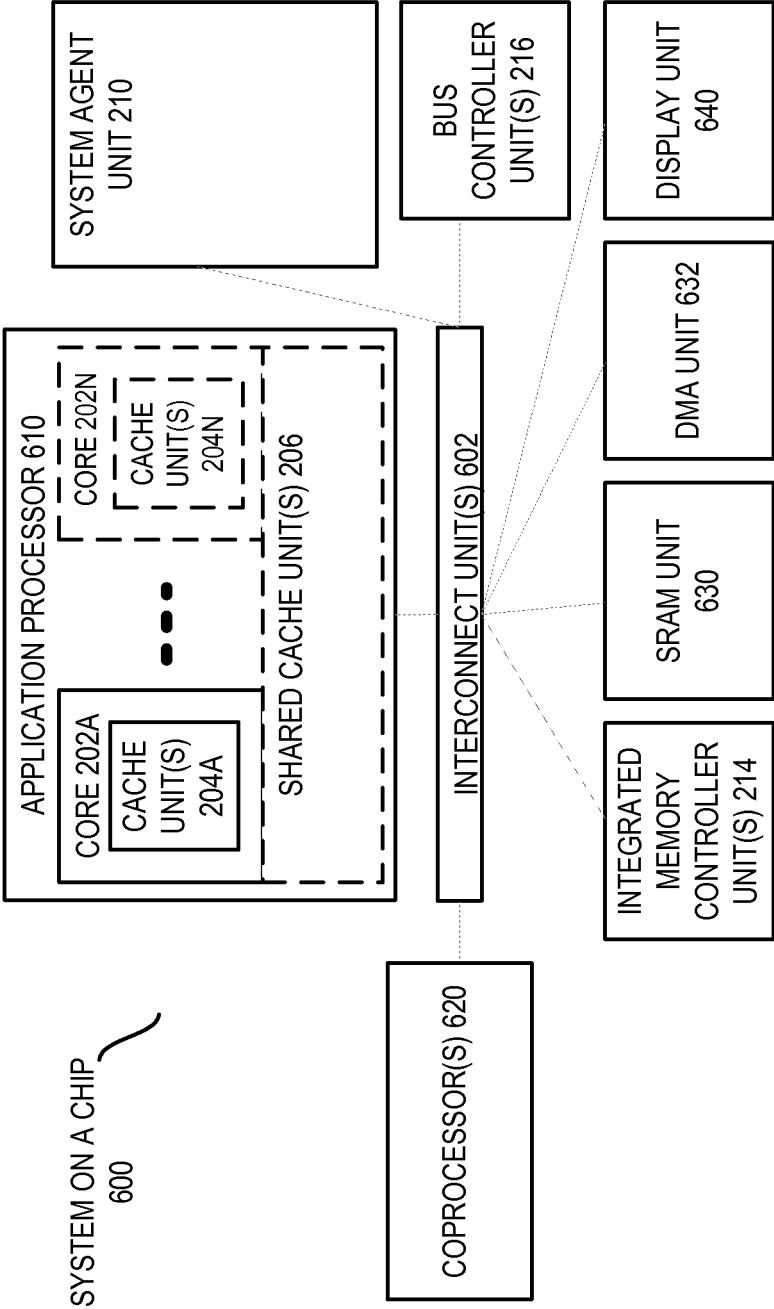


FIG. 6

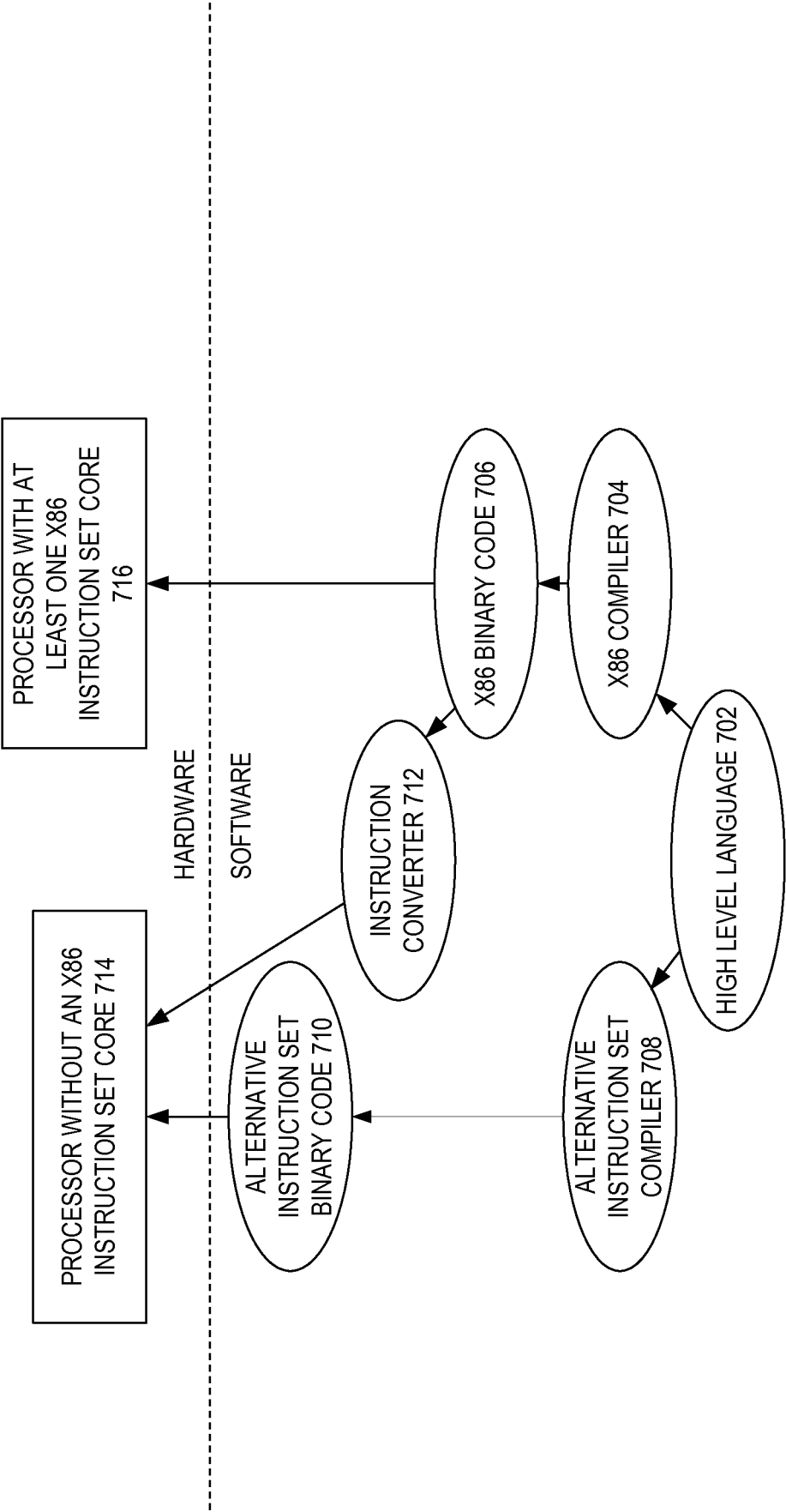


FIG. 7

800 →

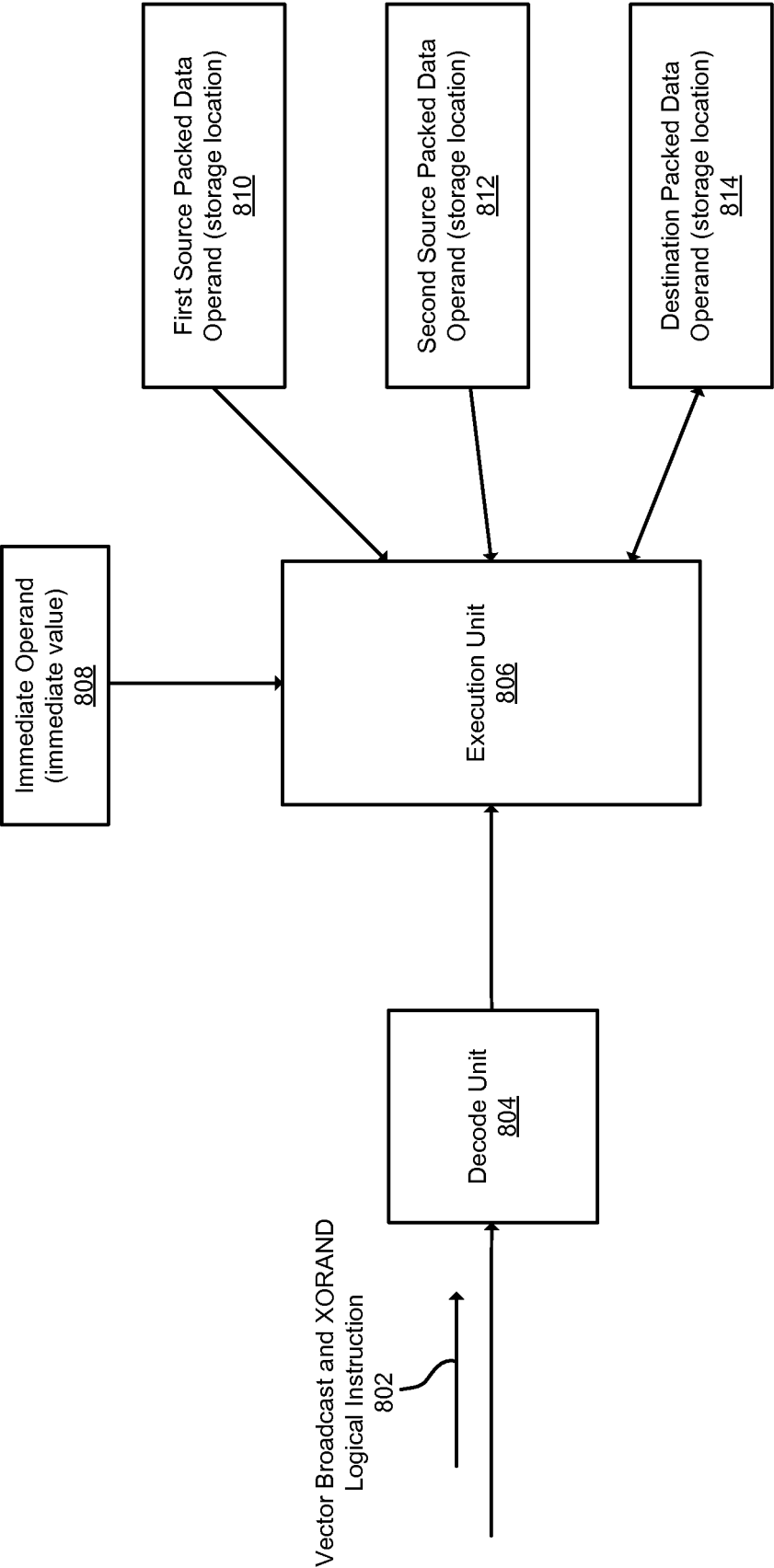
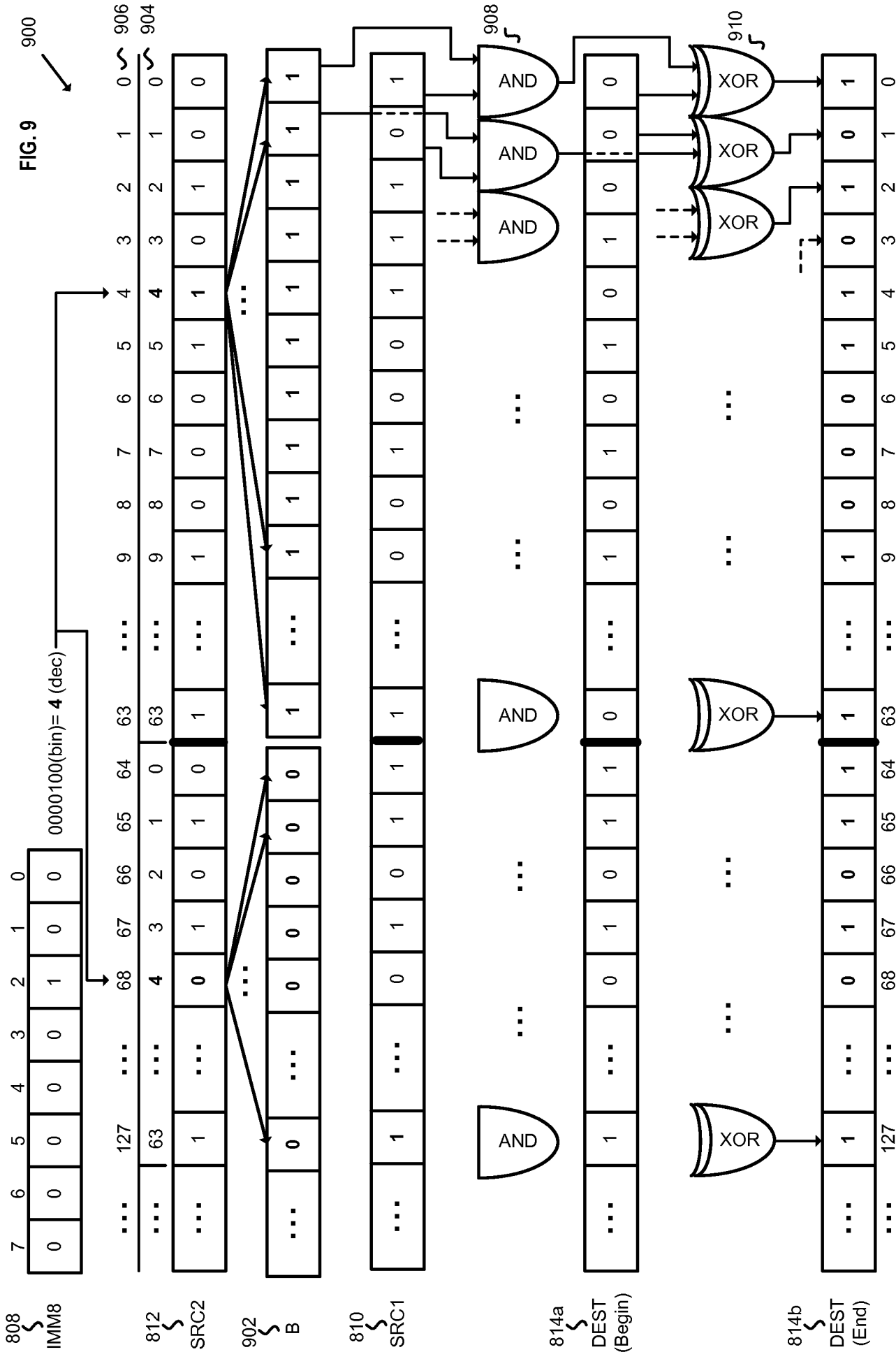
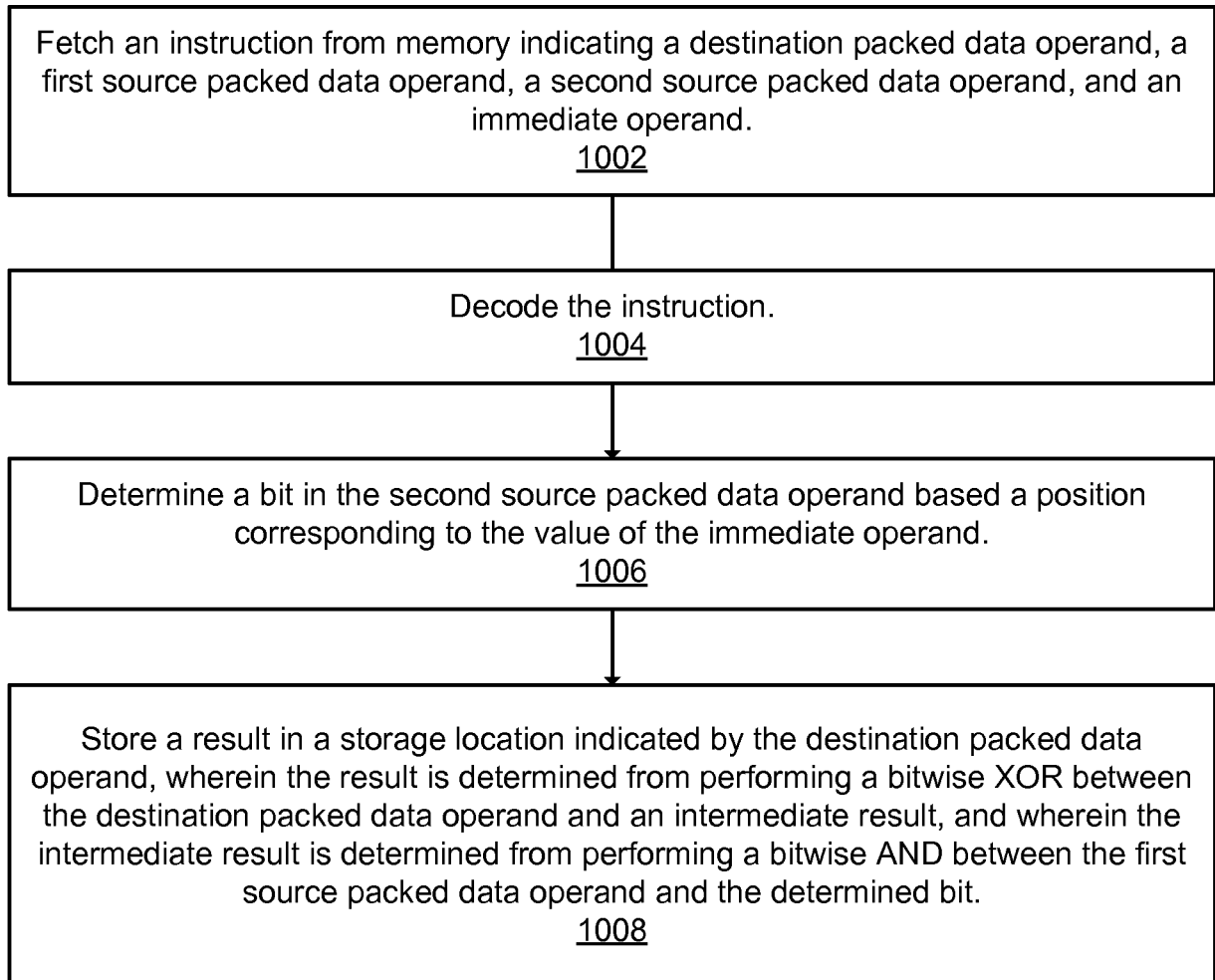


FIG. 8



10/17**FIG. 10**

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```

      1152      1154      1162      1156      1158      1160
      ↓        ↓        ↓        ↓        ↓        ↓
vxorandbcstq zmm0 {k1}, zmm1, zmm2, imm8
1102 → (KL, VL) = (2, 128), (4, 256), (8, 512)
1104 → FOR j := 0 to KL-1
1106 →   i := j * 64
1108 →   B[63:0] := SRC2[i+IMM8] replicated 64 times
1110 →   IF k1[j]
1112 →     DEST[63+i:i] := DEST[63+i:i] ^ (SRC1[63+i:i] & B)
           ELSE
1114 →     IF *merging-masking* // merging-masking
1116 →     THEN *DEST[63+i:i] remains unchanged*
           ELSE // zeroing-masking
1118 →     DEST[63+i:i] = 0
           FI
           ENDFOR
1120 → DEST[MAX_VL-1:VL] := 0

```

FIG. 11

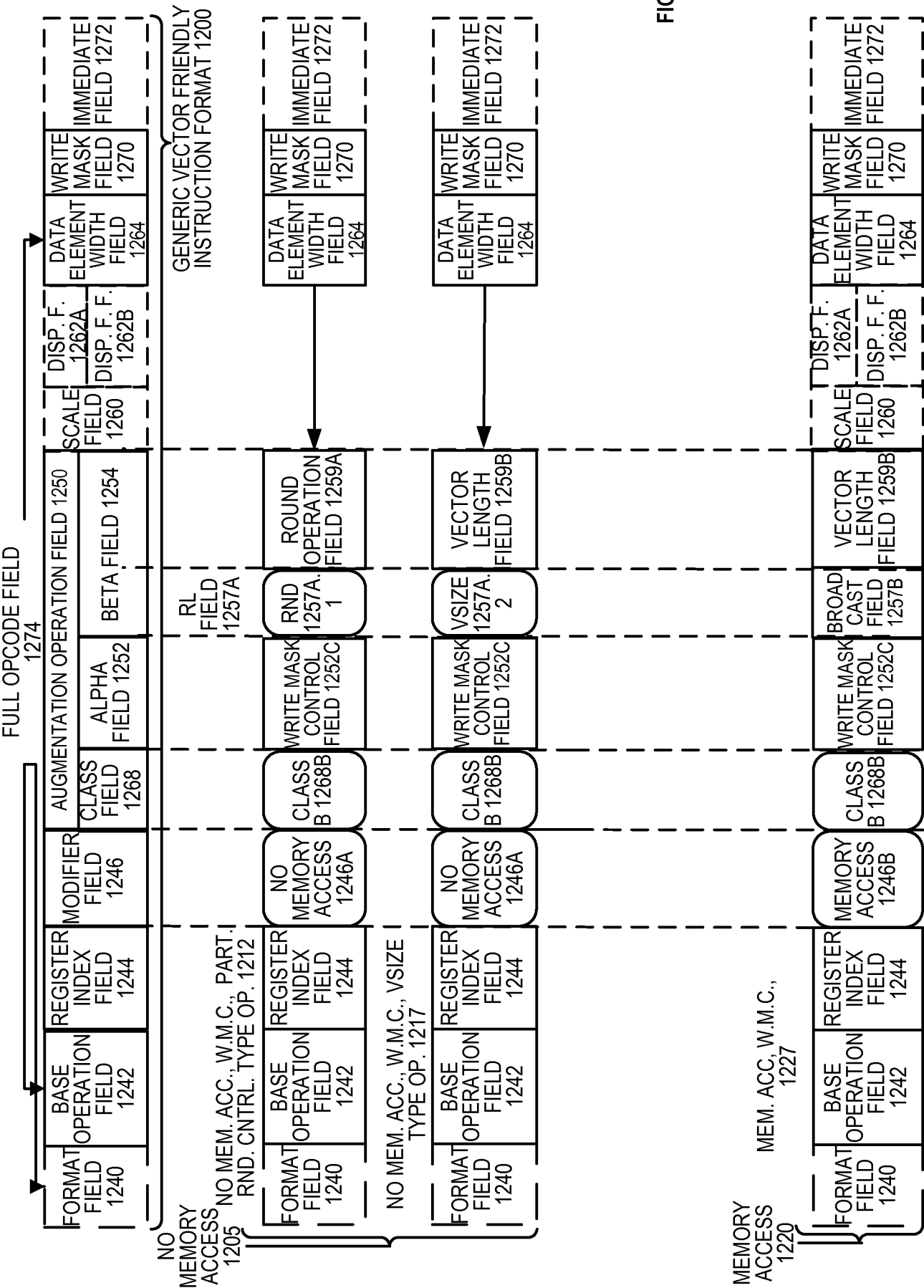


FIG.12B

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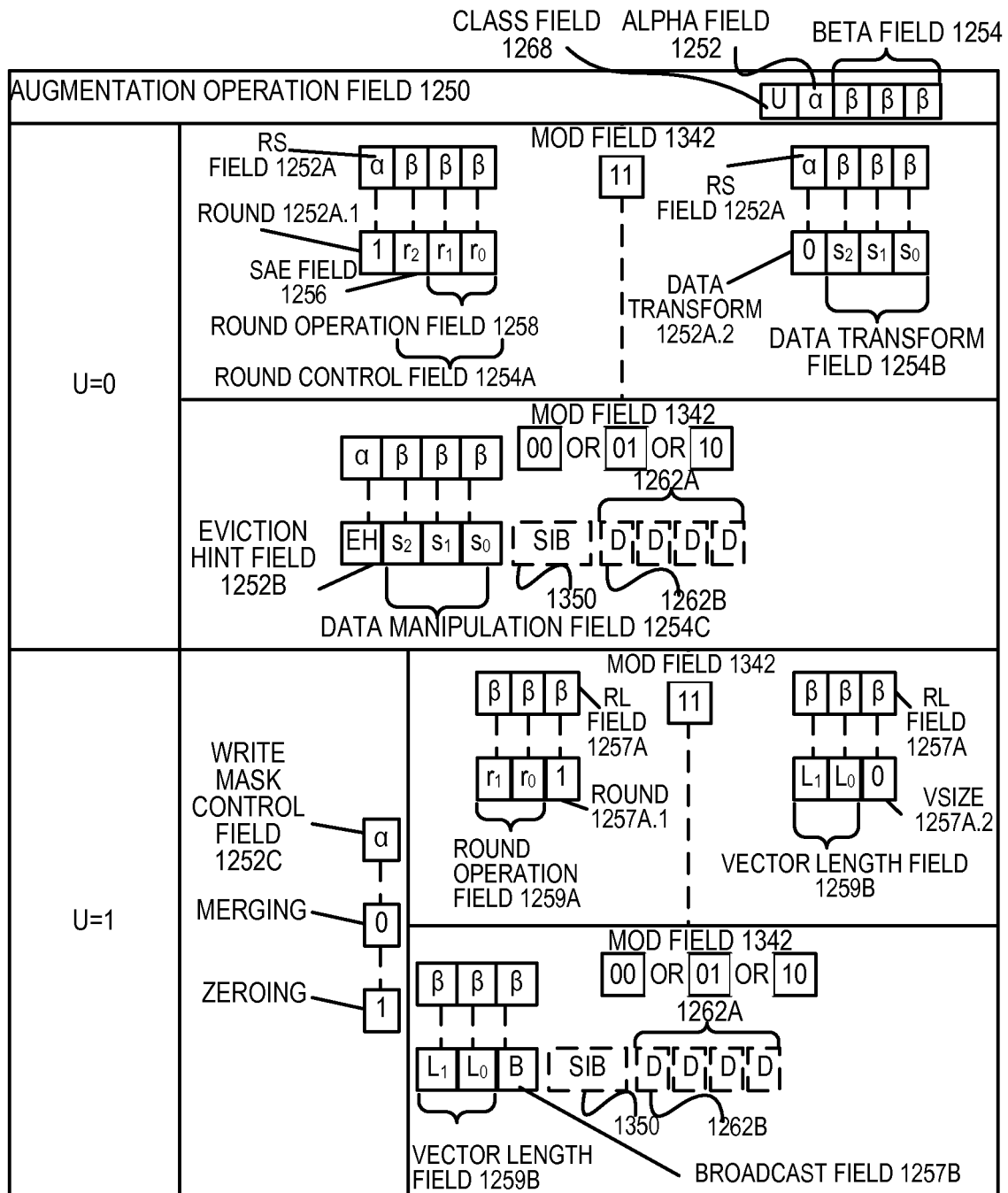
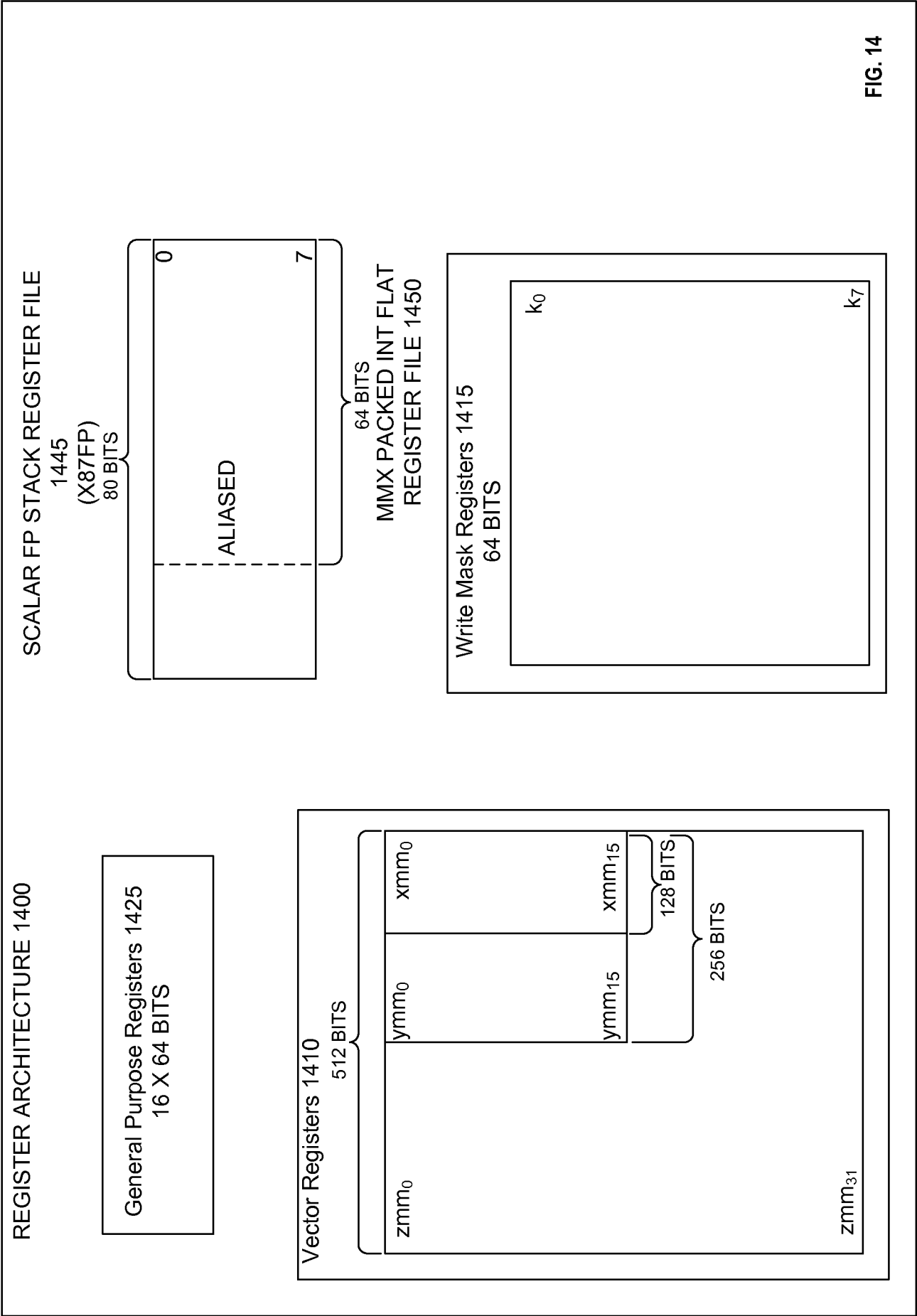


FIG. 13D



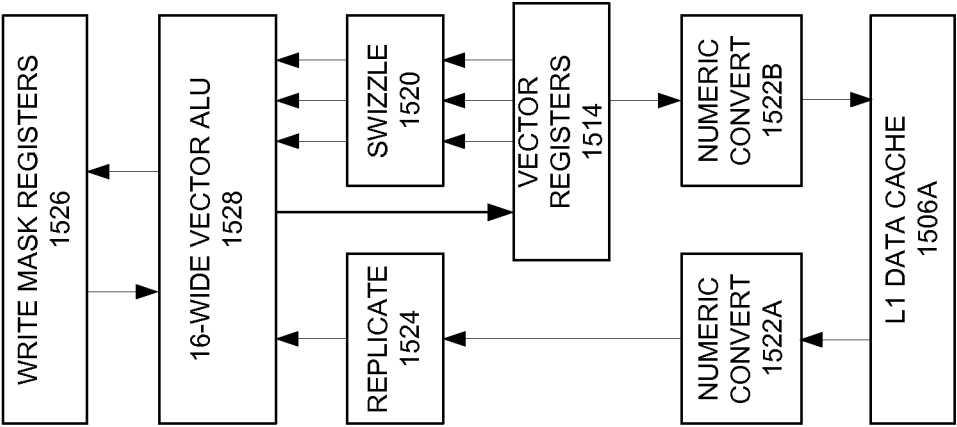


FIG. 15A

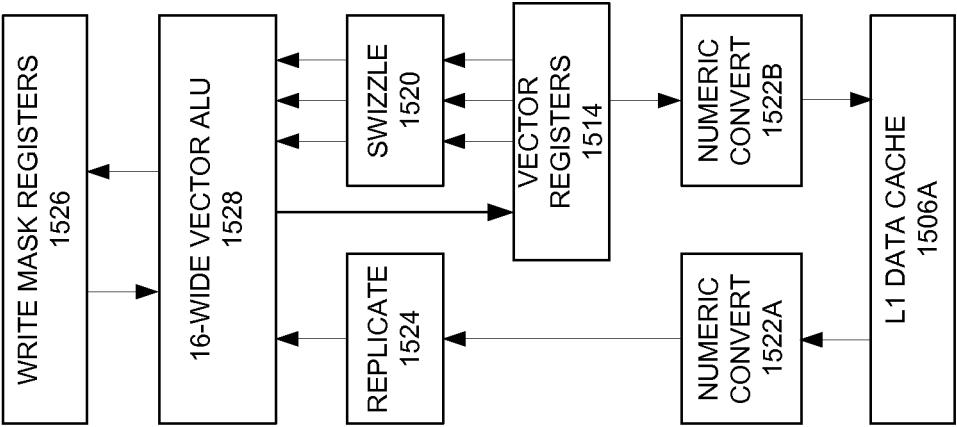


FIG. 15B