

ABSTRACT:

Circuit arrangement comprising a tuner and a splitter-modulator.

A circuit arrangement is described, comprising a tuner and a splitter-modulator comprising a splitter and a modulator, the splitter having an RF signal input for applying an RF input signal, an RF signal output for supplying an RF output signal, a first line for applying the RF input signal to the tuner and a second line for supplying an RF
5 output signal from the modulator, the tuner being adapted to demodulate the RF input signal and the modulator being adapted to convert a baseband signal into the RF output signal to be supplied by said modulator, and comprising devices for generating reference oscillations for the tuner and the splitter-modulator, said devices having at least one quartz crystal which is common for the tuner and the splitter-modulator, and a phase-locked loop which is common
10 for the tuner and the splitter-modulator.

A possibly compact construction by using a minimal number of components at low manufacturing costs is achieved in that the tuner comprises at least one signal-processing branch for frequency-selective reception, amplification and/or frequency conversion of the RF signals of each frequency band, and the devices for generating
15 reference oscillations (oscillation generator) comprise a separate oscillator stage for each signal-processing branch, one of these oscillator stages also being used for generating the reference oscillation for the splitter-modulator.

Fig. 2.

This Sheet Has Been Replaced

