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(54) Title: DETECTING ATTACKS ON POWER SUPPLIES BY COMPARING TWO SHIFT REGISTERS

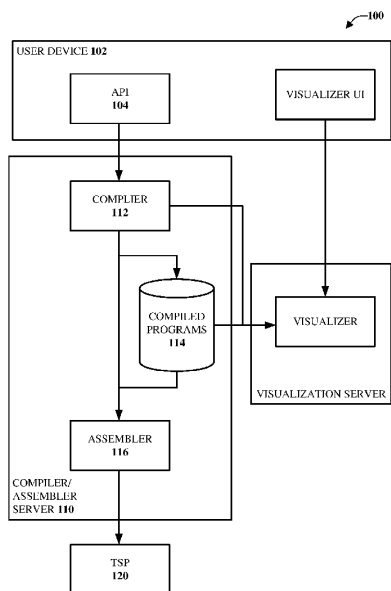


FIG. 1

(57) Abstract: Described are embodiments related to counter threat vector. A system includes two Linear Feedback Shift Registers (LFSR) of an equal size in the number of bits, initialized at power up or reset to an initial condition, wherein the values of the two LFSRs are compared during each clock cycle and if there is a mismatch an error is reported and threat mitigation initiated. This Abstract and the independent Claims are concise signifiers of embodiments of the claimed inventions. The Abstract does not limit the scope of the claimed inventions.