

10

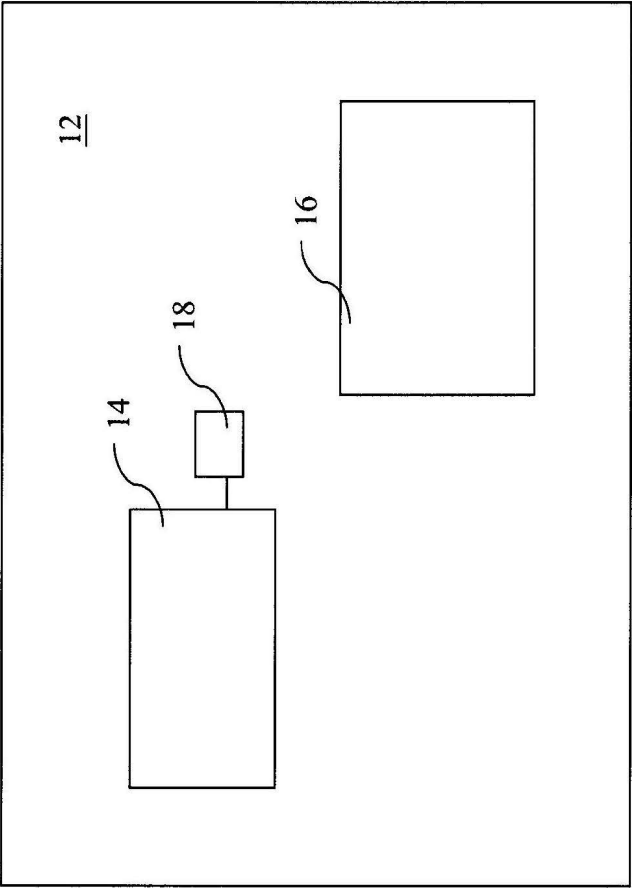


Fig. 1

14

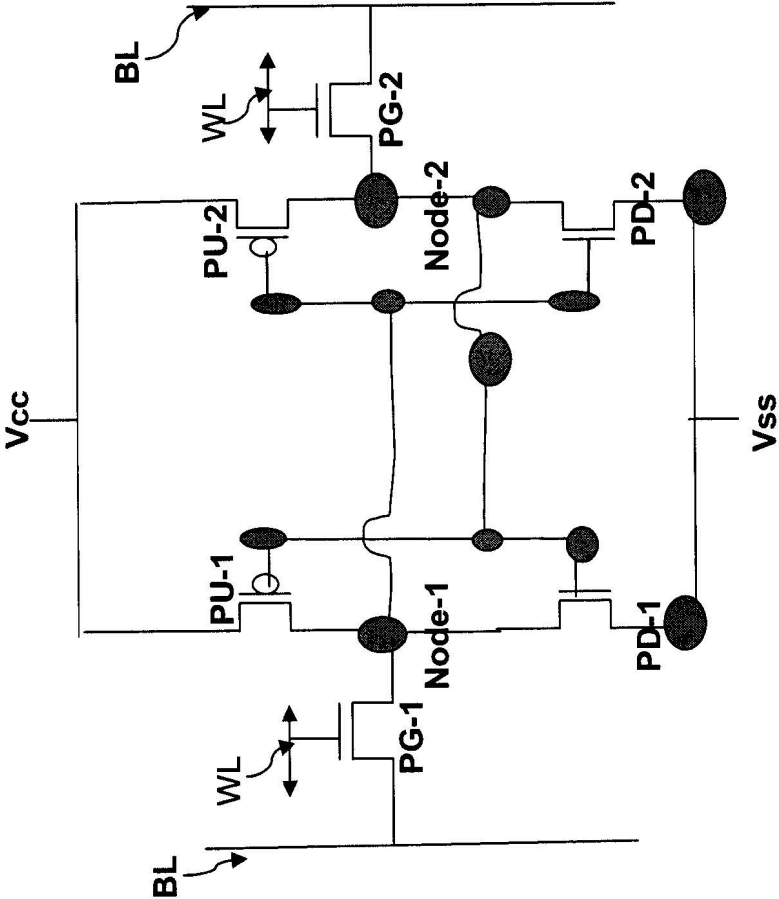


Fig. 2

16

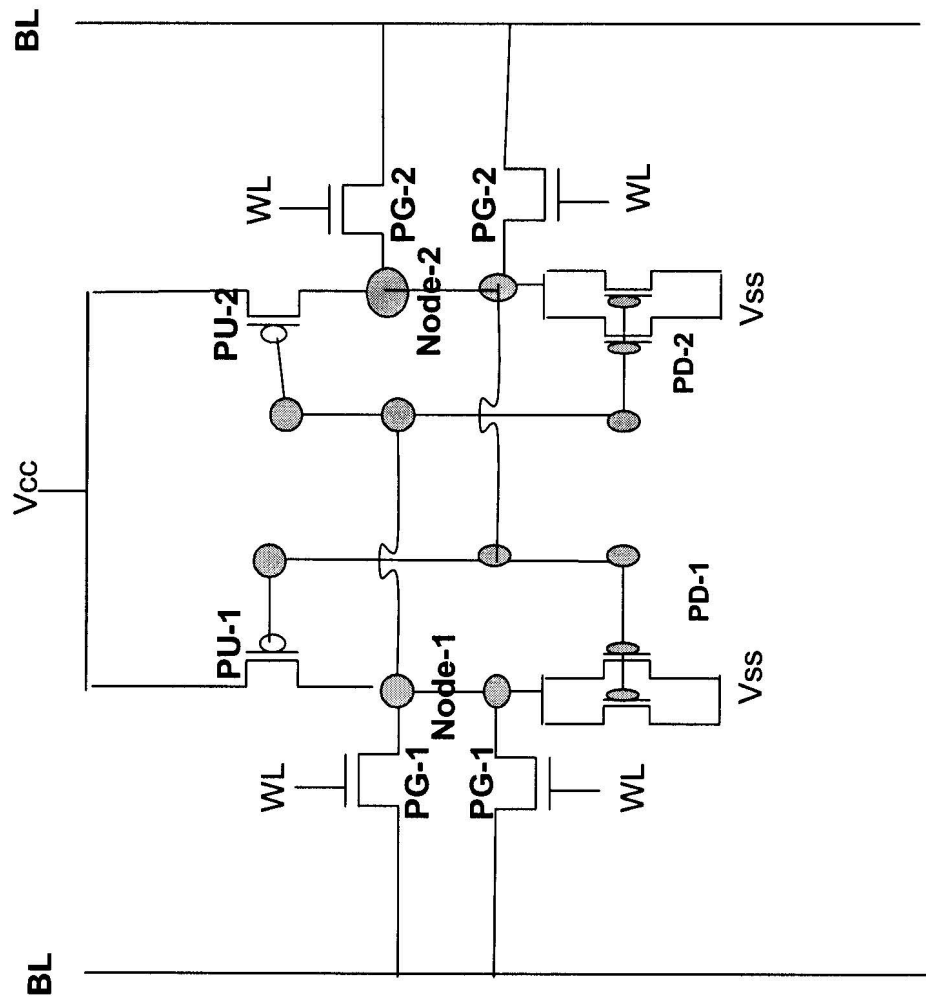


Fig. 3

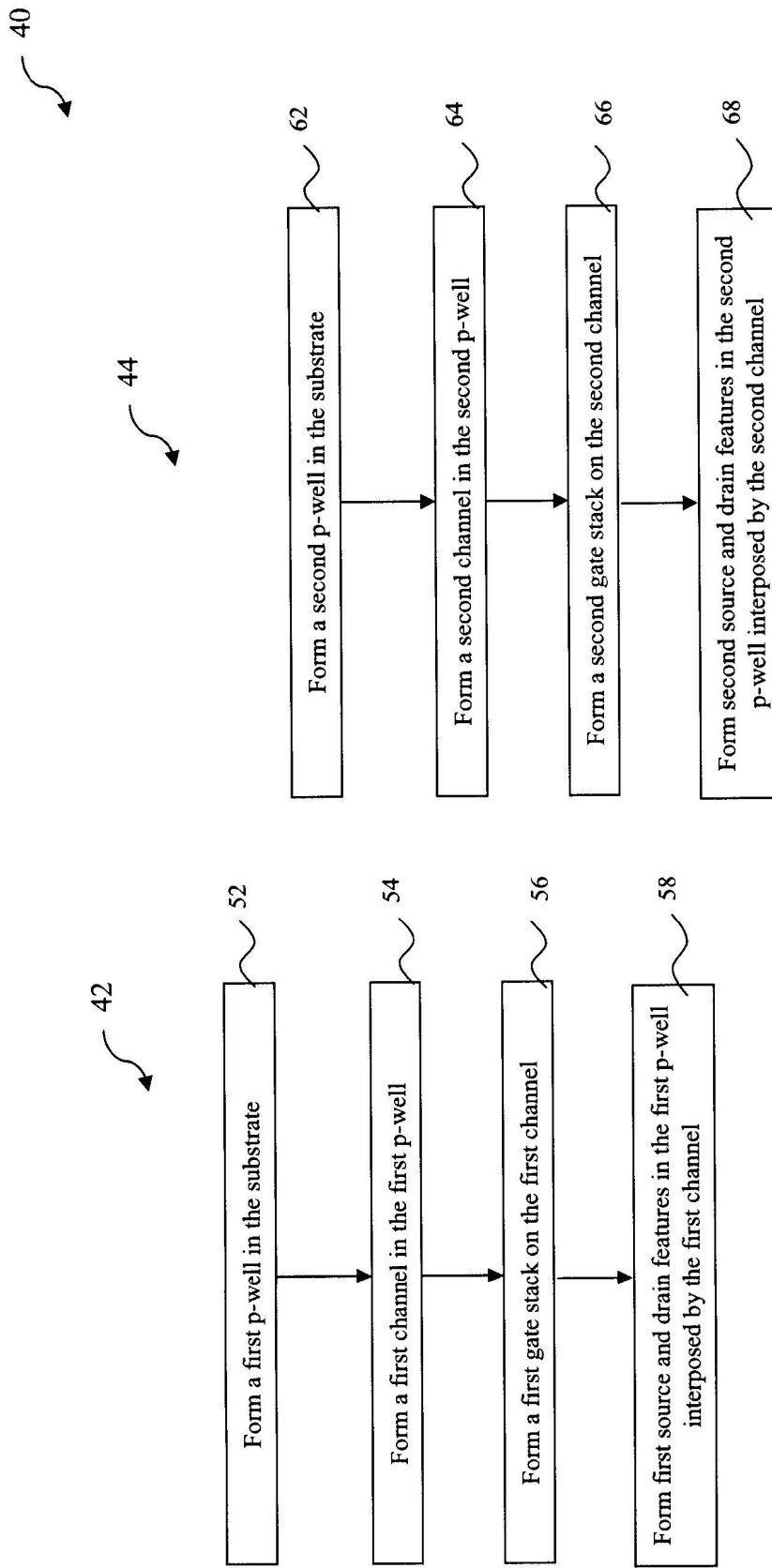


Fig. 4

80

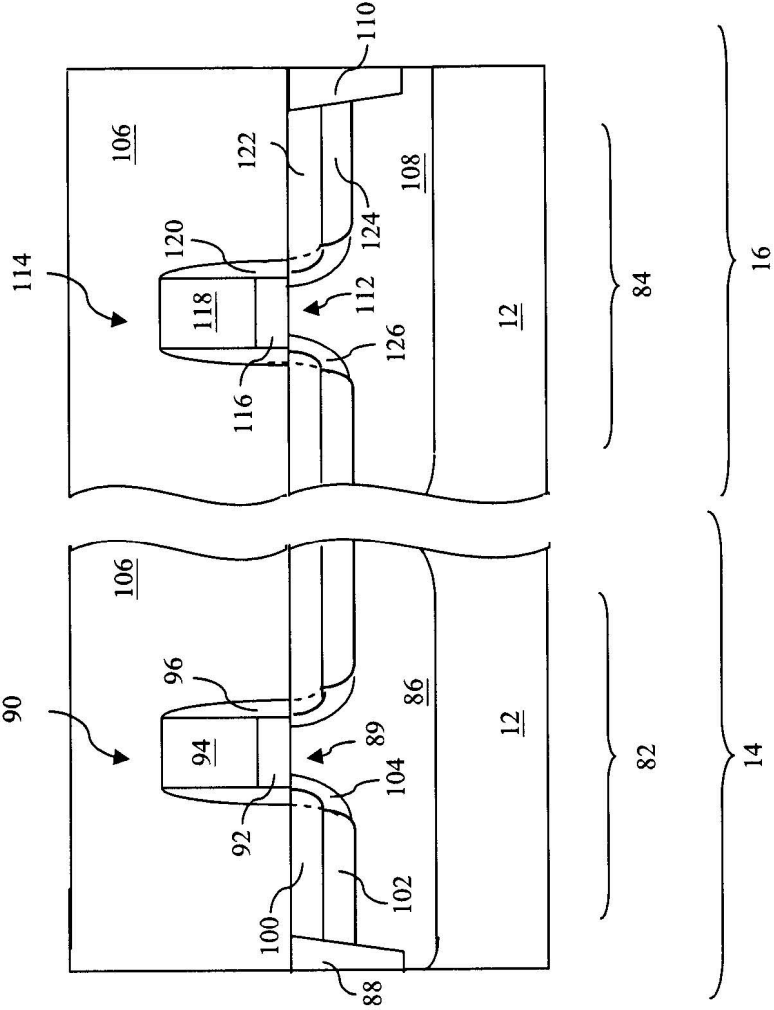


Fig. 5

130

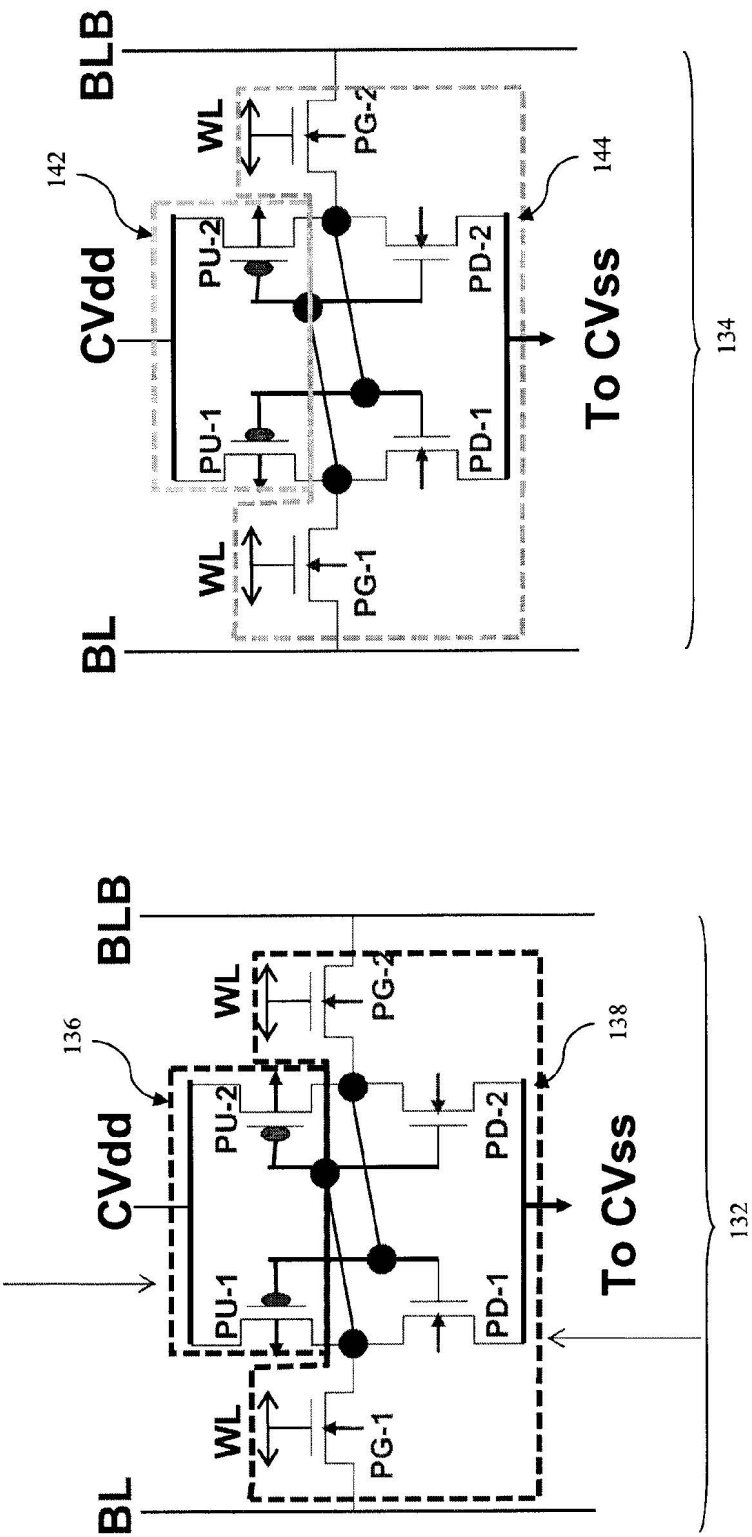


Fig. 6

150

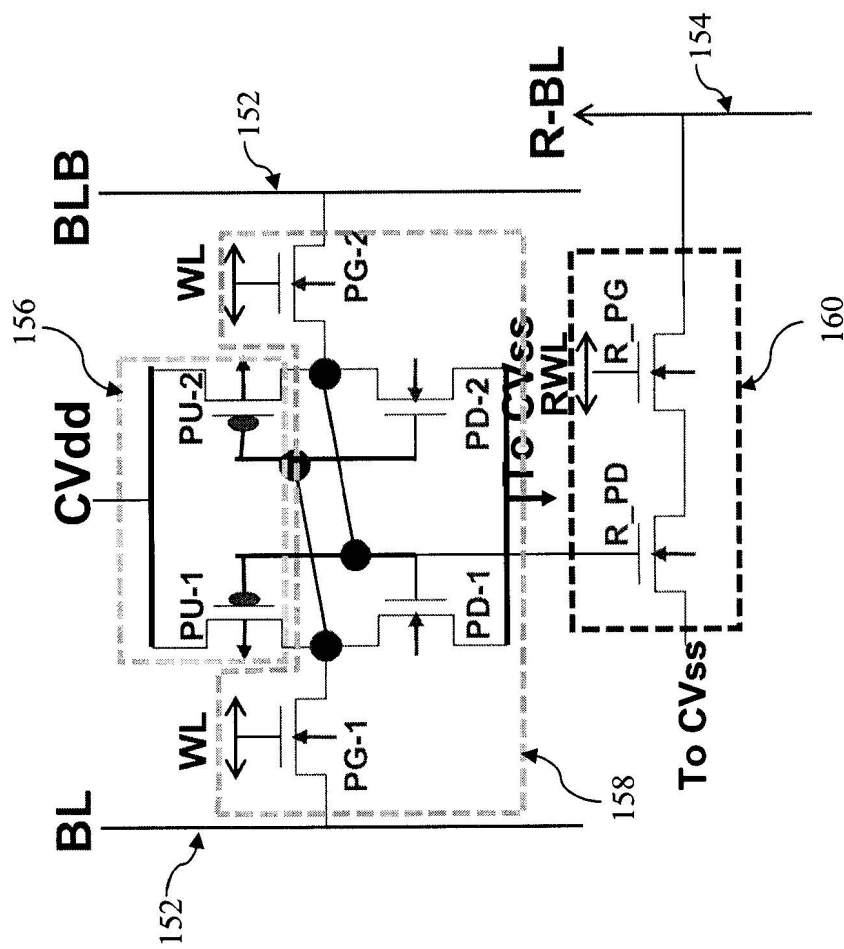


Fig. 7

200

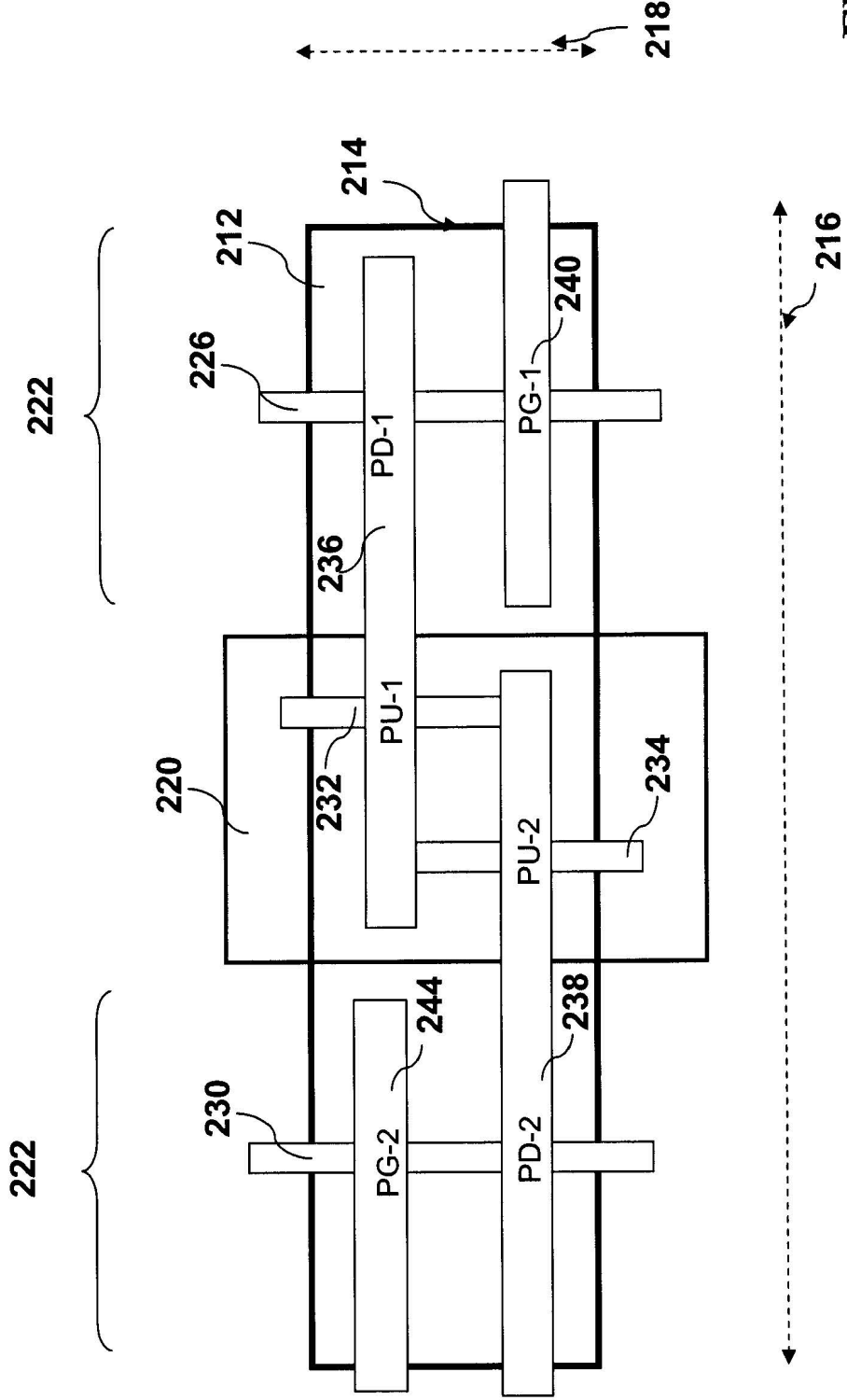


FIG. 8

200

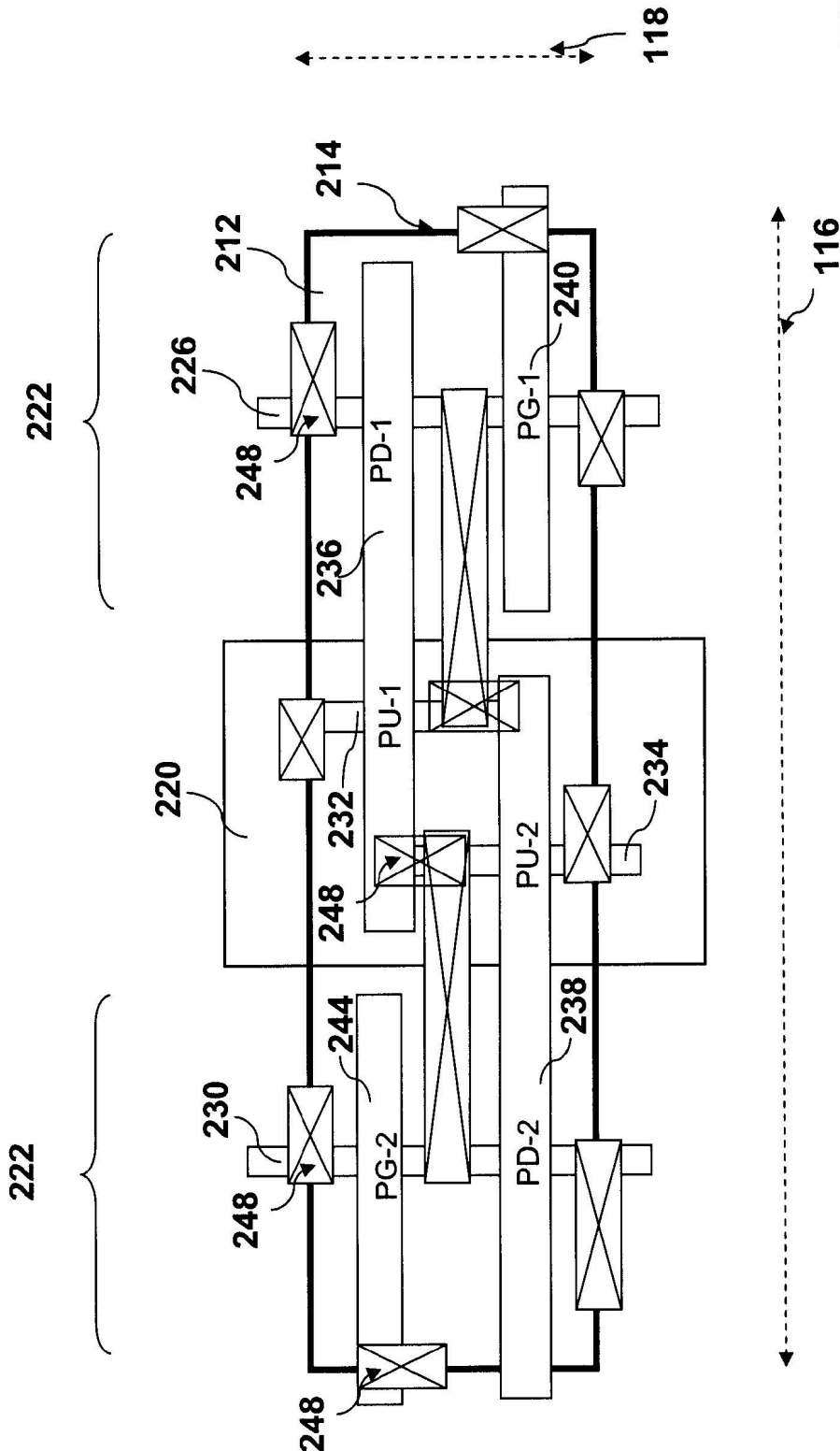


FIG. 9

250

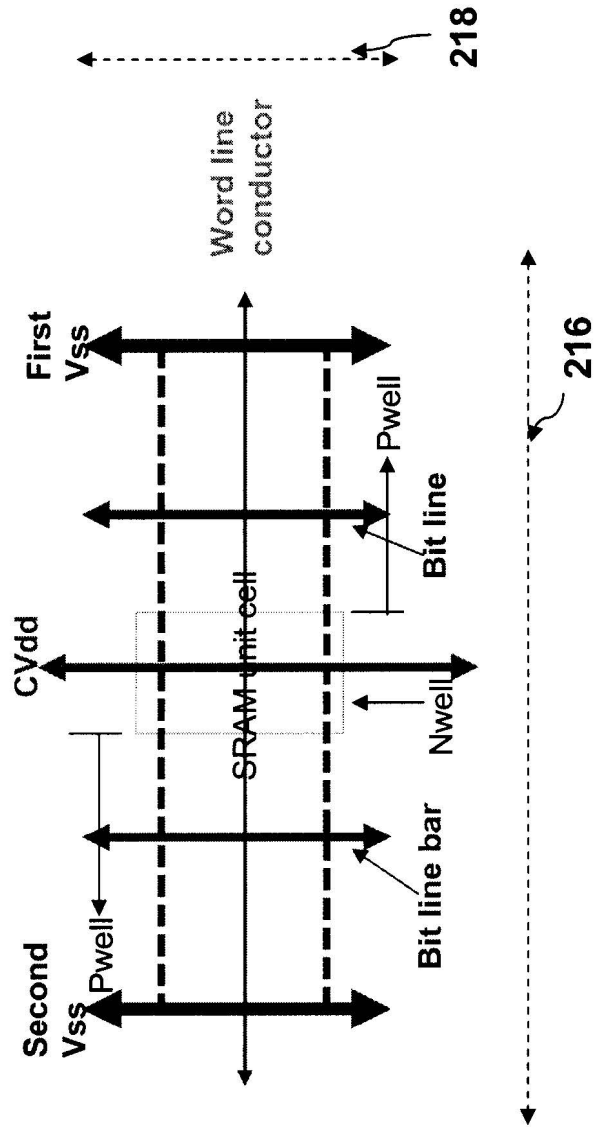


FIG. 10

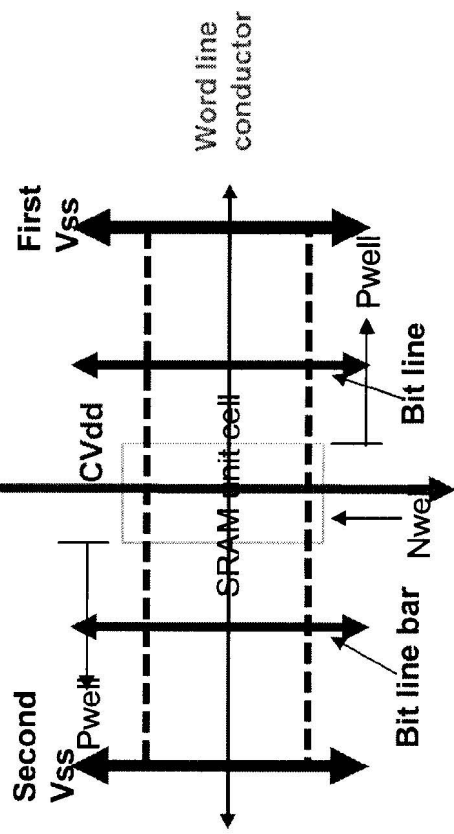
260

periphery Vdd power line (single power)

Enable
control signal

Voltage control
circuitry

To voltage control circuit



216

218

FIG. 11

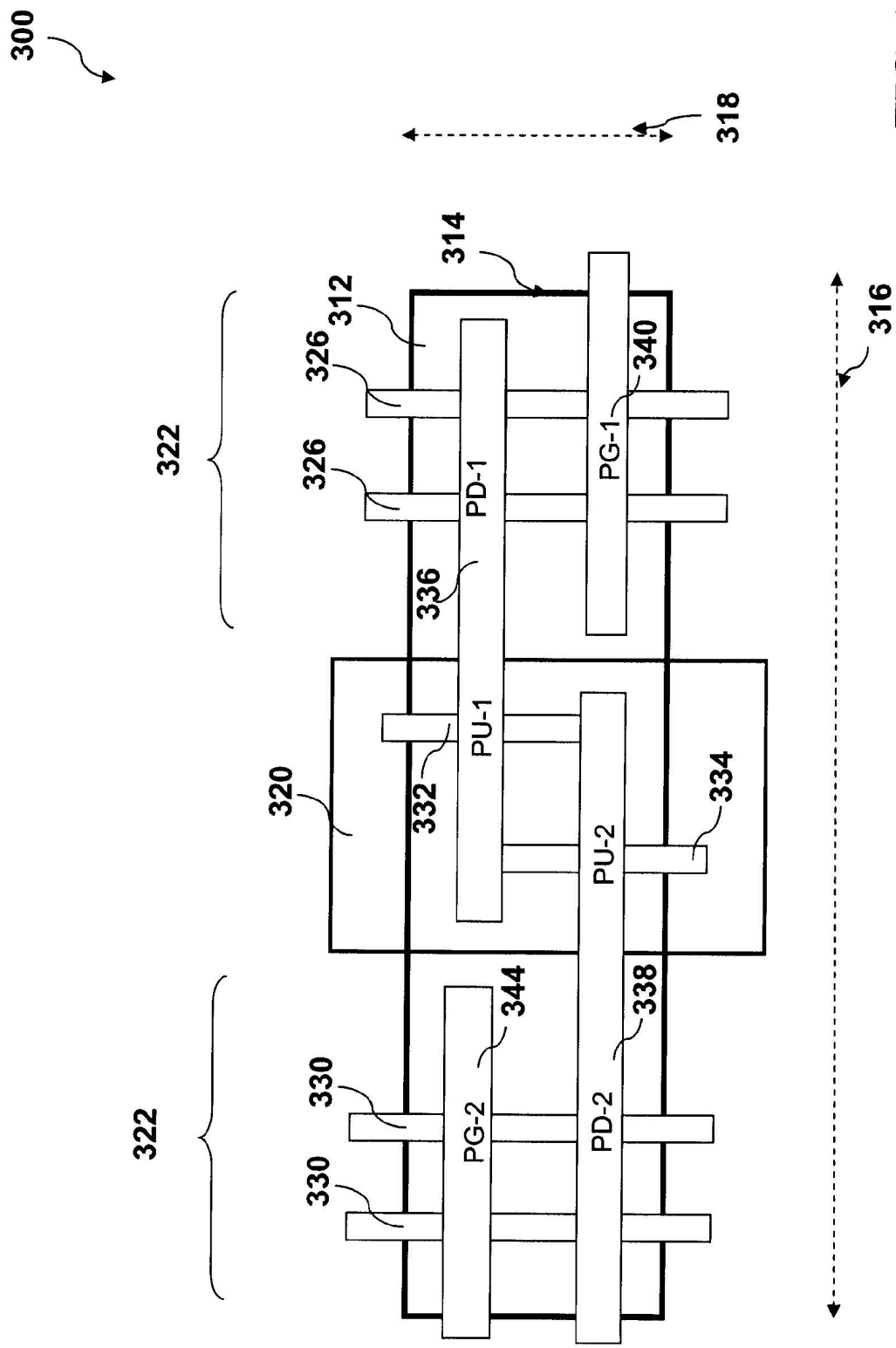


FIG. 12

300

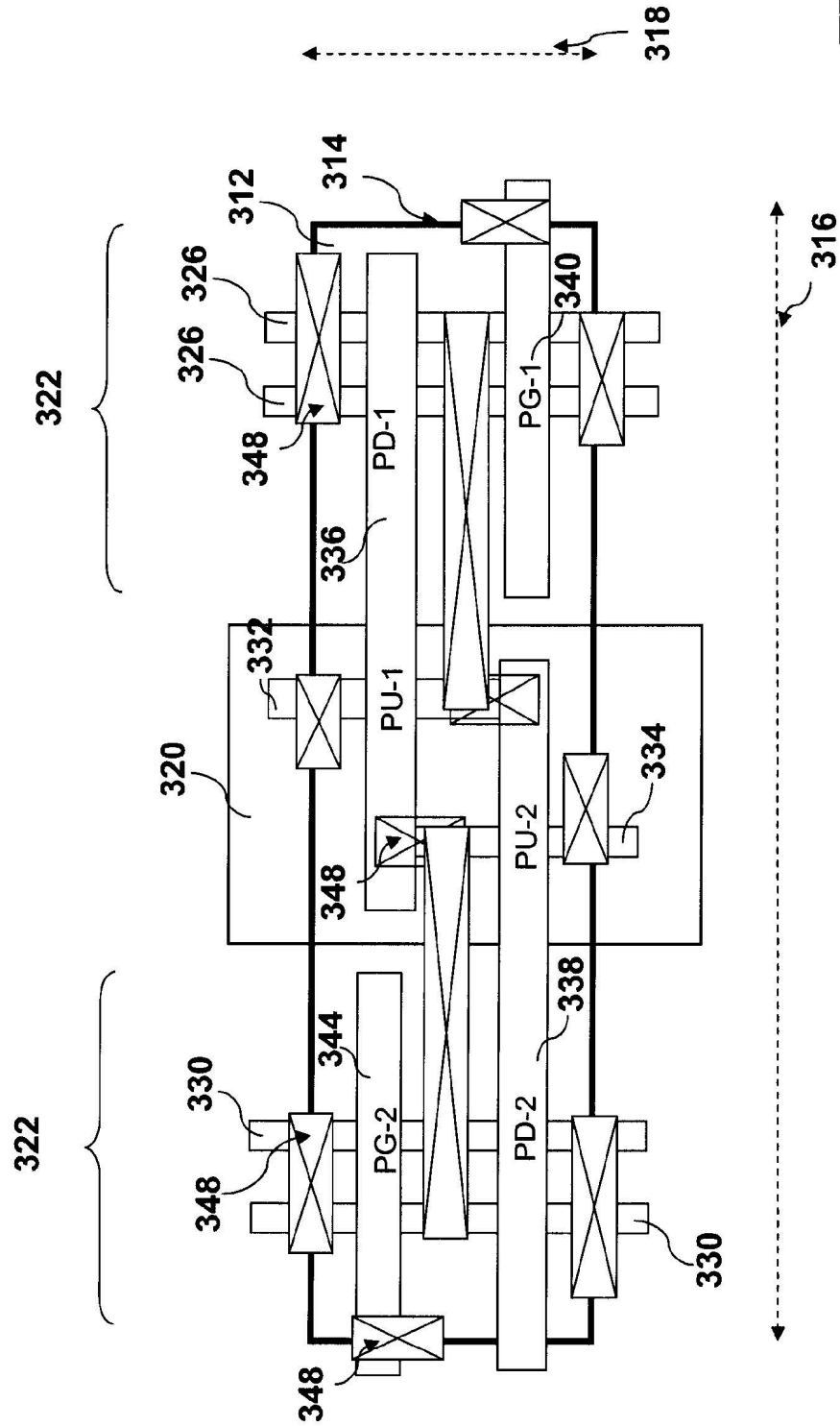


FIG. 13

350

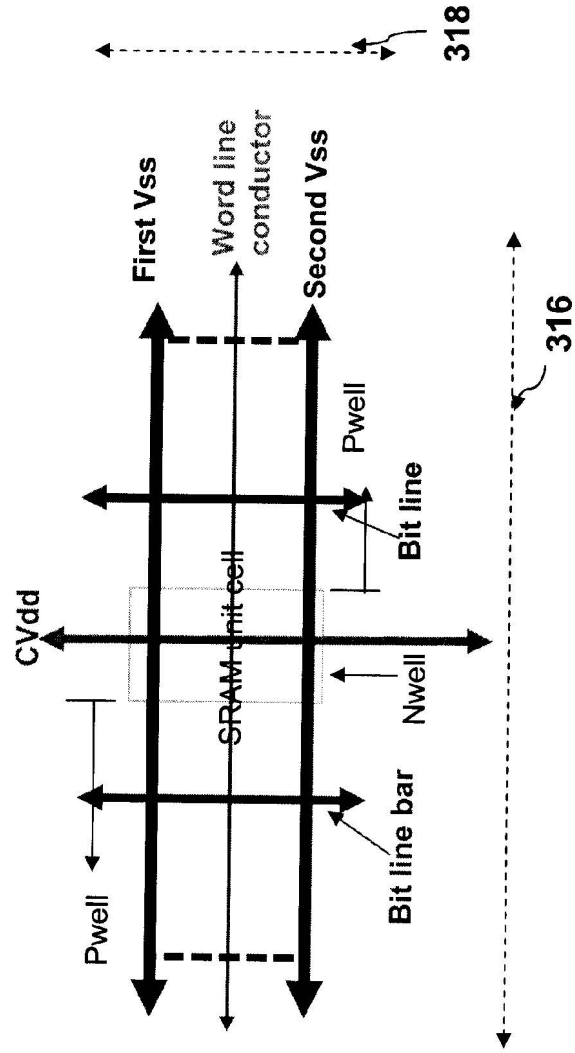


FIG. 14

360

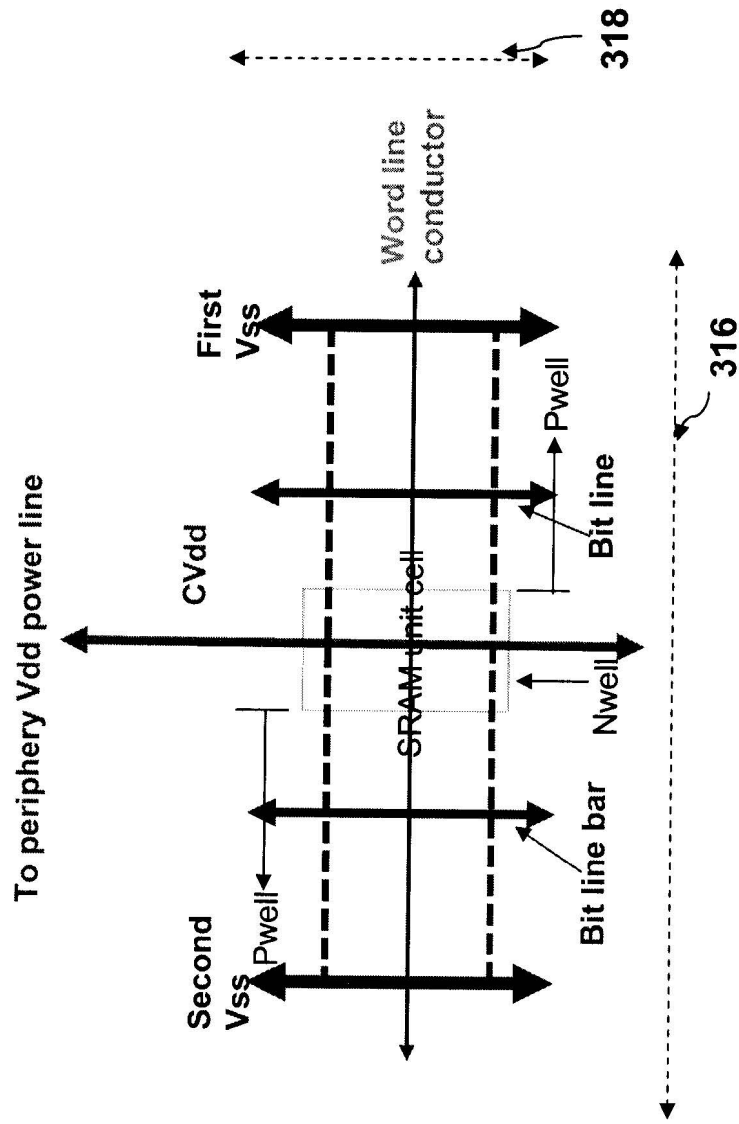


FIG. 15

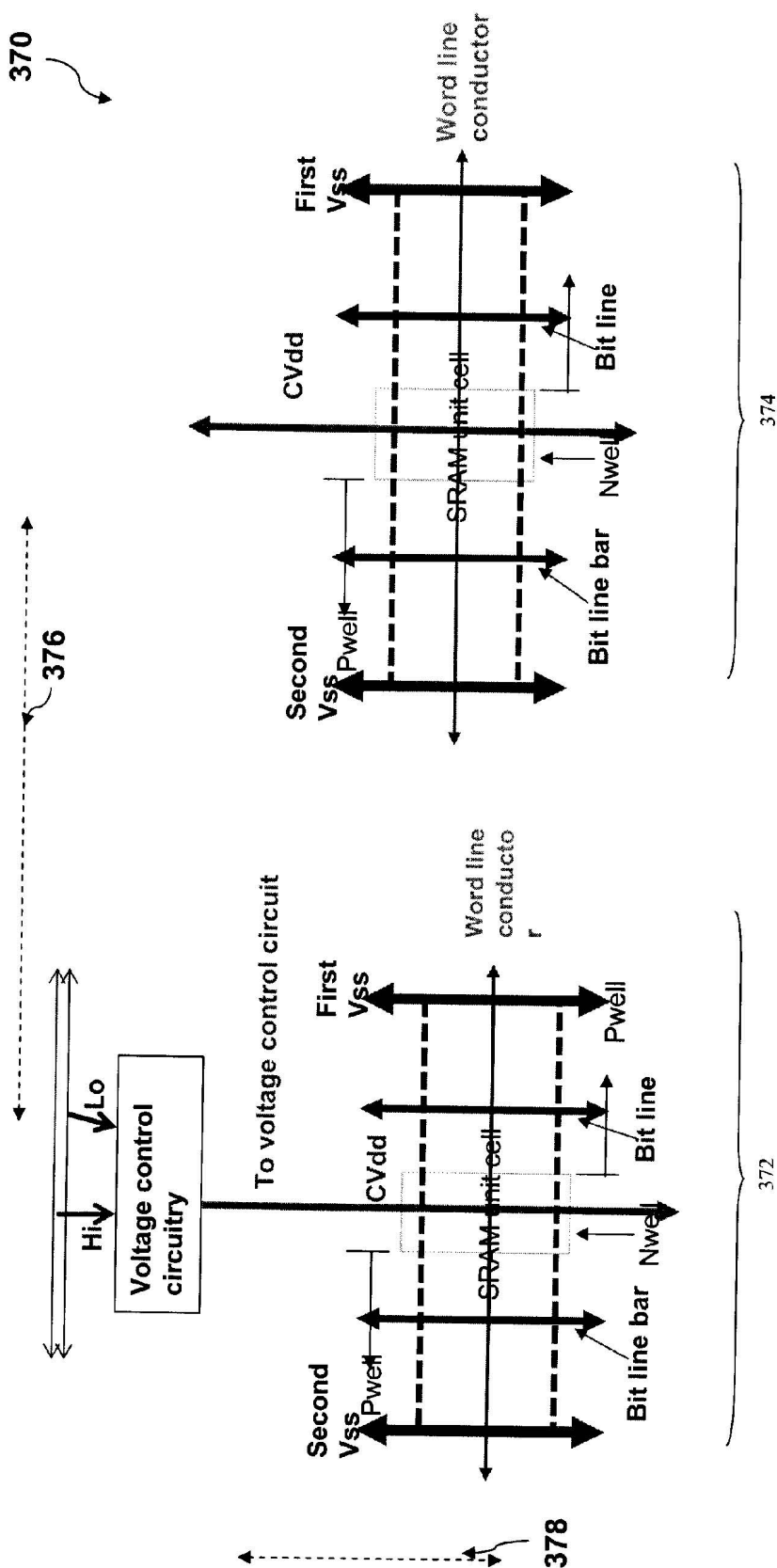


FIG. 16

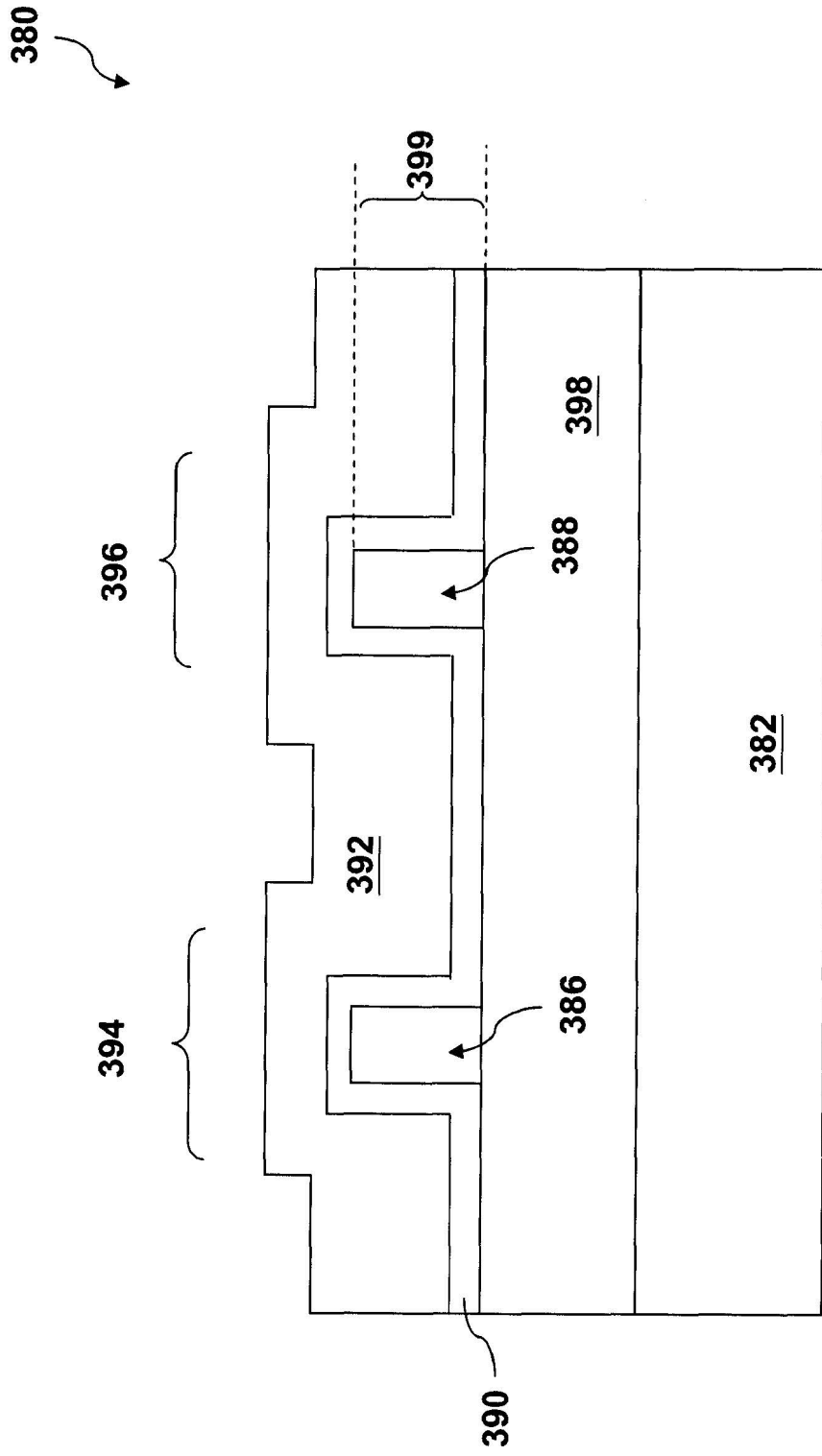


FIG. 17