# CLAIMS

Claim 1. A liquid crystal display panel comprising:

a pair of substrates;

a sealing material; and

a liquid crystal layer enclosed by the pair of substrates and the sealing material,

wherein one of the pair of substrates includes a pixel electrode provided with multiple slits, and a flat plate-shaped common electrode located on a different layer from the pixel electrode, with an insulating film between the pixel electrode and the common electrode,

a non-display area includes a gate driver, multiple gate lead-out wires extending from the gate driver toward a display area, and a common lead-out wire electrically connected to the common electrode,

the common lead-out wire intersects the multiple gate lead-out wires, and

the common electrode covers an area between the common lead-out wire intersecting the multiple gate lead-out wires and a part of the perimeter of the display area along the common lead-out wire.

Claim 2. The liquid crystal display panel according to claim 1,

wherein the sealing material is provided at a position further away from the display area than the common lead-out wire,

a part of the sealing material intersects the multiple gate lead-out wires, and

the common electrode further covers an area between a part of the sealing material intersecting the multiple gate lead-out wires and the common lead-out wire along a part of the perimeter of the display area.

Claim 3. The liquid crystal display panel according to claim 1,

wherein the sealing material is provided at a position further away from the display area than the common lead-out wire,

a part of the sealing material intersects the multiple gate lead-out wires, and

an electrode isolated from the common electrode and electrically connected to the common electrode covers an area between a part of the sealing material intersecting the multiple gate lead-out wires and the common lead-out wire along a part of the perimeter of the display area.

Claim 4. The liquid crystal display panel according to claim 1,

wherein the sealing material is provided at a position further away from the display area than the common lead-out wire,

a part of the sealing material intersects the multiple gate lead-out wires, and

a combination of the common electrode and an electrode isolated from the common electrode and electrically connected to the common electrode covers an area between a part of the sealing material intersecting the multiple gate lead-out wires and the common lead-out wire along a part of the perimeter of the display area.

Claim 6. The liquid crystal display panel according to claim 3 or 4,

wherein the electrode isolated from the common electrode and electrically connected to the common electrode is an electrode located on the same layer as the pixel electrode.

Claim 6. The liquid crystal display panel according to claim 3 or 4,

wherein the electrode isolated from the common electrode and electrically connected to the common electrode is an electrode located on the same layer as the common electrode.

Claim 7. The liquid crystal display panel according to claim 3 or 4,

wherein the electrode isolated from the common electrode and electrically connected to the common electrode is an electrode located on the same layer as the common lead-out wire.

Claim 8. The liquid crystal display panel according to any one of claims 1 to 7,

wherein the counter substrate includes a black matrix, and the sealing material at least partially overlaps the black matrix.

Claim 9. The liquid crystal display panel according to any one of claims 1 to 8,

wherein an electrode located on the same layer as the multiple gate lead-out wires and overlapping the sealing material is provided with a slit.

Claim 10. The liquid crystal display panel according to any one of claims 1 to 7,

wherein the counter substrate includes a black matrix, and the sealing material does not overlap the black matrix.

Claim 11. The liquid crystal display panel according to any one of claims 1 to 10,

wherein the gate driver is a first gate driver,

the multiple gate lead-out wires led out from the gate driver are first multiple gate lead-out wires,

an area across the display area from the first gate drivers is provided with a second gate driver, second multiple gate lead-out wires extending from the second gate driver toward the

display area, and a second common lead-out wire electrically connected to the common electrode,

the second common lead-out wire intersects the second multiple gate lead-out wires, and

the common electrode covers an area between the second common lead-out wire intersecting the second multiple gate lead-out wires and a part of the perimeter of the display area along the second common lead-out wire.

Claim 12. The liquid crystal display panel according to any one of claims 1 to 11,

wherein the one of the pair of substrates includes a thin film transistor, and

the thin film transistor includes a semiconductor layer including an oxide semiconductor.

Claim 13. The liquid crystal display panel according to claim 12,

wherein the oxide semiconductor contains indium, gallium, zinc and oxygen.

# CLAIMS

1. A liquid crystal display panel comprising: a pair of substrates; a sealing material; and a liquid crystal layer enclosed by the pair of substrates and the sealing material, wherein one of the pair of substrates includes a pixel electrode provided with multiple slits, and a flat plate-shaped common electrode located on a different layer from the pixel electrode, with an insulating film between the pixel electrode and the common electrode, a non-display area includes a gate driver, multiple gate lead-out wires extending from the gate driver toward a display area, and a common lead-out wire electrically connected to the common electrode, the common lead-out wire intersects the multiple gate lead-out wires, and the common electrode covers an area between the common lead-out wire intersecting the multiple gate lead-out wires and a part of the perimeter of the display area along the common lead-out wire.

2. The liquid crystal display panel according to claim 1, wherein the sealing material is provided at a position further away from the display area than the common lead-out wire, a part of the sealing material intersects the multiple gate lead-out wires, and the common electrode further covers an area between a part of the sealing material intersecting the multiple gate lead-out wires and the common lead-out wire along a part of the perimeter of the display area.

3. The liquid crystal display panel according to claim 1, wherein the sealing material is provided at a position further away from the display area than the common lead-out wire, a part of the sealing material intersects the multiple gate lead-out wires, and an electrode isolated from the common electrode and

electrically connected to the common electrode covers an area between a part of the sealing material intersecting the multiple gate lead-out wires and the common lead-out wire along a part of the perimeter of the display area.

4. The liquid crystal display panel according to claim 3, wherein the electrode isolated from the common electrode and electrically connected to the common electrode is an electrode located on the same layer as the pixel electrode.

5. The liquid crystal display panel according to claim 3, wherein the electrode isolated from the common electrode and electrically connected to the common electrode is an electrode located on the same layer as the common electrode.

6. The liquid crystal display panel according to claim 3, wherein the electrode isolated from the common electrode and electrically connected to the common electrode is an electrode located on the same layer as the common lead-out wire.

7. The liquid crystal display panel according to claim 1, wherein the sealing material is provided at a position further away from the display area than the common lead-out wire, a part of the sealing material intersects the multiple gate lead-out wires, and a combination of the common electrode and an electrode isolated from the common electrode and electrically connected to the common electrode covers an area between a part of the sealing material intersecting the multiple gate lead-out wires and the common lead-out wire along a part of the perimeter of the display area.

8. The liquid crystal display panel according to claim 7, wherein the electrode isolated from the common electrode and electrically connected to the common electrode is an electrode located on the same layer as the pixel electrode.

9. The liquid crystal display panel according to claim 7, wherein the electrode isolated from the common electrode and electrically connected to the common electrode is an electrode located on the same layer as the common electrode.

10. The liquid crystal display panel according to claim 7, wherein the electrode isolated from the common electrode and electrically connected to the common electrode is an electrode located on the same layer as the common lead-out wire.

11. The liquid crystal display panel according to claim 1, wherein the counter substrate includes a black matrix, and the sealing material at least partially overlaps the black matrix.

12. The liquid crystal display panel according to claim 1, wherein an electrode located on the same layer as the multiple gate lead-out wires and overlapping the sealing material is provided with a slit.

13. The liquid crystal display panel according claim 1, wherein the counter substrate includes a black matrix, and the sealing material does not overlap the black matrix.

14. The liquid crystal display panel according to claim 1, wherein the gate driver is a first gate driver, the multiple gate lead-out wires led out from the gate driver are first multiple gate lead-out wires, an area across the display area

from the first gate drivers is provided with a second gate driver, second multiple gate lead-out wires extending from the second gate driver toward the display area, and a second common lead-out wire electrically connected to the common electrode, the second common lead-out wire intersects the second multiple gate lead-out wires, and the common electrode covers an area between the second common lead-out wire intersecting the second multiple gate lead-out wires and a part of the perimeter of the display area along the second common lead-out wire.

15. The liquid crystal display panel according to claim 1, wherein the one of the pair of substrates includes a thin film transistor, and the thin film transistor includes a semiconductor layer including an oxide semiconductor.

16. The liquid crystal display panel according to claim 15, wherein the oxide semiconductor contains indium, gallium, zinc and oxygen.