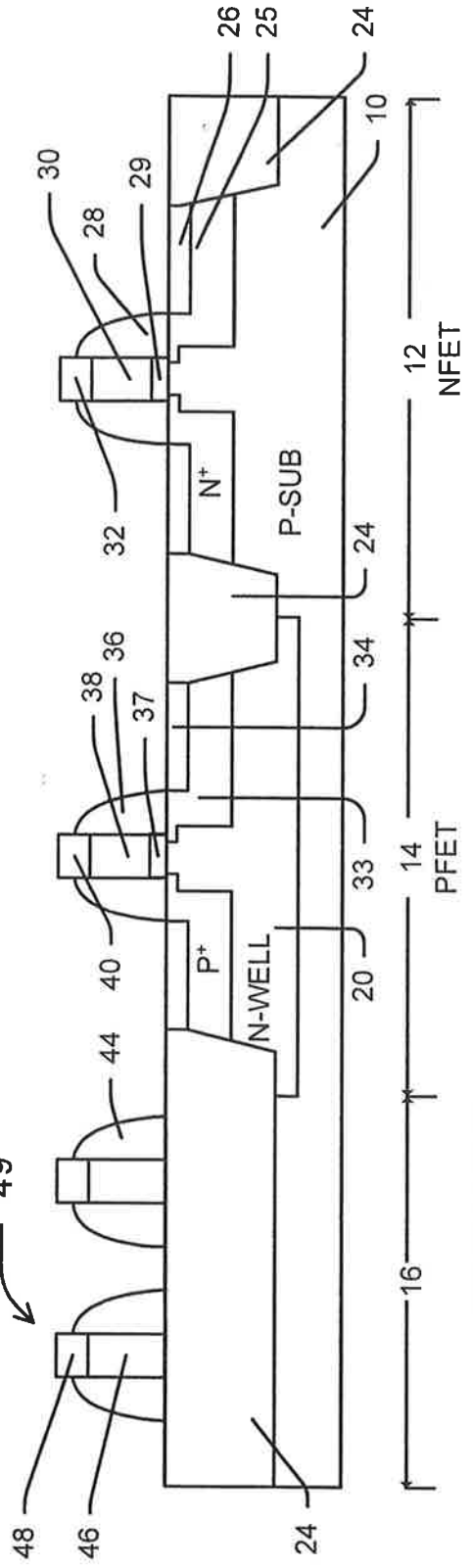


FIGURE 1

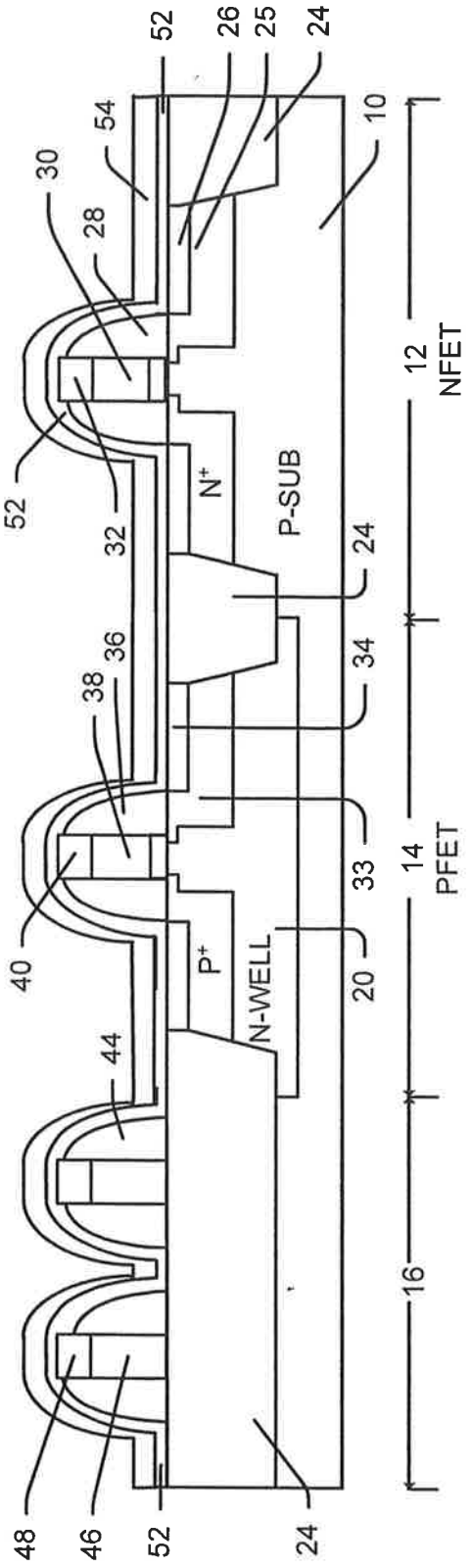


FIGURE 2

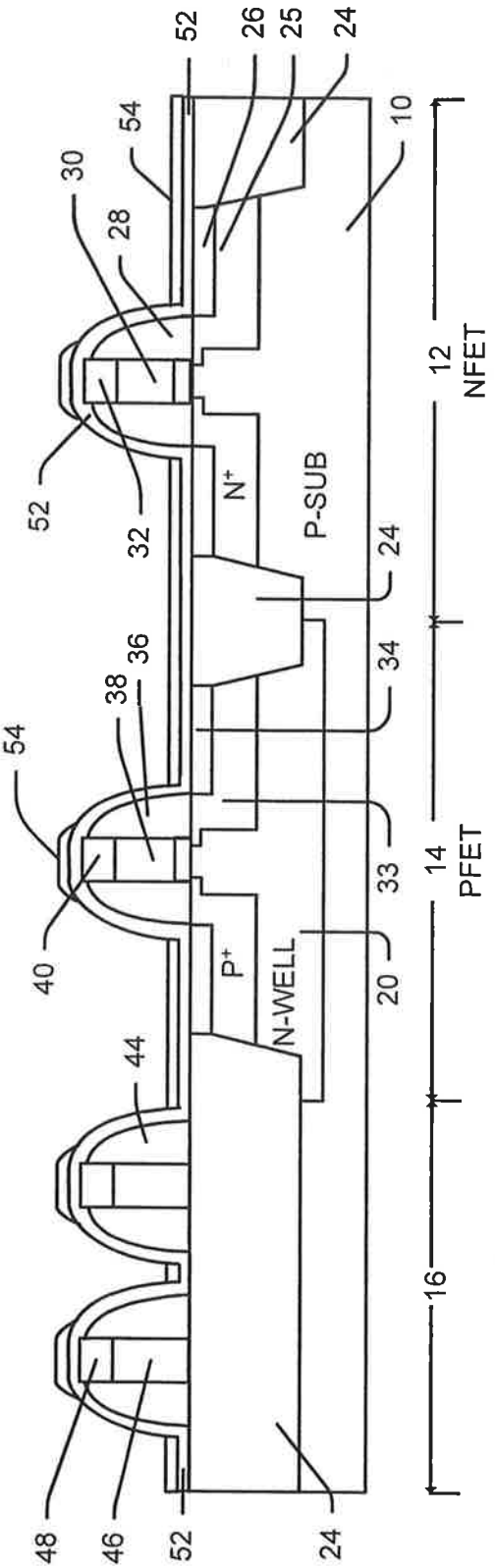


FIGURE 3

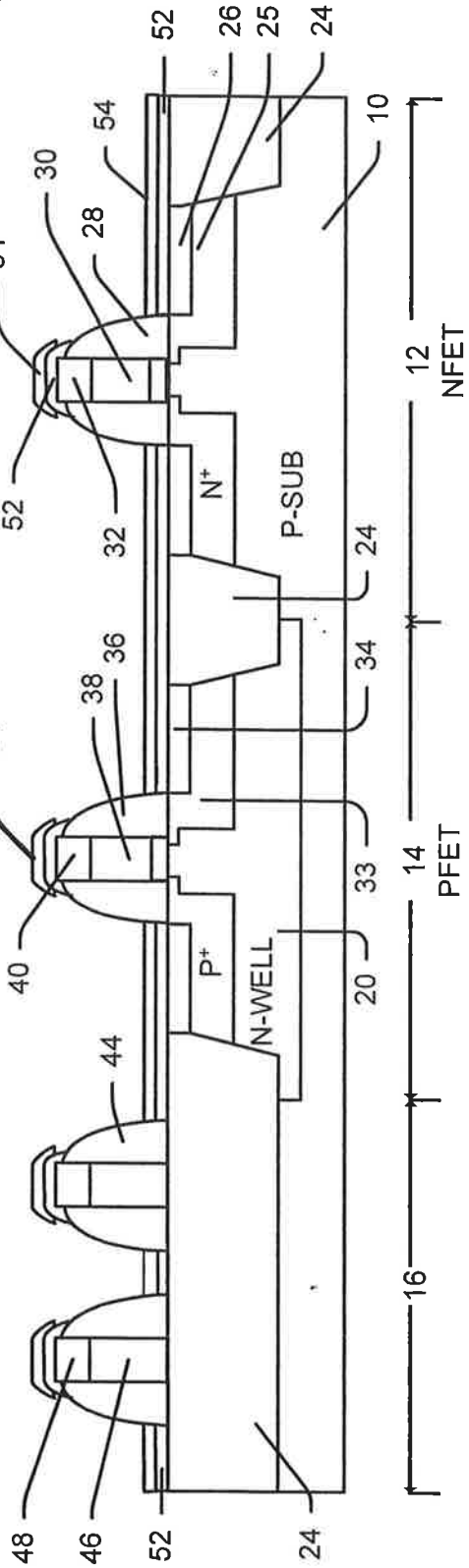


FIGURE 4

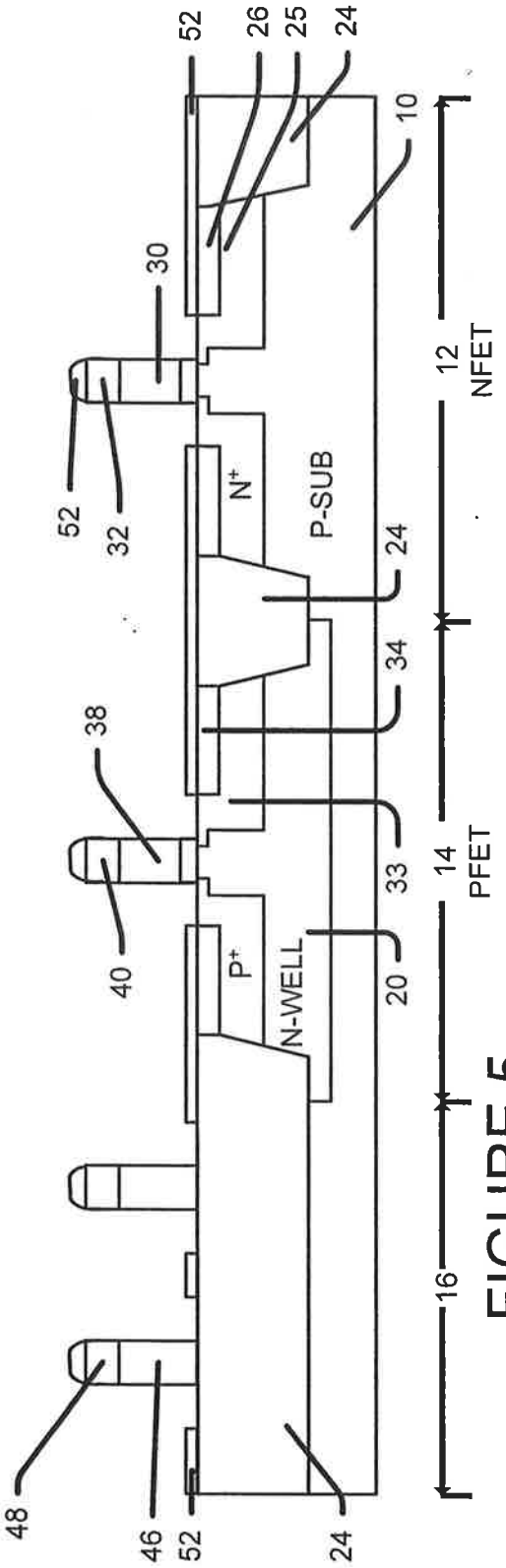

52
26
25
24
10
30
52
32
N+
P-SUB
24
34
38
33
40
P+
N-WELL
20
48
46
52
24
12
NFET
14
PFET
16

FIGURE 5

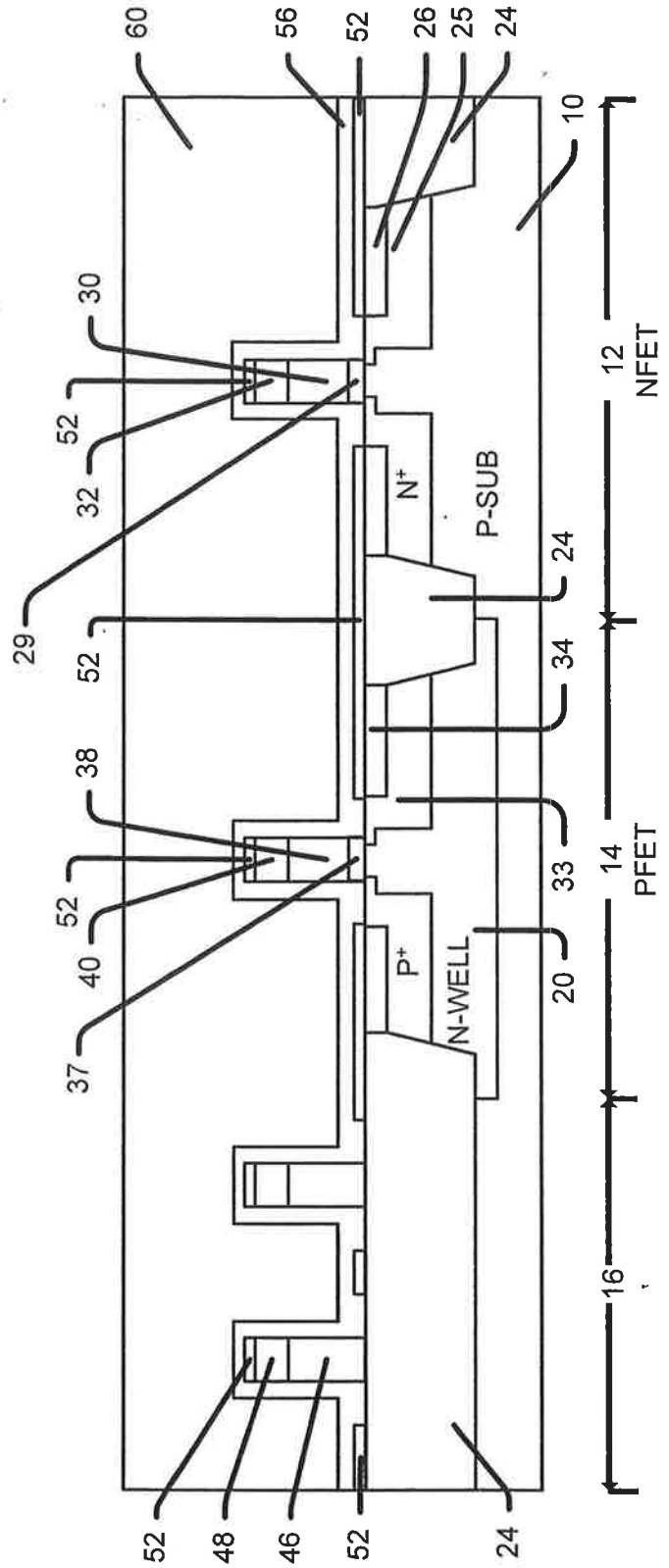

60
56
52
26
25
24
10
30
52
32
29
52
38
52
40
37
52
48
46
52
24
N+
P-SUB
24
34
33
P+
N-WELL
20
12
NFET
14
PFET
16

FIGURE 6A

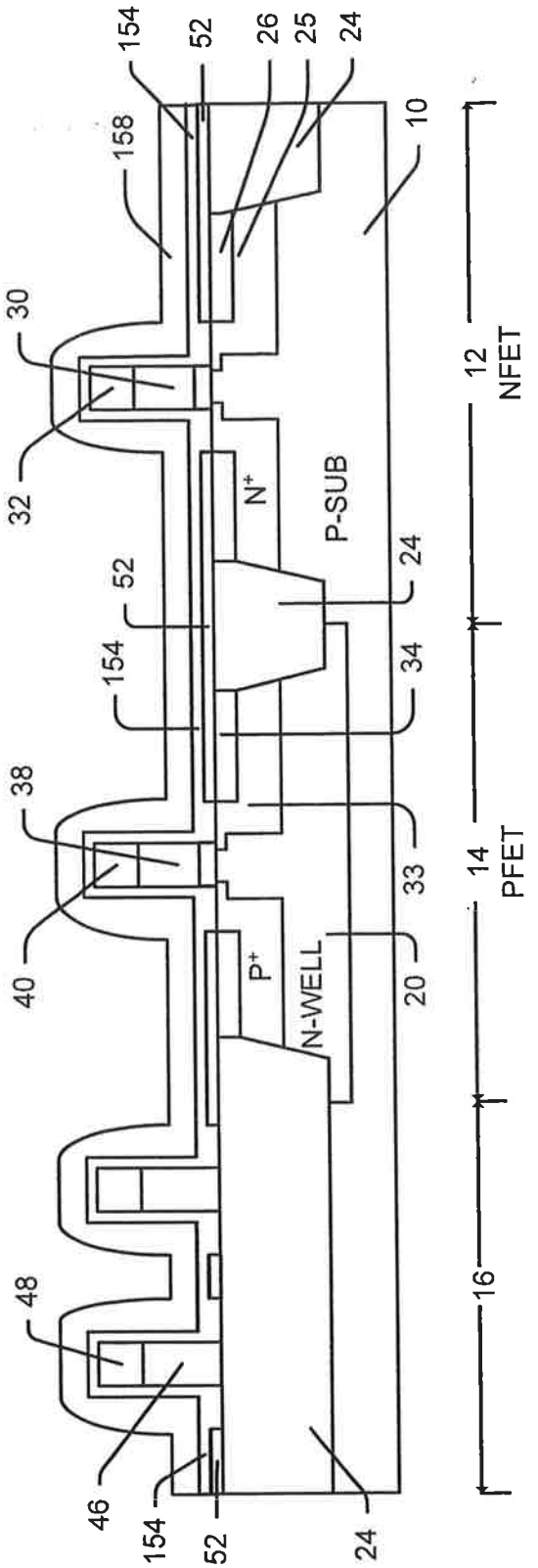

154
52
26
25
24
158
10
30
32
N+
P-SUB
52
24
154
34
38
33
40
P+
N-WELL
20
48
46
154
52
24
12
NFET
14
PFET
16

FIGURE 6B

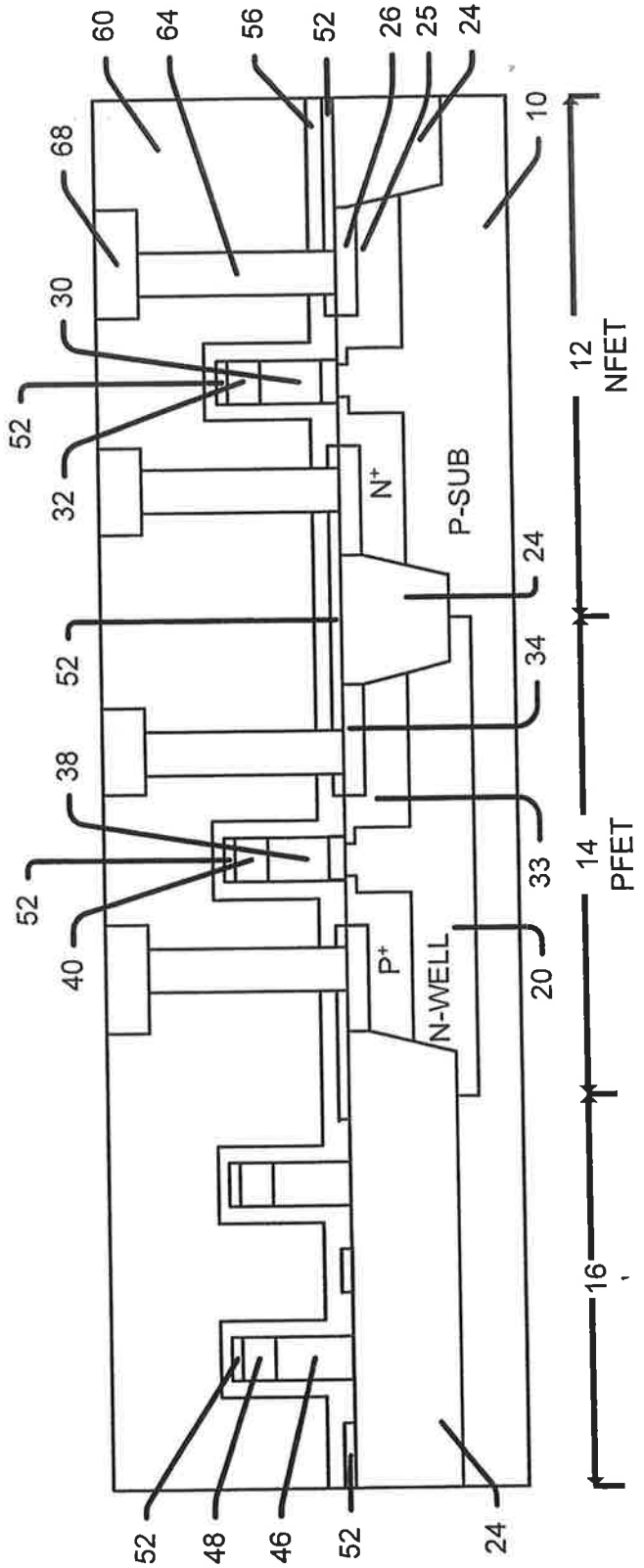

60
64
56
52
26
25
24
10
68
30
52
32
52
38
52
40
52
48
46
52
24
N+
P-SUB
24
34
33
P+
N-WELL
20
12
NFET
14
PFET
16

FIGURE 7A

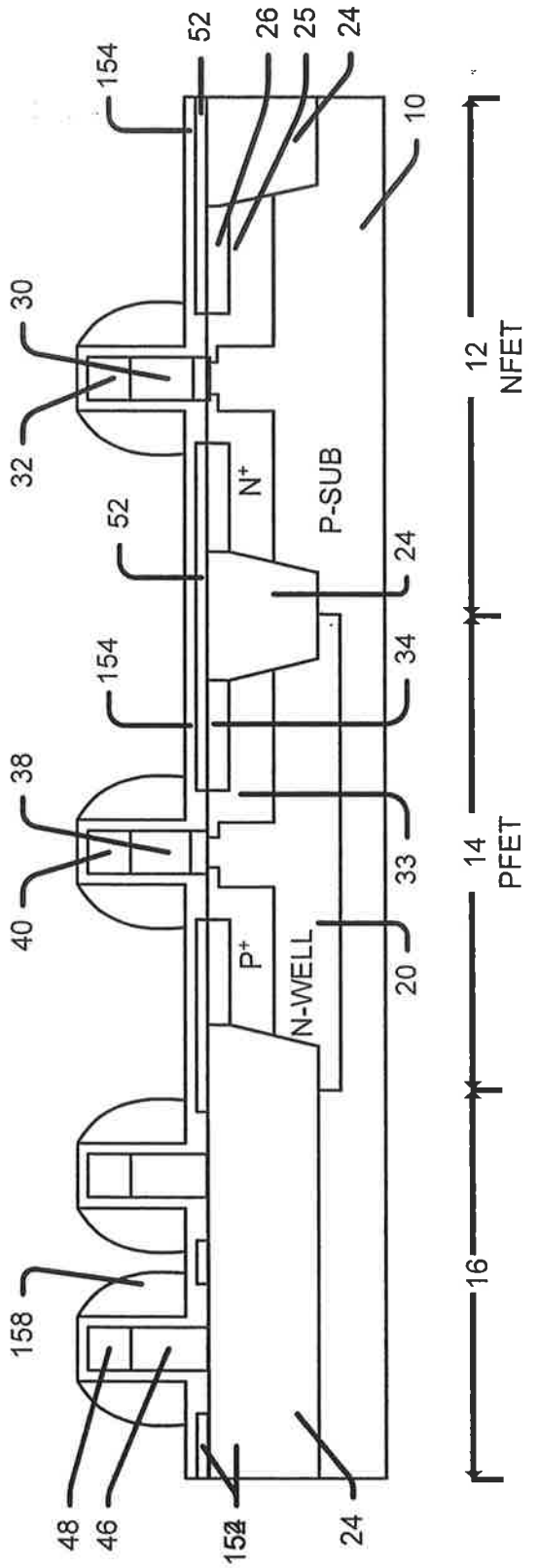

52
26
25
24
154
10
30
32
52
N+
P-SUB
24
154
34
38
40
33
P+
N-WELL
20
158
48
46
152
24
12
NFET
14
PFET
16

FIGURE 7B

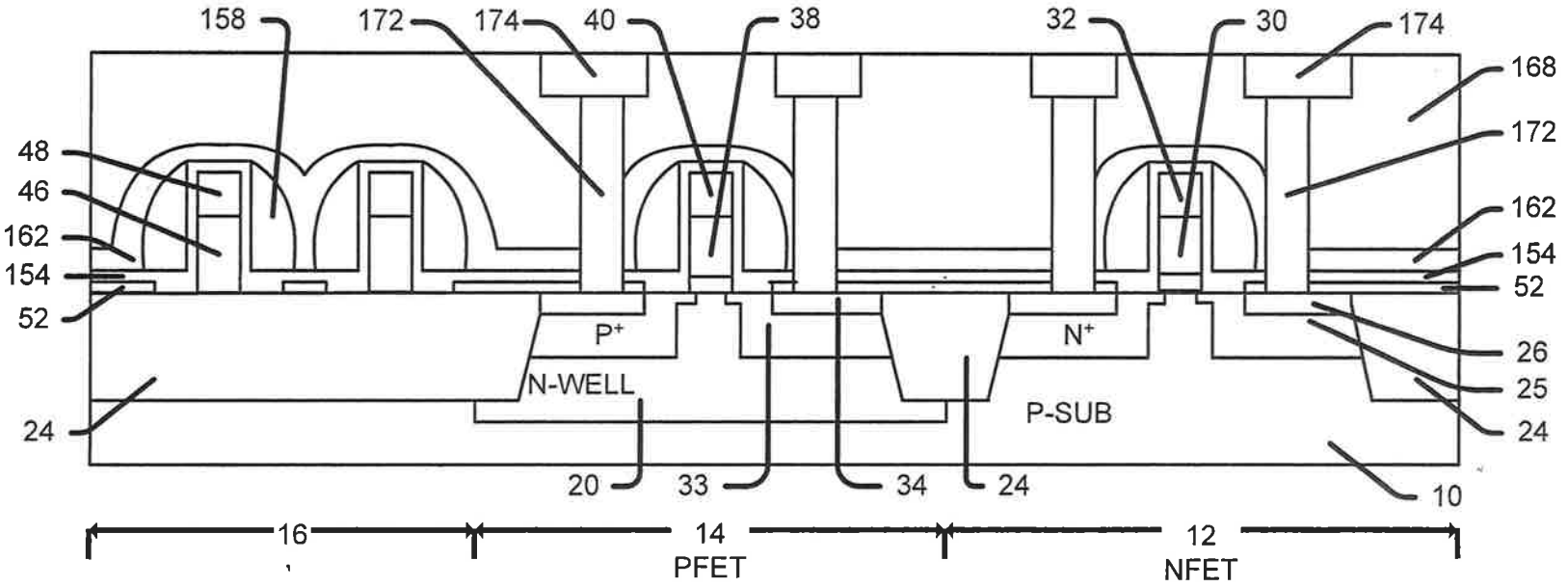

158
172
174
40
38
32
30
174
168
172
162
154
52
48
46
162
154
52
P+
N+
N-WELL
P-SUB
24
26
25
24
20
33
34
24
10
16
14
PFET
12
NFET

FIGURE 8