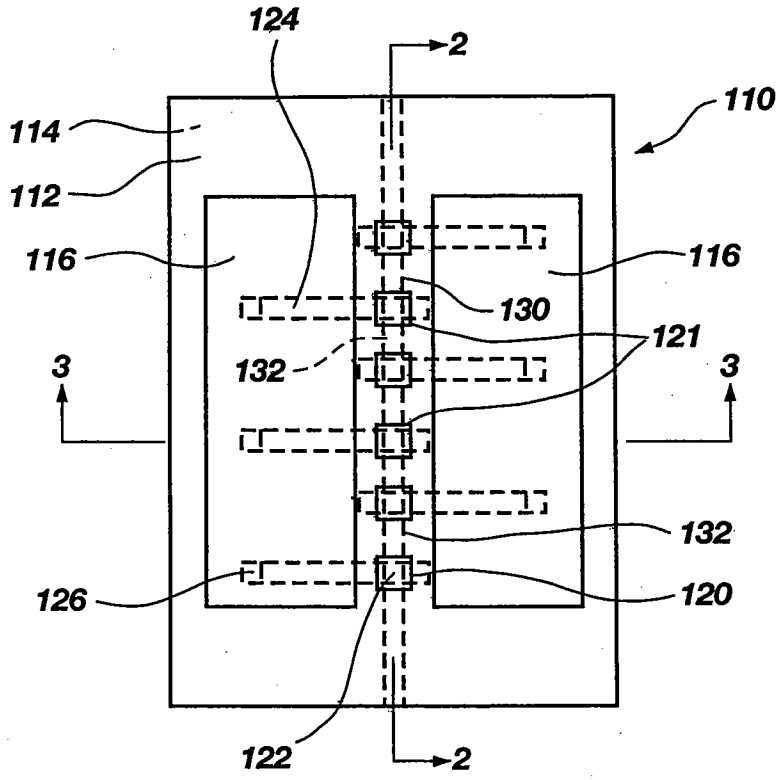


Fig. 1

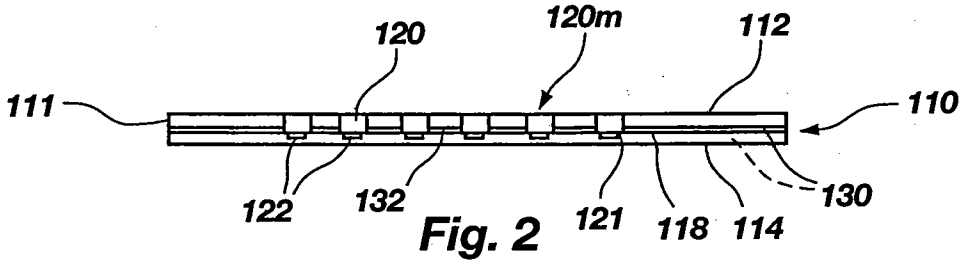


Fig. 2

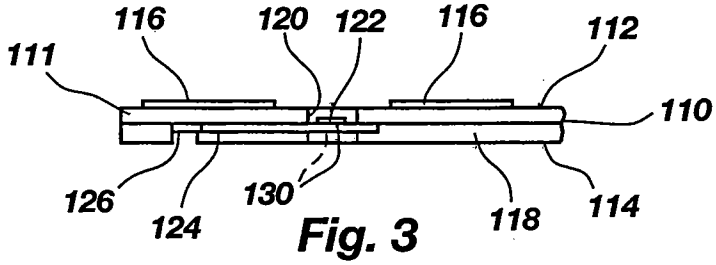


Fig. 3

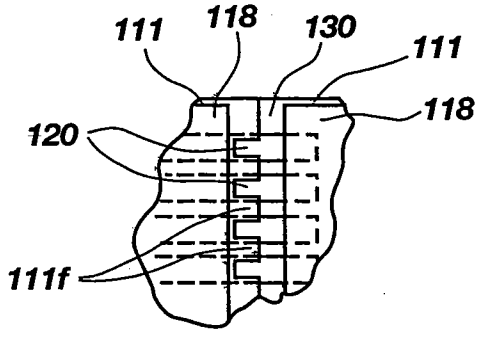


Fig. 1A

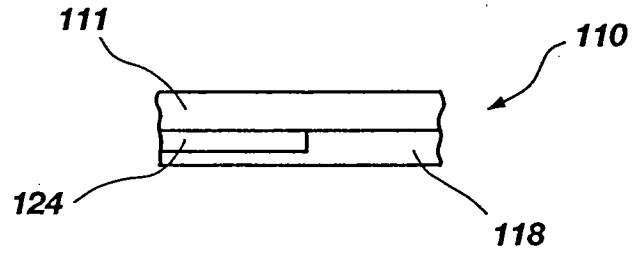


Fig. 4A

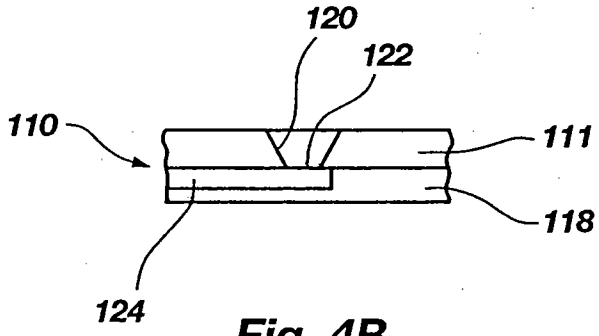


Fig. 4B

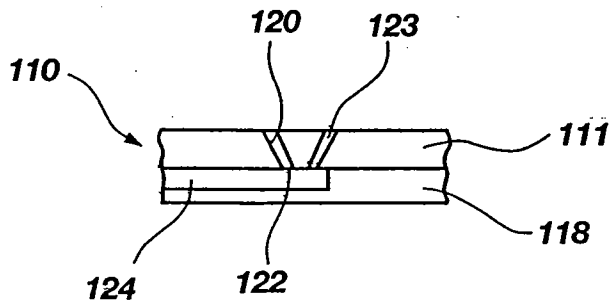


Fig. 4C

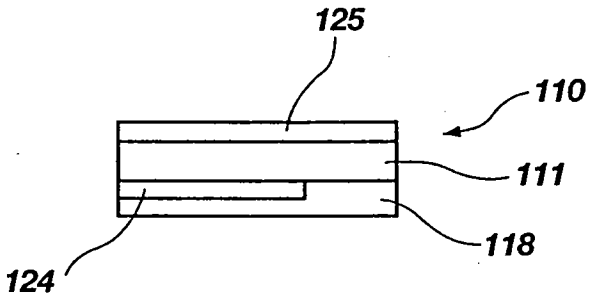


Fig. 5A

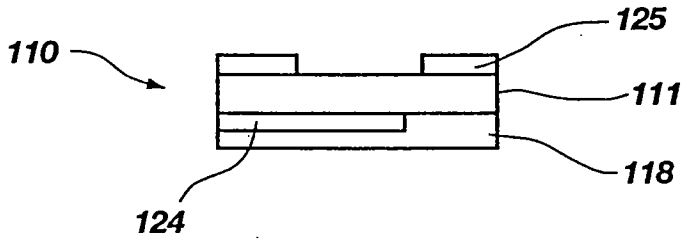


Fig. 5B

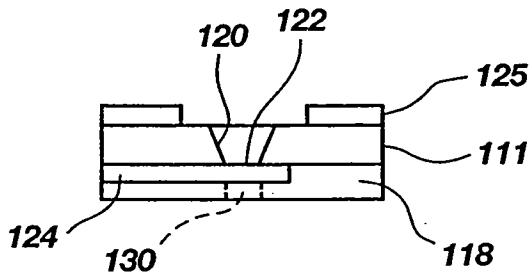


Fig. 5C

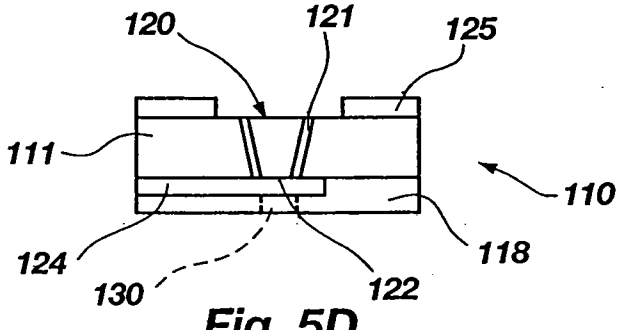


Fig. 5D

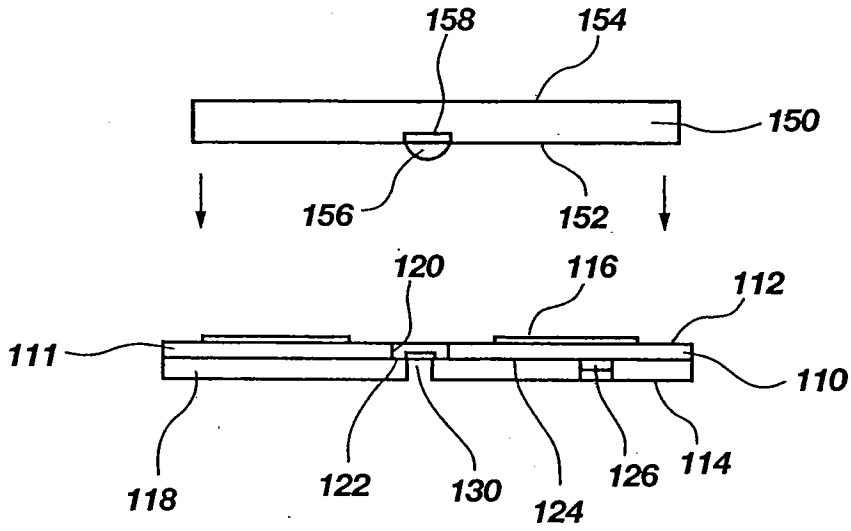

158
154
150
156
152
120
116
112
111
110
118
122
130
124
126
114

Fig. 6A

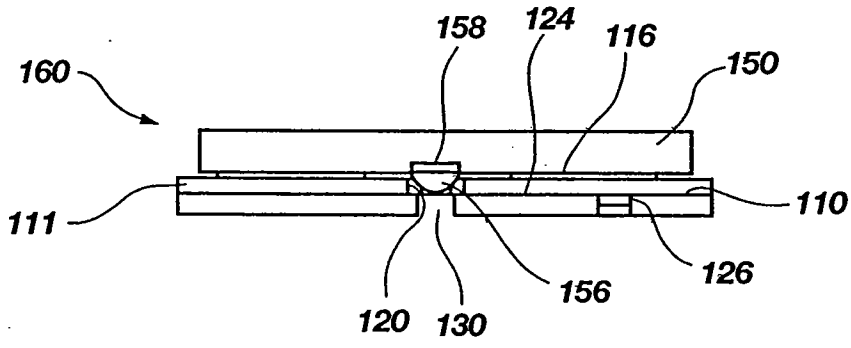

158
124
116
150
160
111
110
156
126
120
130

Fig. 6B

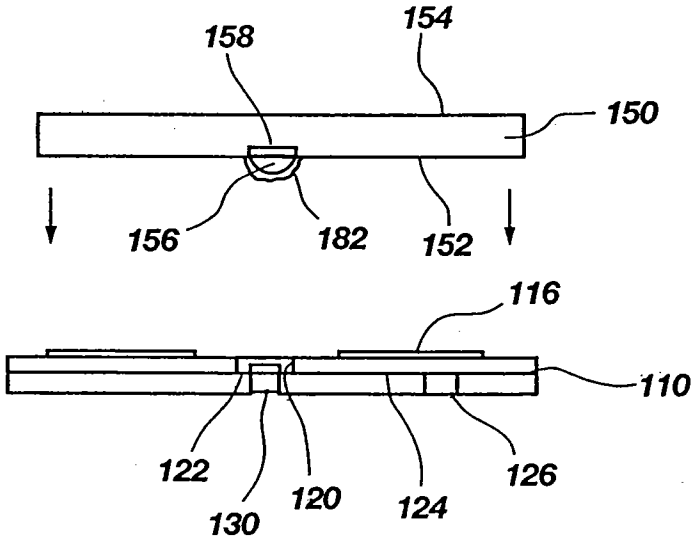

158
154
150
156
182
152
116
110
122
130
120
124
126

Fig. 7A

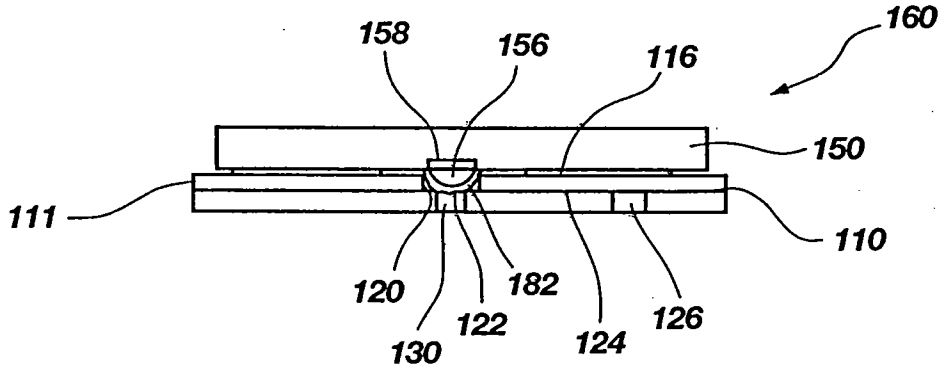

158
156
116
160
150
111
110
120
182
122
130
124
126

Fig. 7B

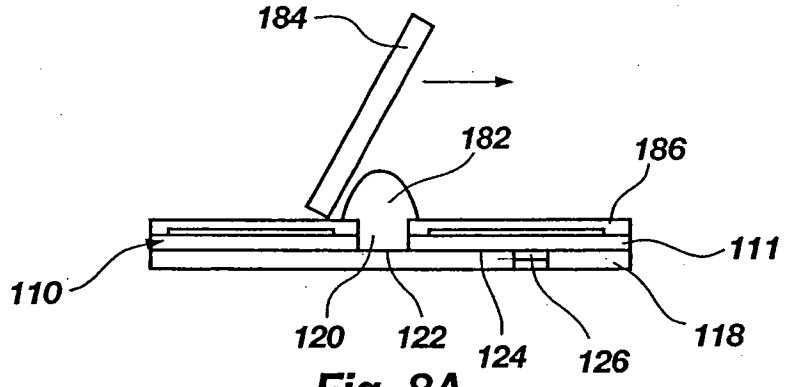


Fig. 8A

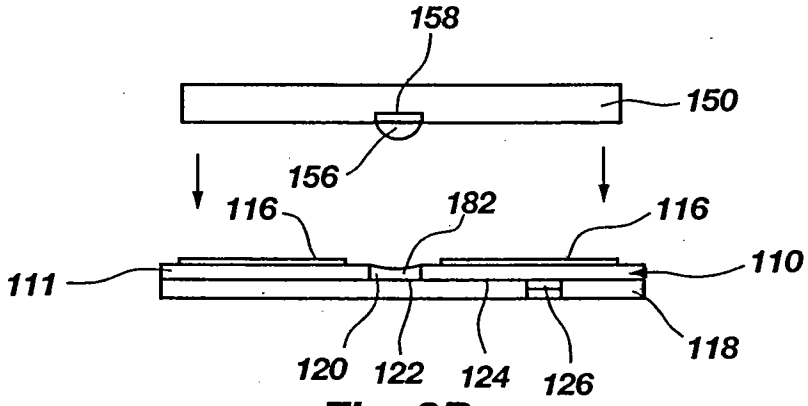


Fig. 8B

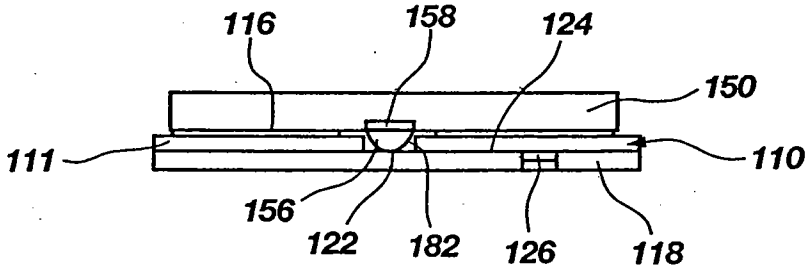


Fig. 8C

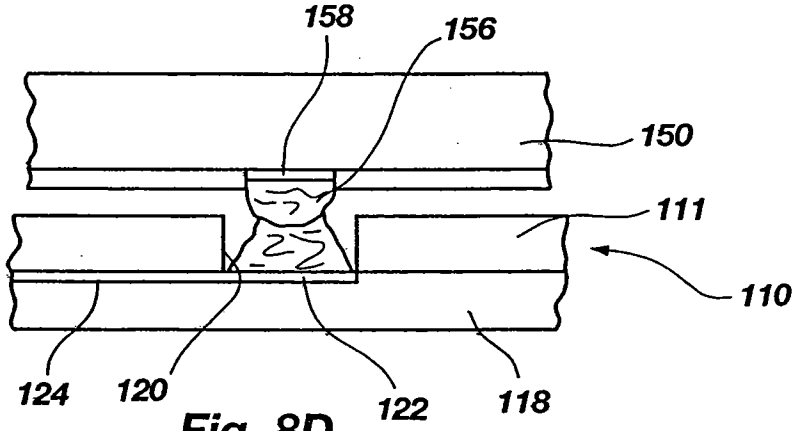


Fig. 8D

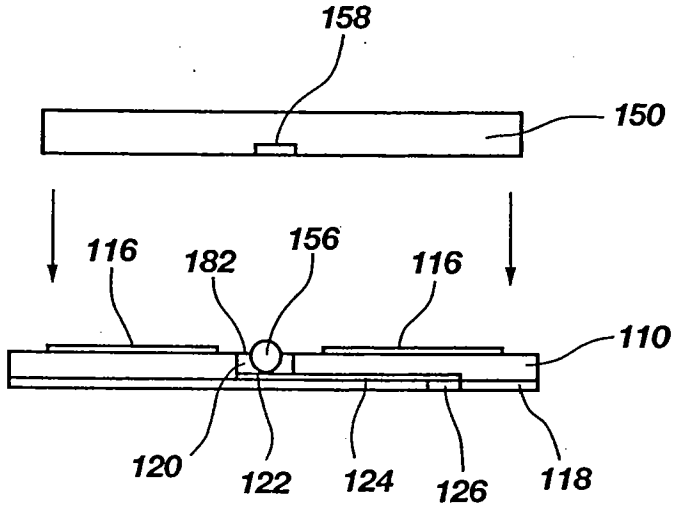

158
150
116
182
156
116
110
120
122
124
126
118

Fig. 9A

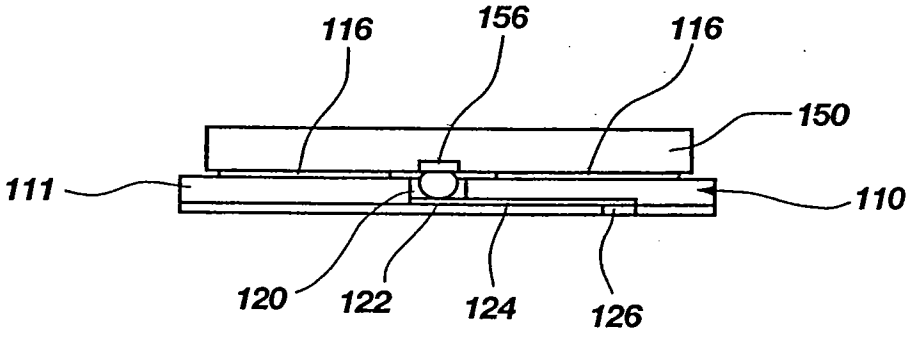

116
156
116
150
111
110
120
122
124
126

Fig. 9B

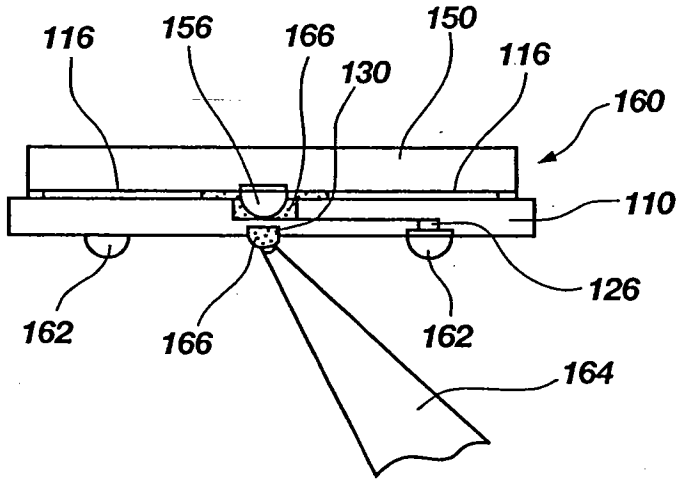

116
156
166
150
116
130
160
110
126
162
166
162
164

Fig. 10

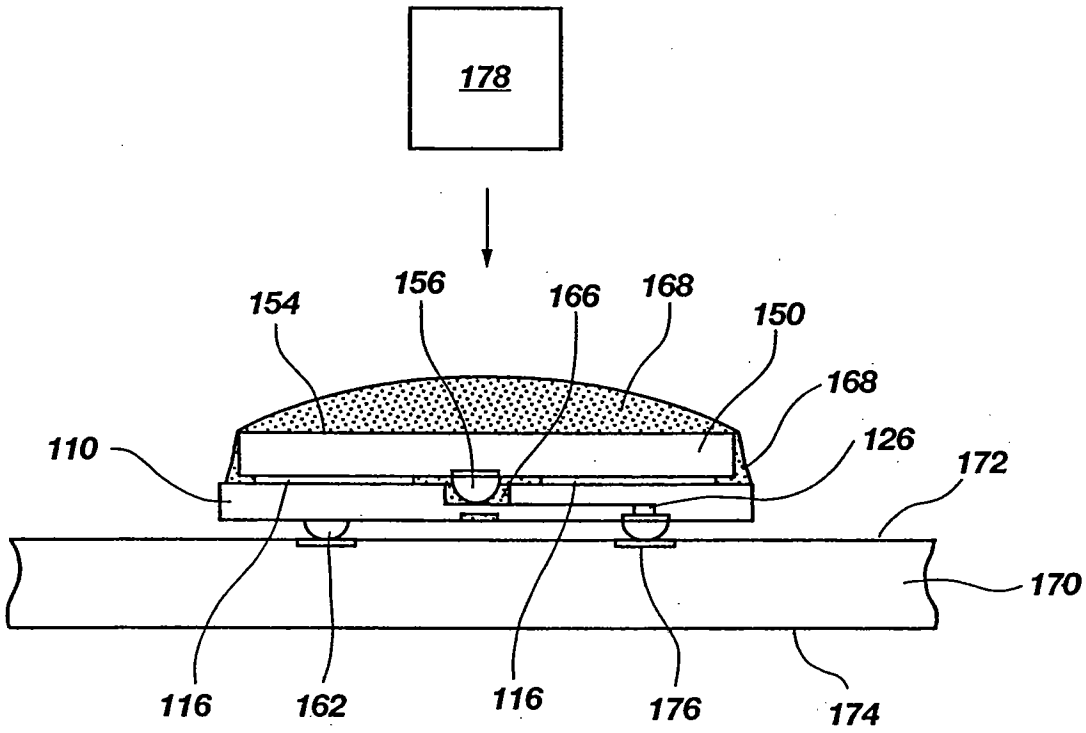

178
154
156
166
168
150
168
110
126
172
170
116
162
116
176
174

Fig. 11

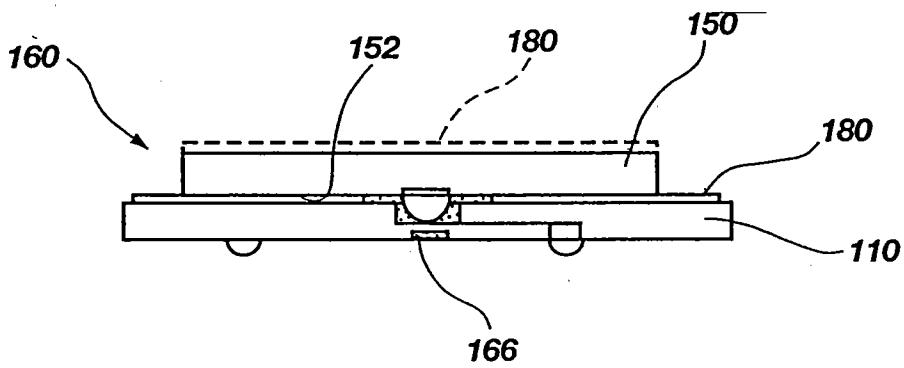


Fig. 12

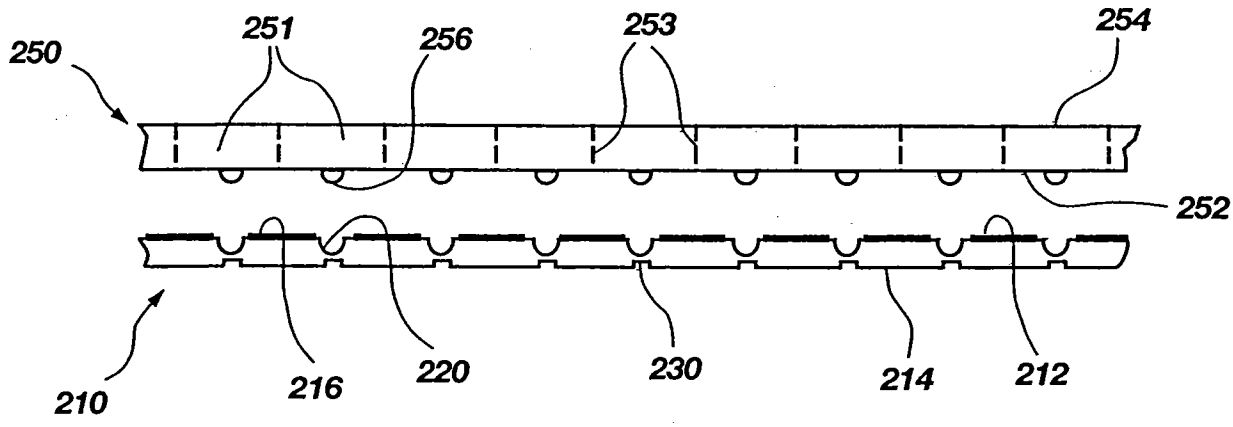


Fig. 13A

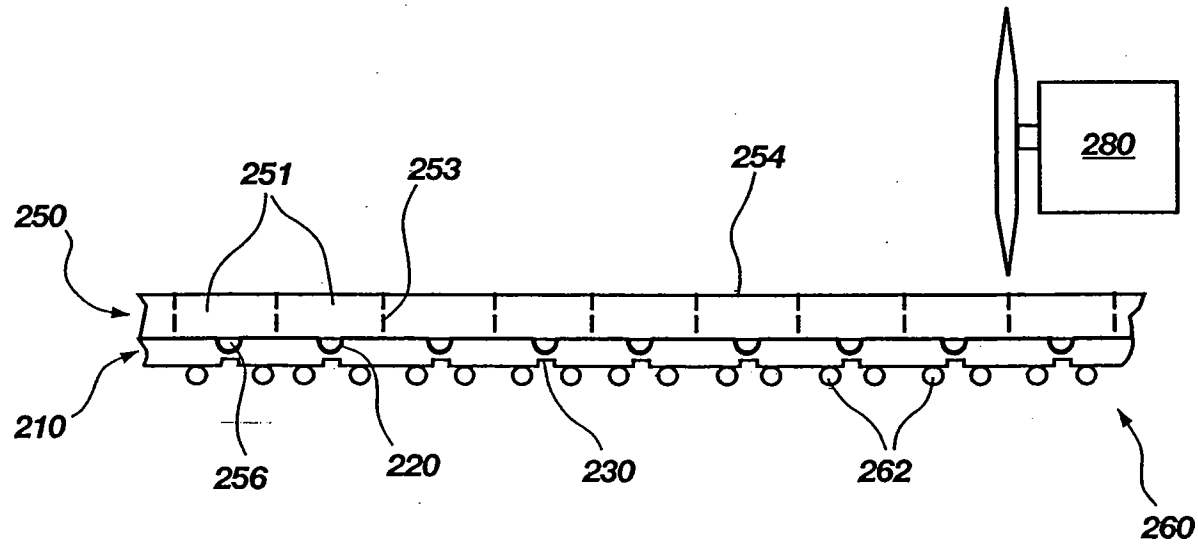


Fig. 13B

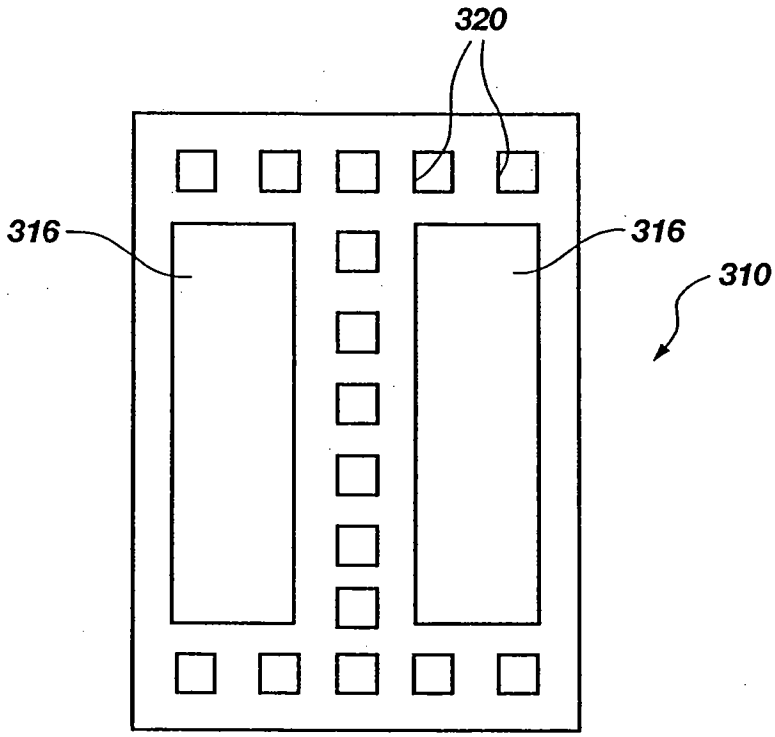


Fig. 14

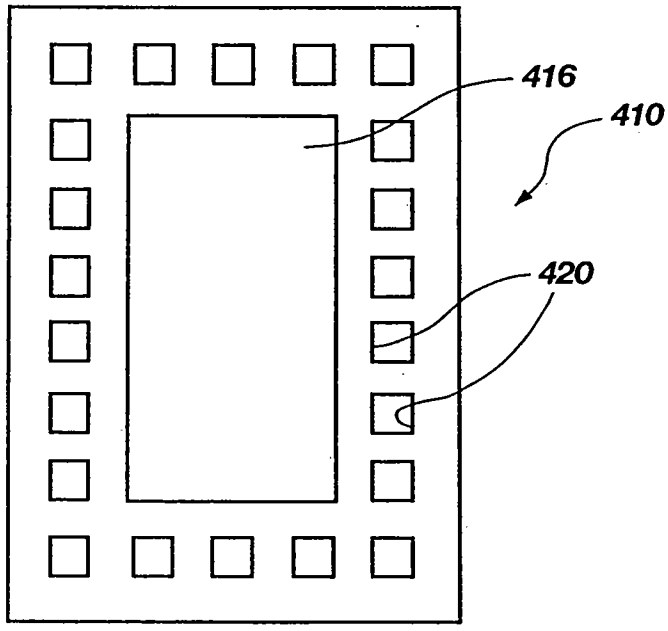


Fig. 15

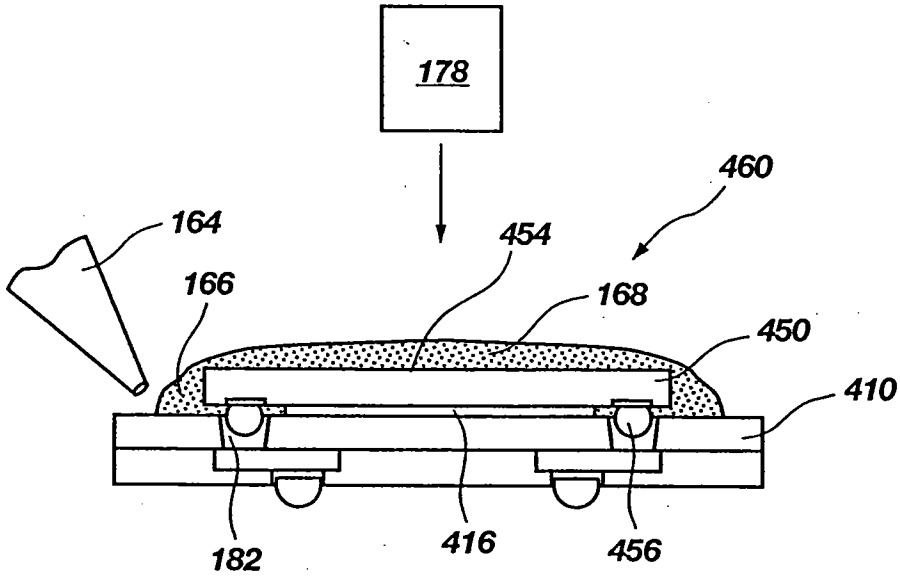

178
460
164
454
166
168
450
410
182
416
456

Fig. 16

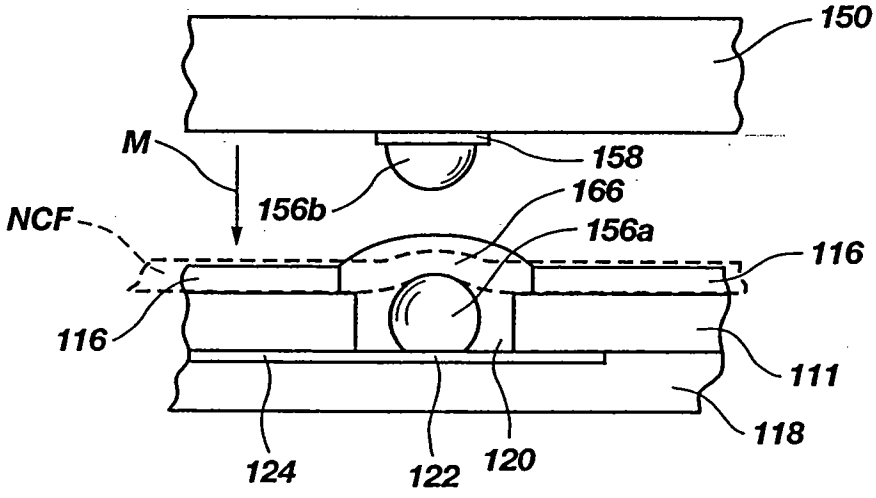

150
M
158
156b
166
NCF
156a
116
116
111
118
124
122
120

Fig. 18

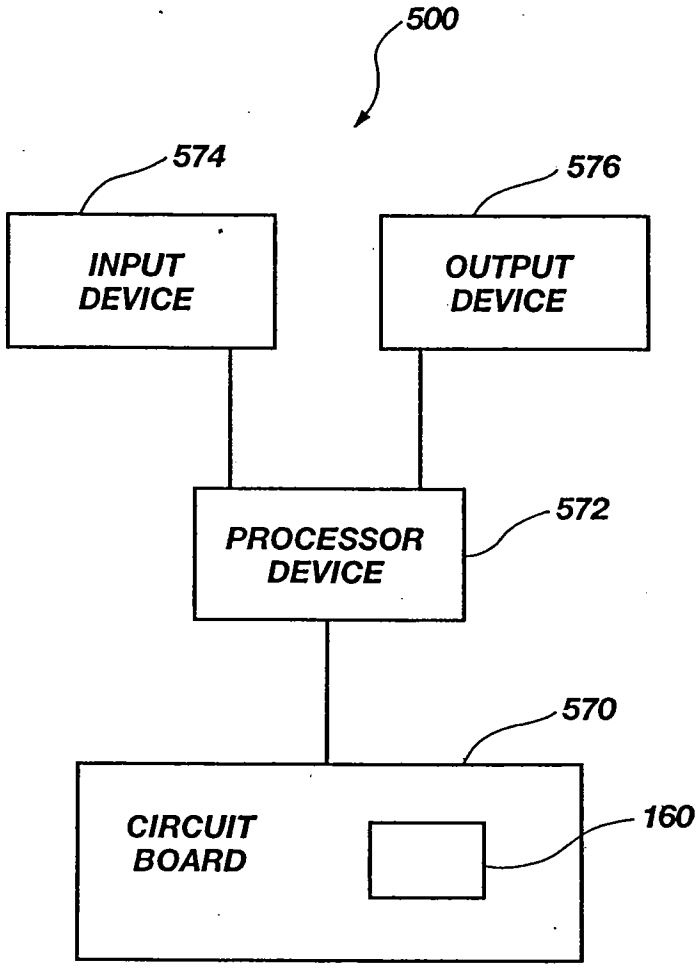

500
574
576
INPUT DEVICE
OUTPUT DEVICE
572
PROCESSOR DEVICE
570
CIRCUIT BOARD
160

Fig. 17

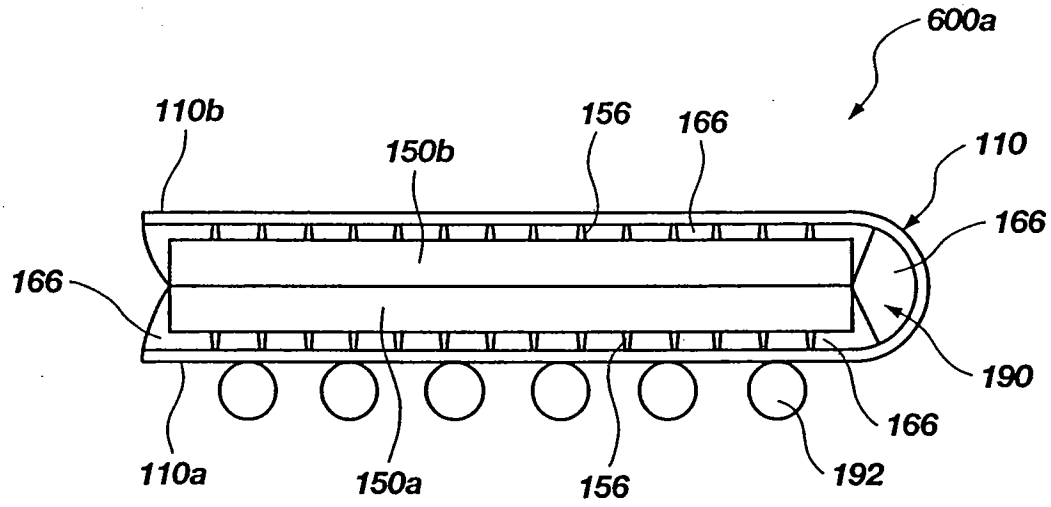


Fig. 19

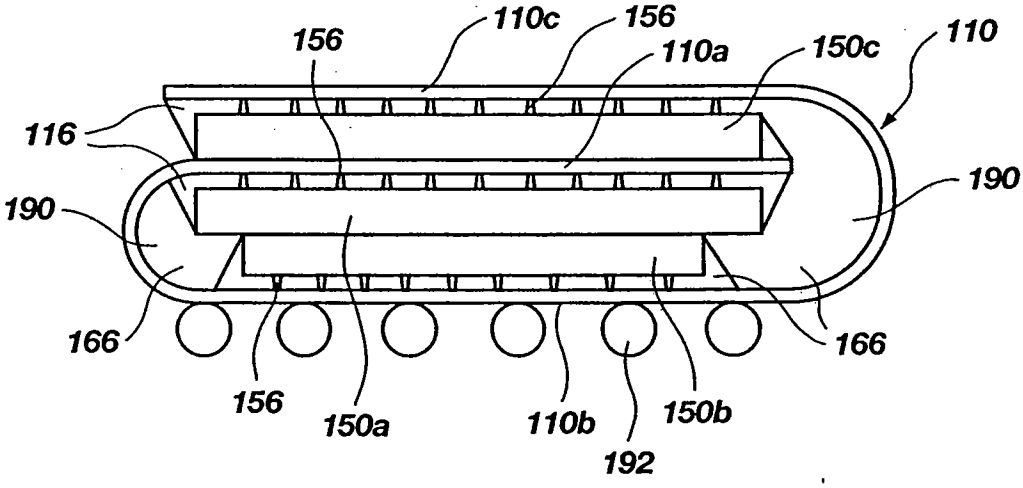


Fig. 20

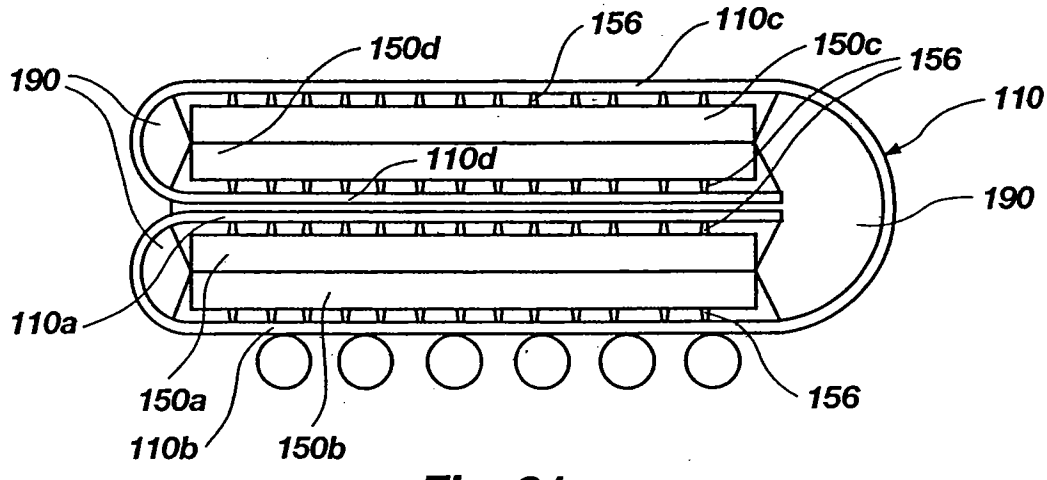


Fig. 21