A B S T R A C T

An IC card (1) has a CPU (10) which performs various operation processing, a data memory (12) which stores internal data, and a working memory (11) which stores the results of the processing. The CPU (10) performs first operation processing on the internal data stored in the data memory (12) and predetermined data for conversion, stores data for collation obtained by the first operation processing on the internal data and the data for conversion in a working memory (11), performs second operation processing corresponding to the first operation processing on external data received from the external device and the data for collation stored in the working memory (11), and collates data obtained by the second operation processing with the data for conversion.

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