

FIG. 1A

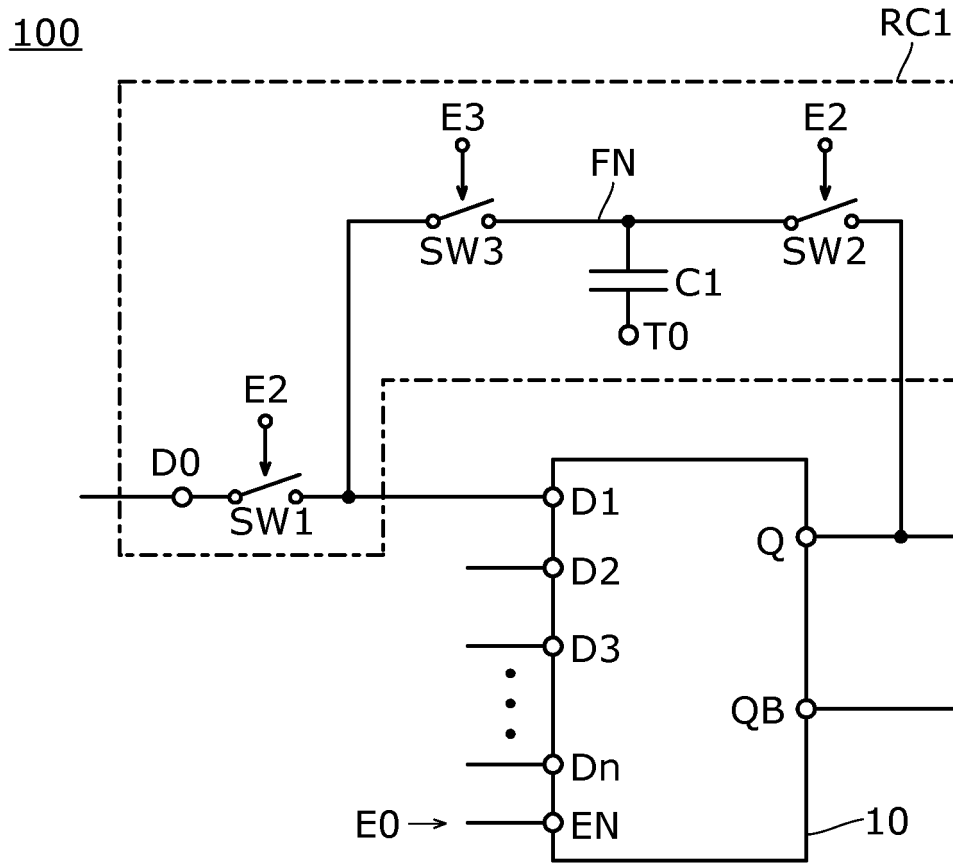


FIG. 1B

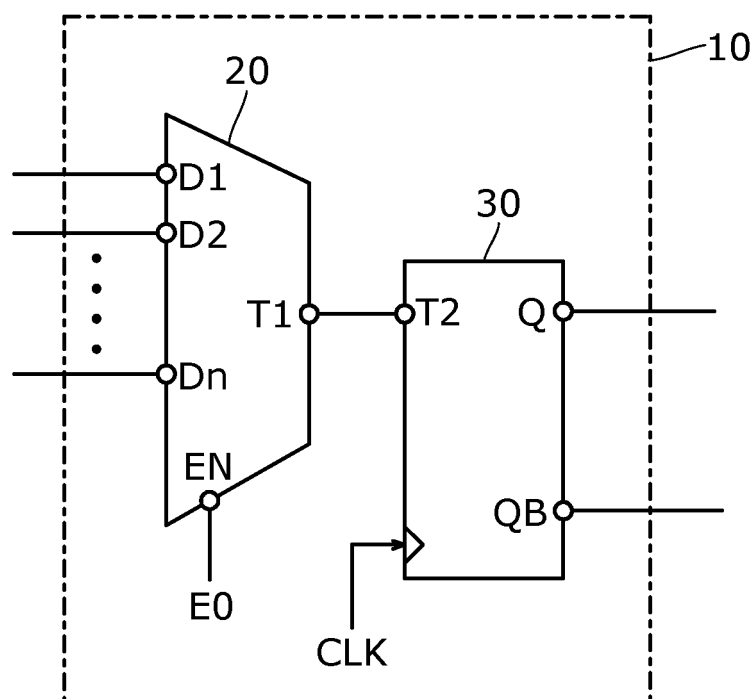


FIG. 2A

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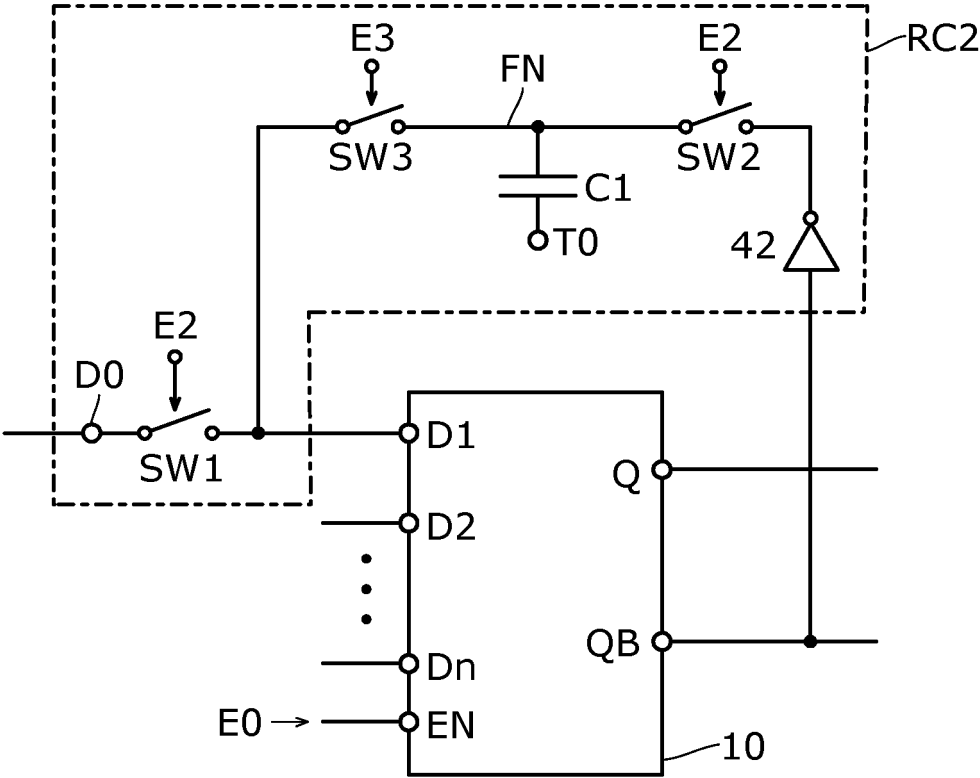


FIG. 2B

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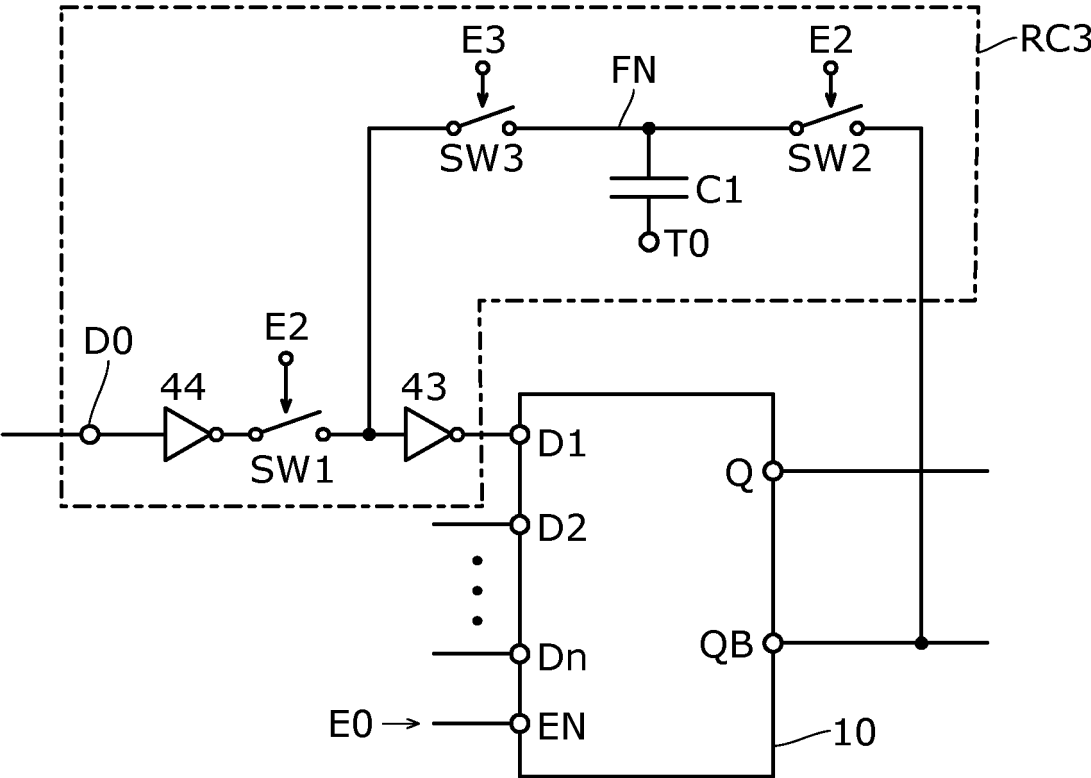
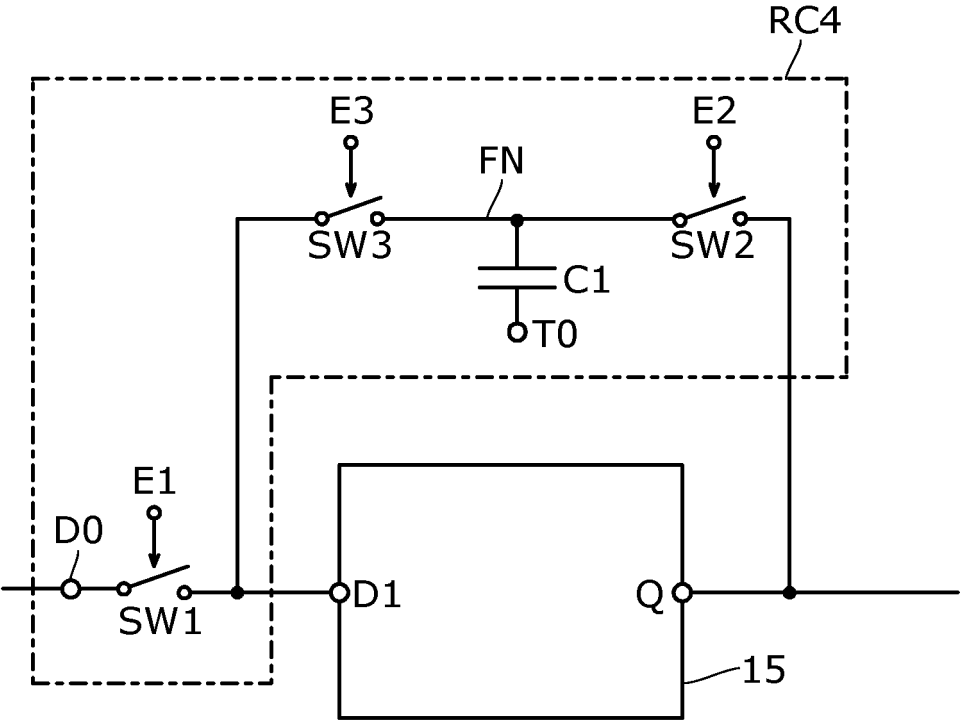


FIG. 3

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FIG. 4

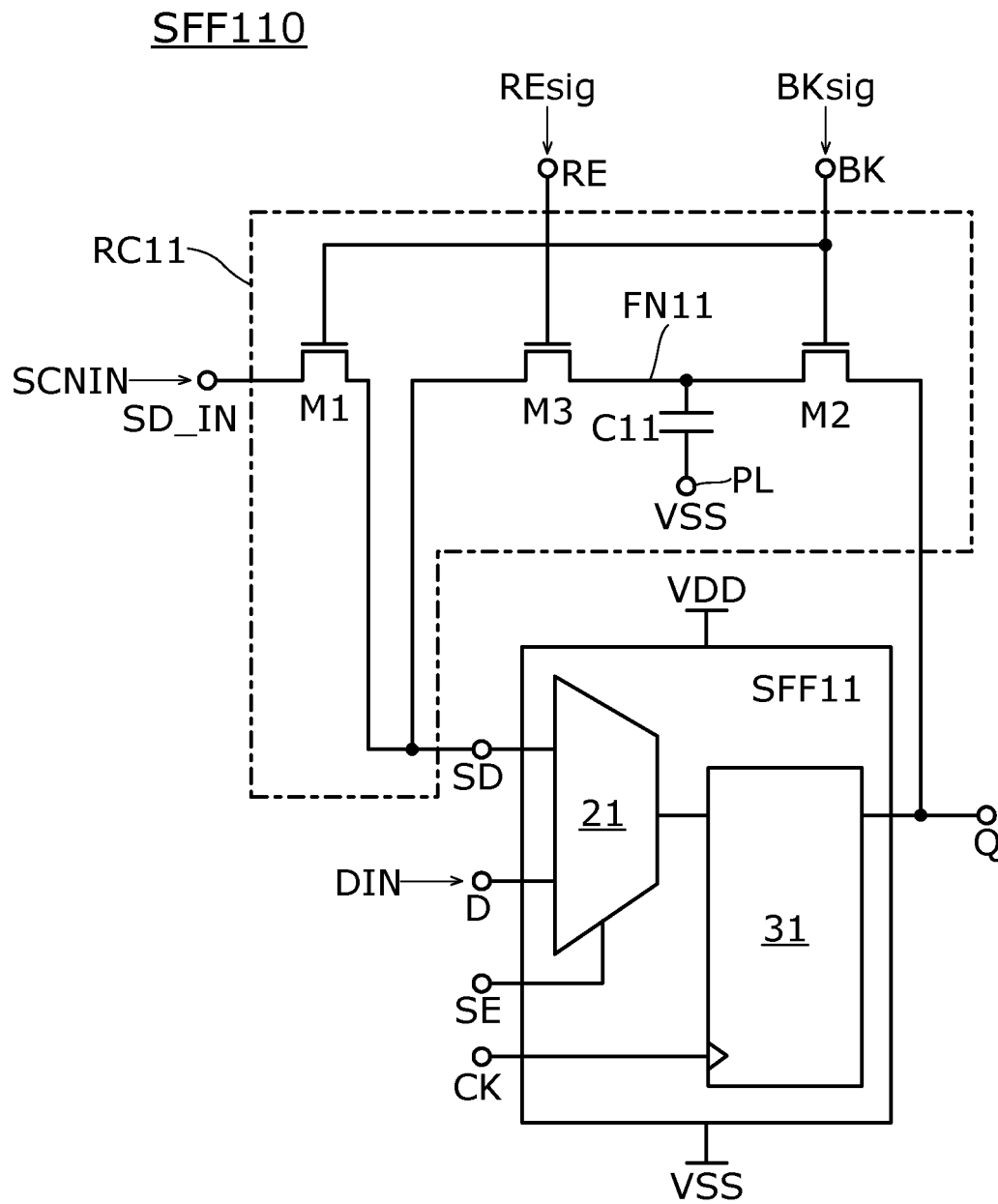
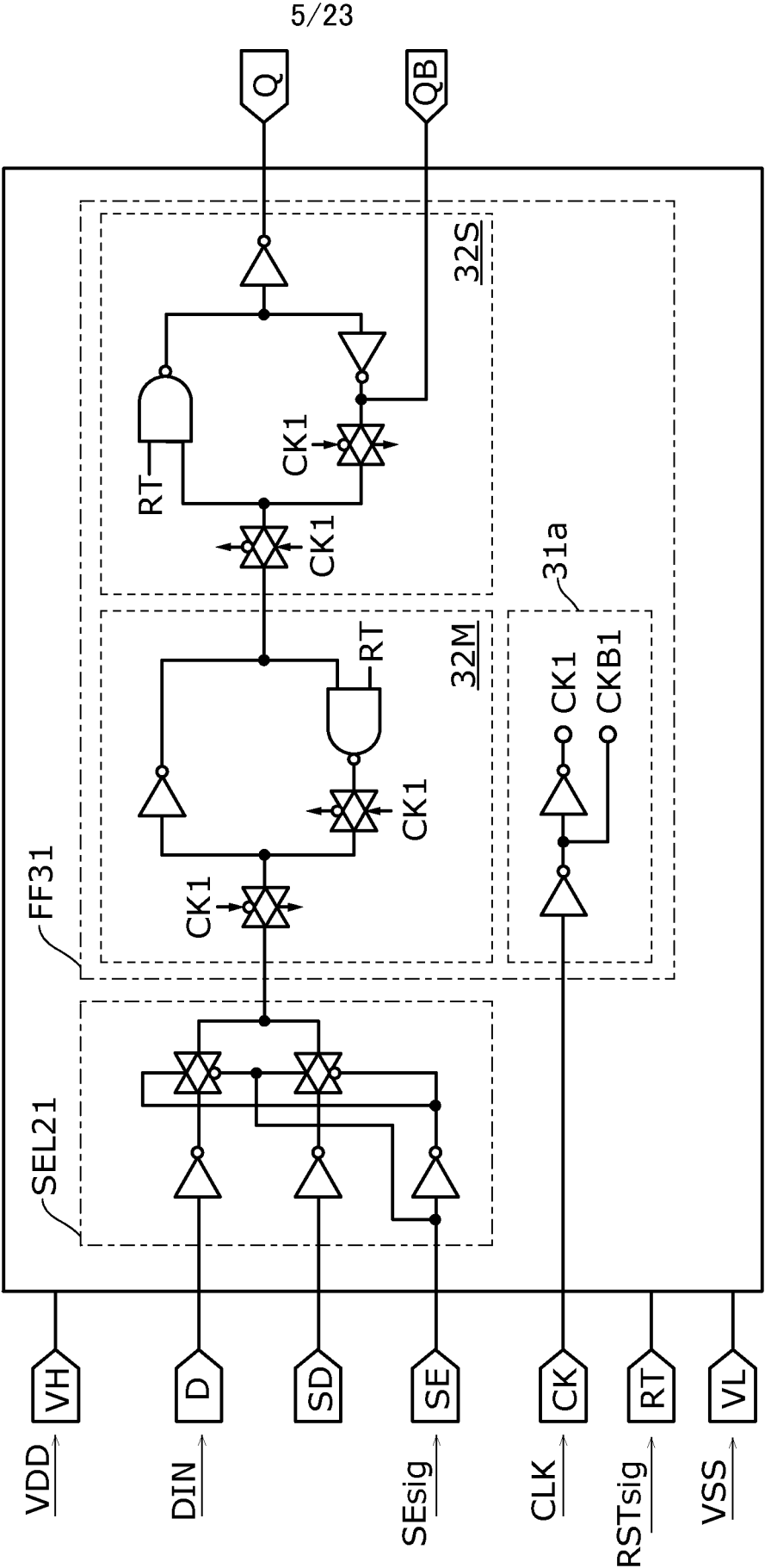


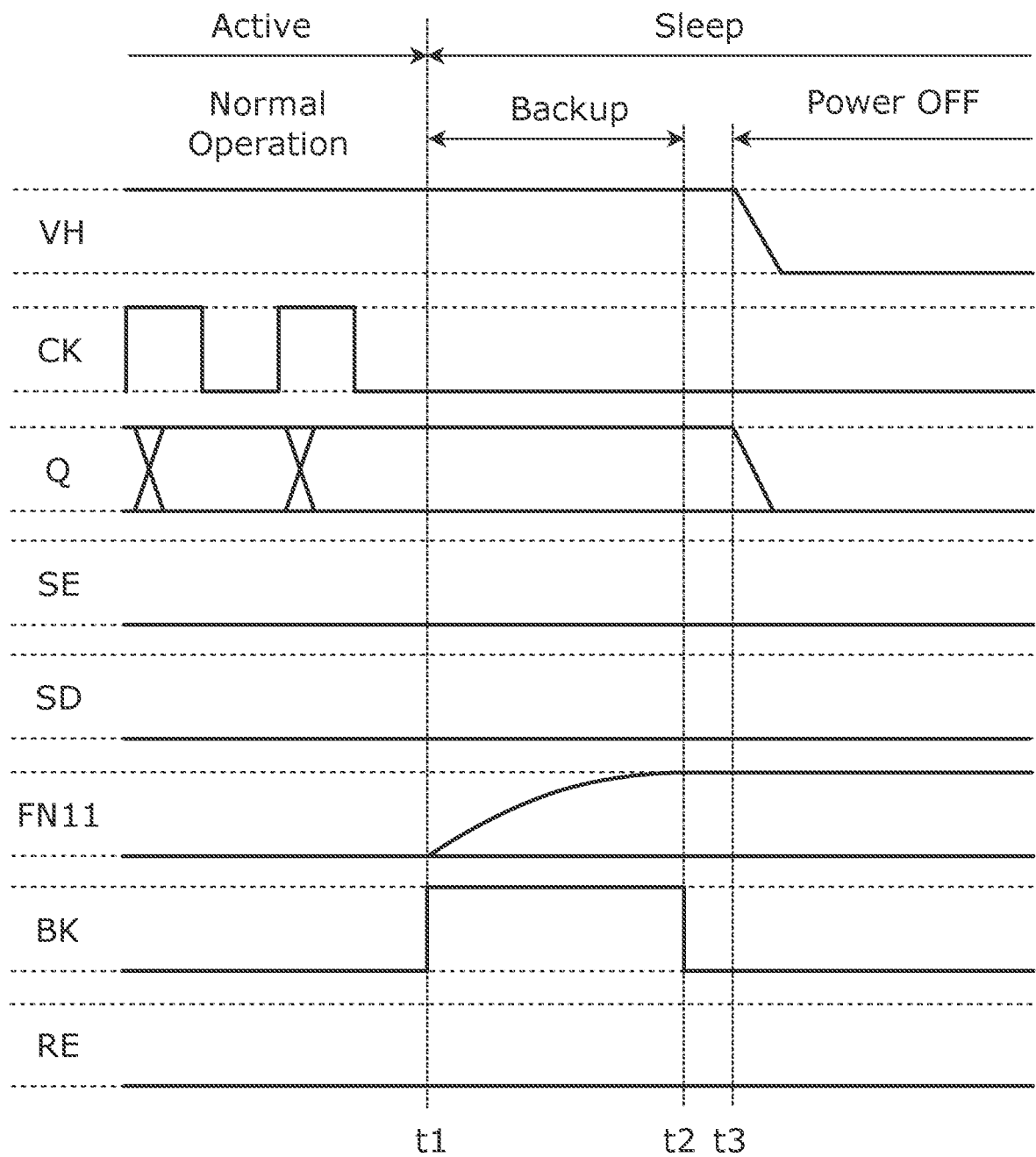
FIG. 5

SFF11



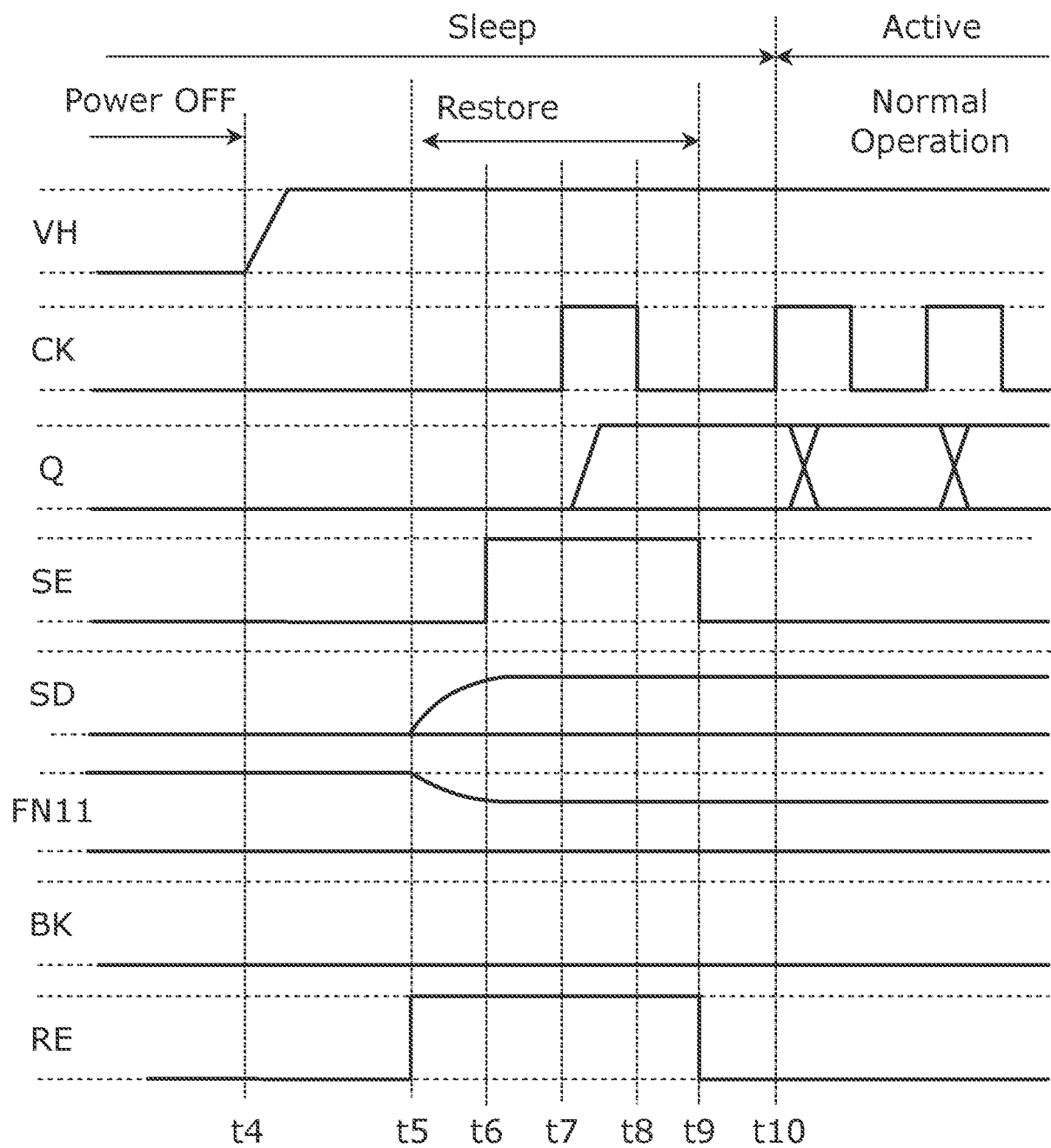
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FIG. 6



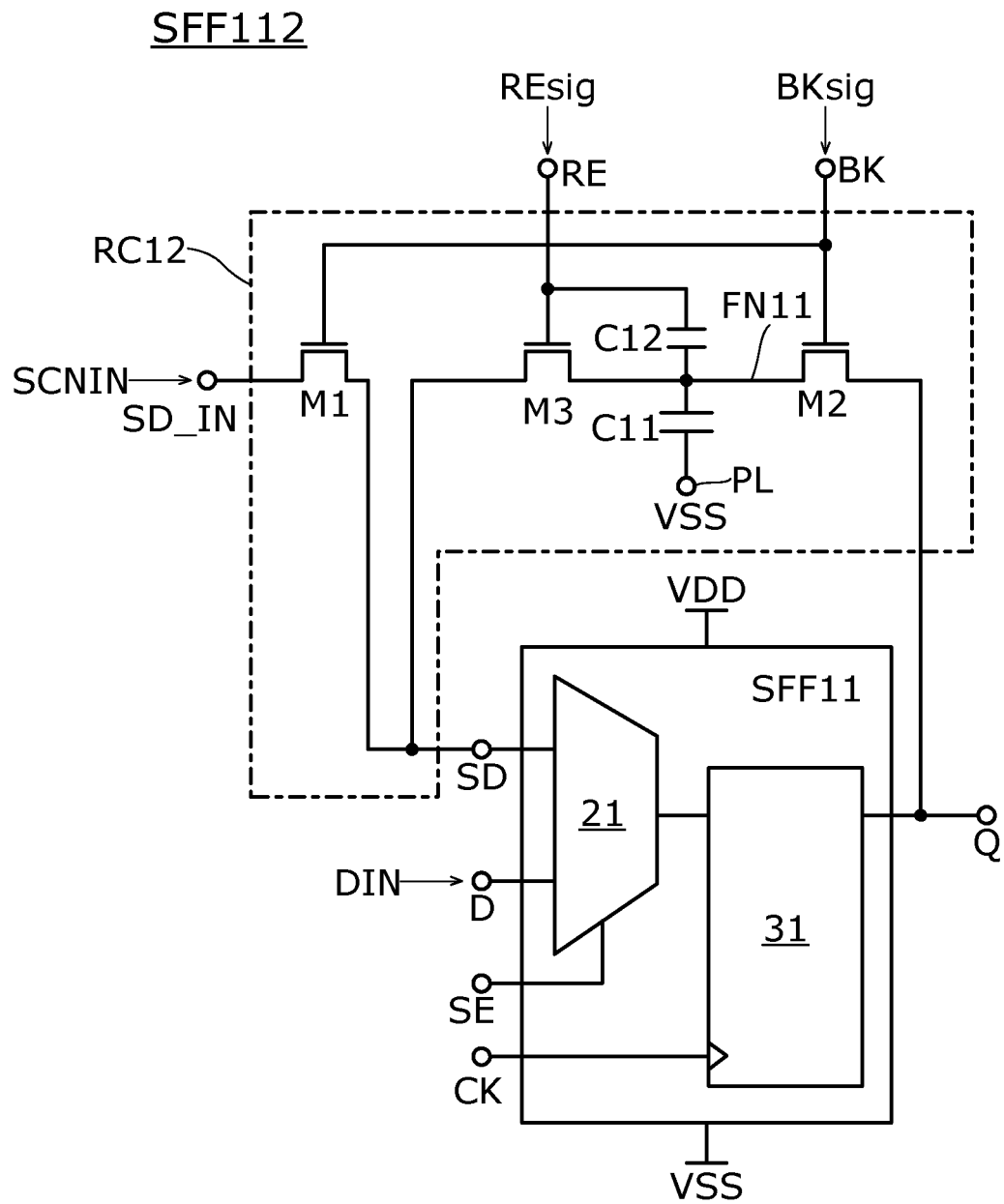
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FIG. 7



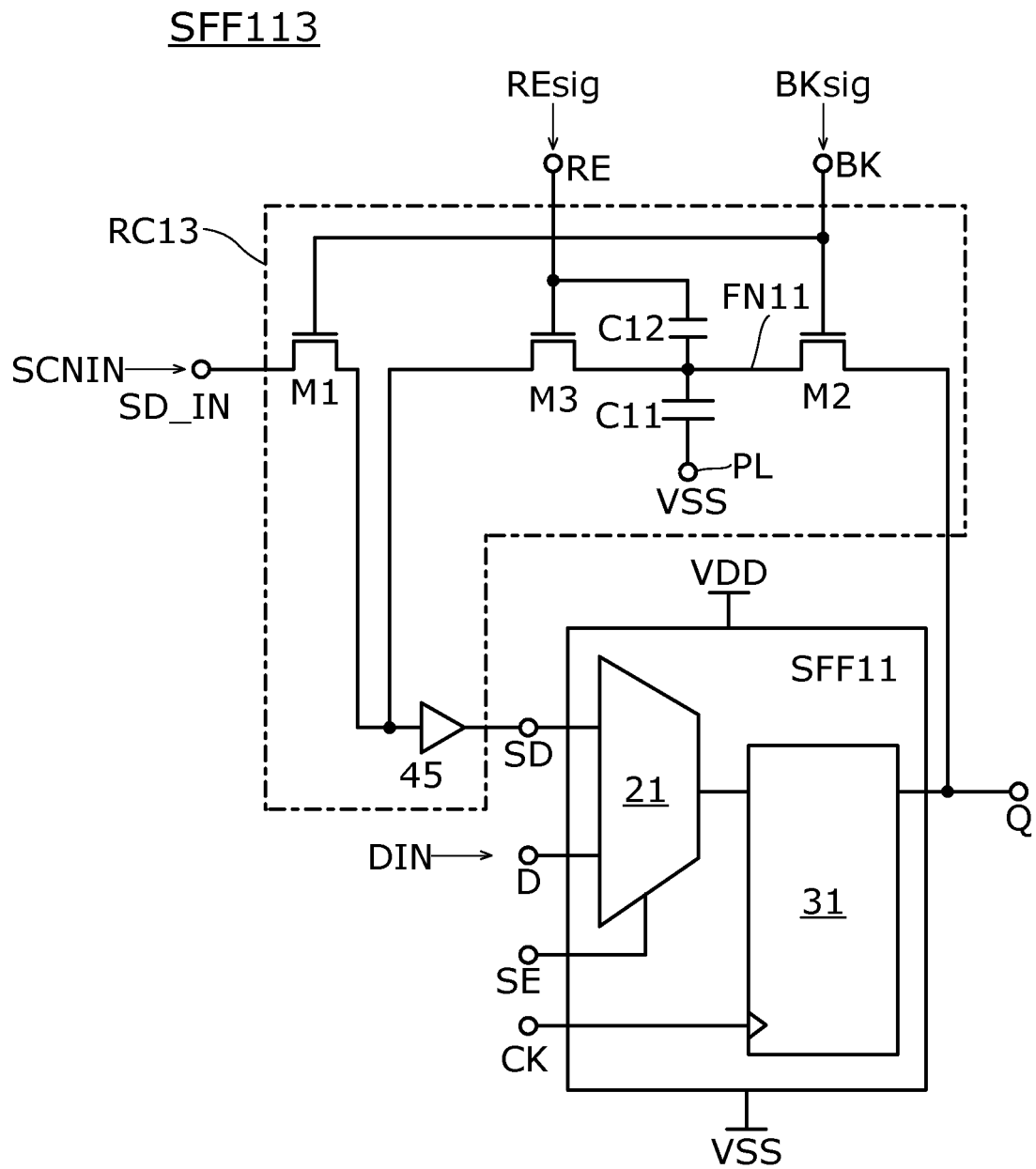
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FIG. 8



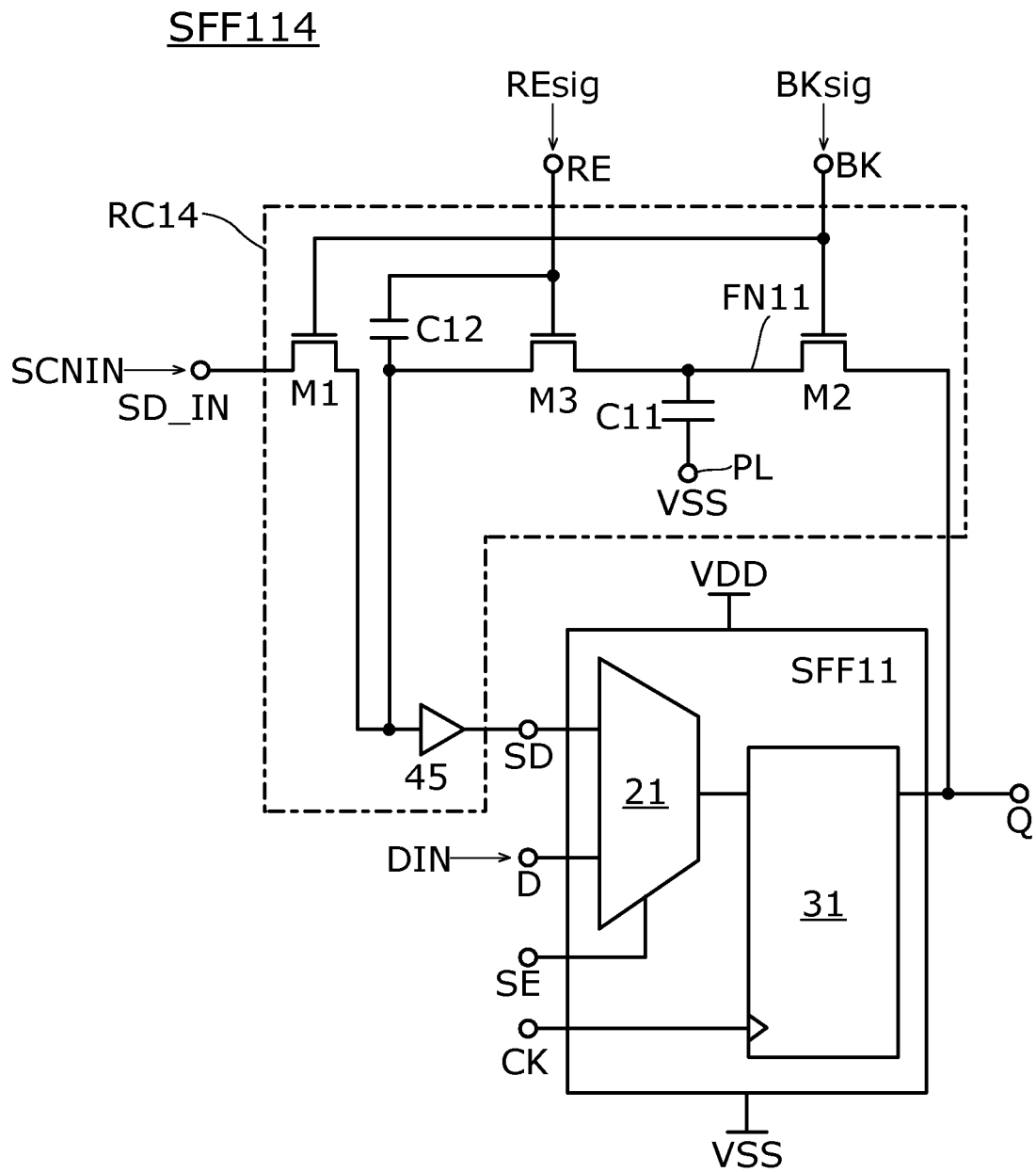
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FIG. 9



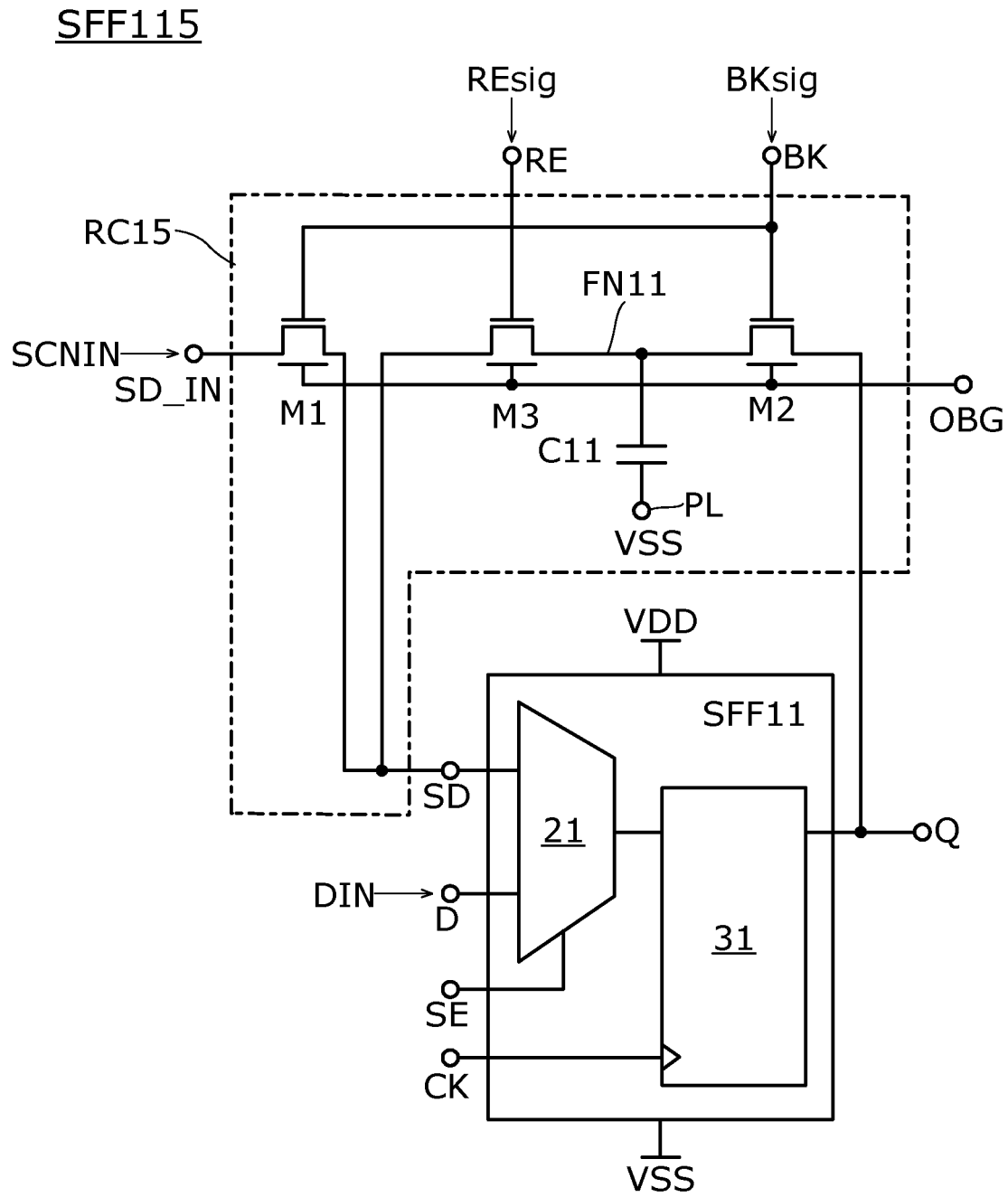
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FIG. 10



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FIG. 11



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FIG. 12

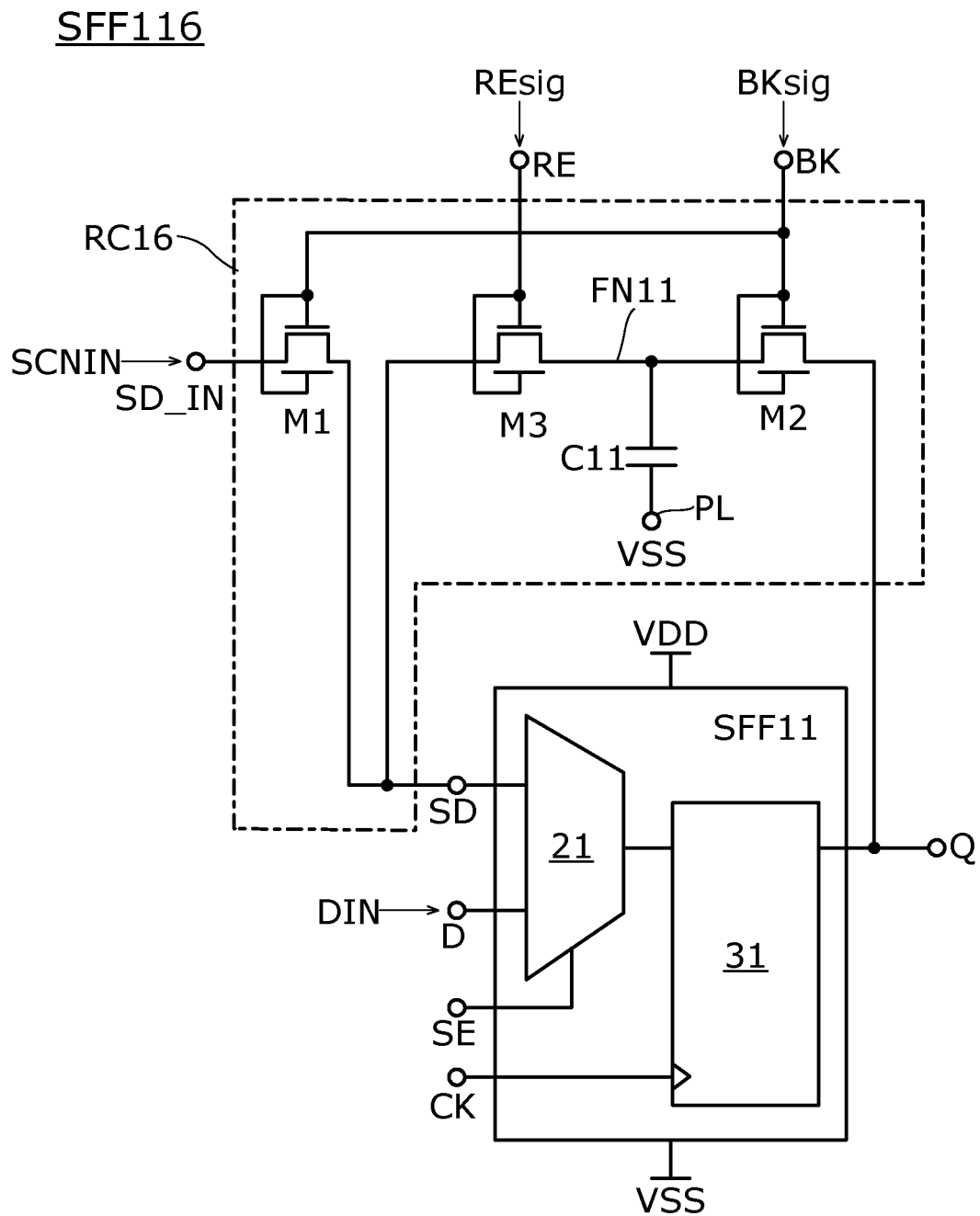


FIG. 13

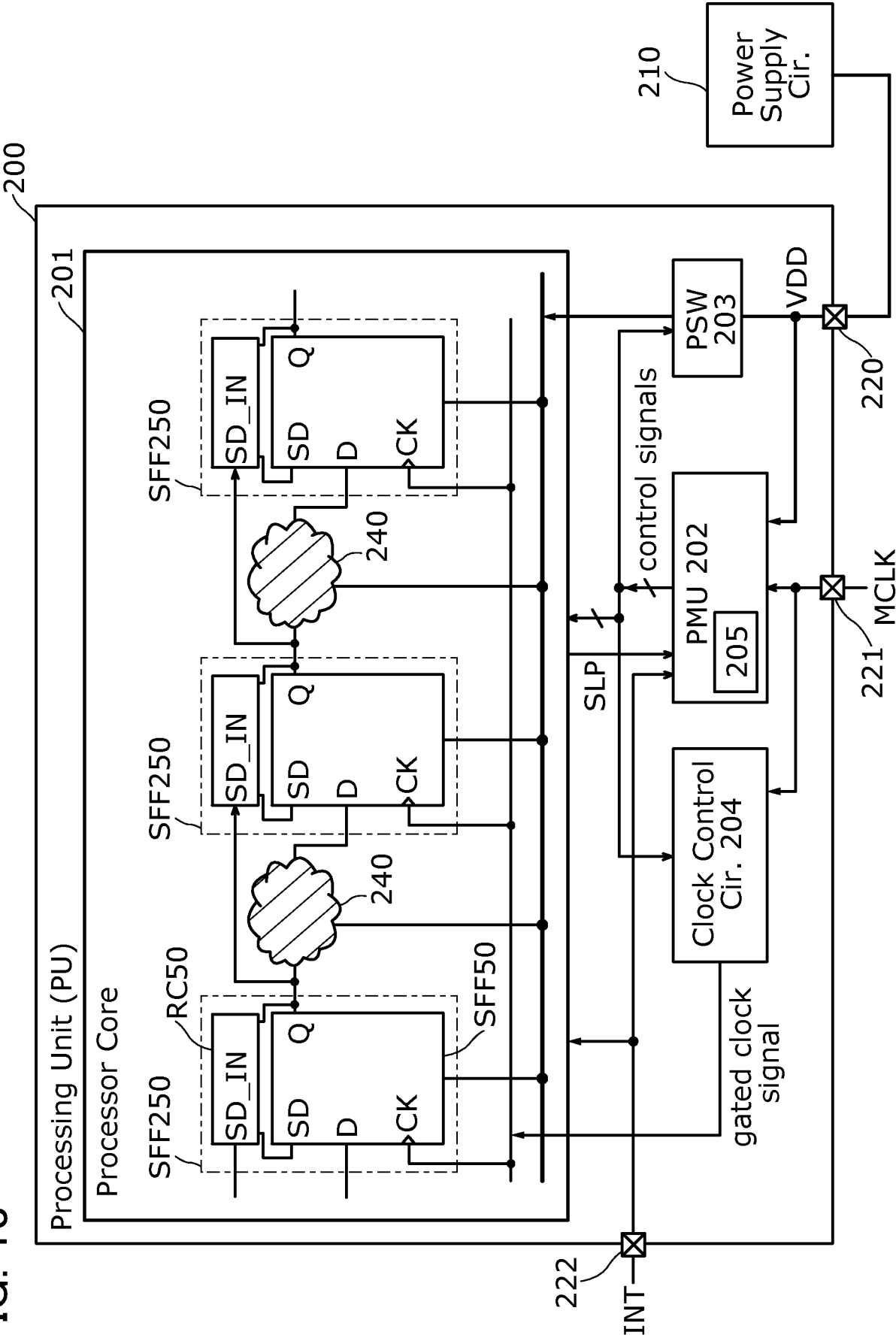
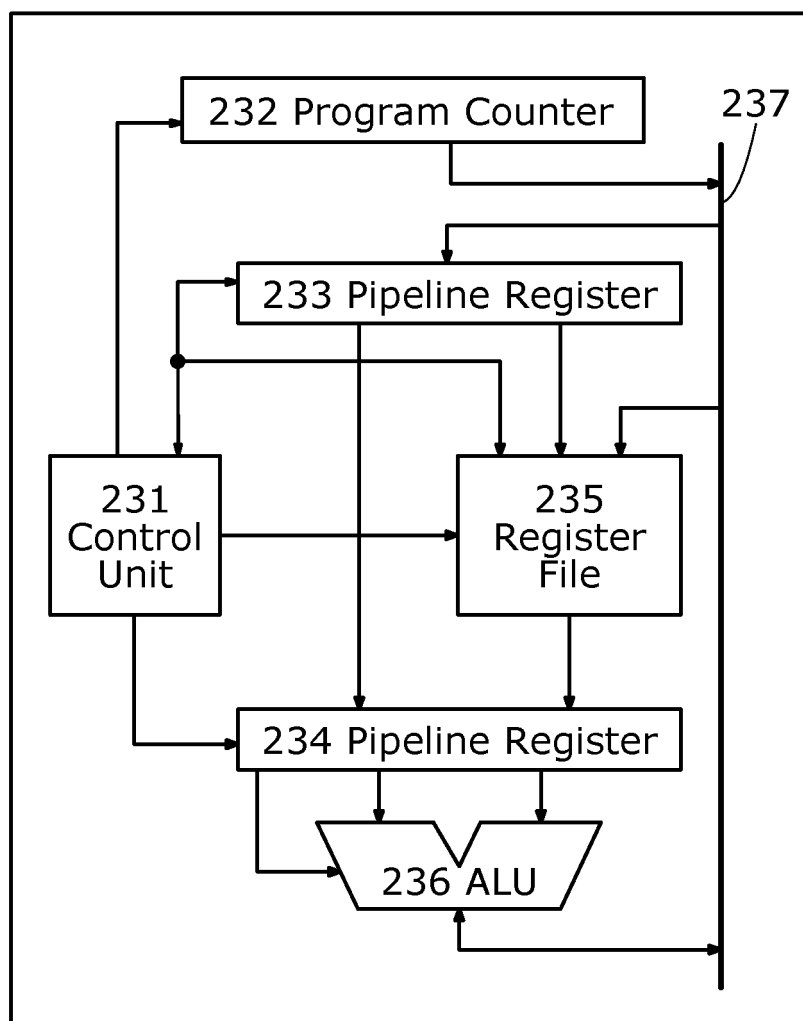


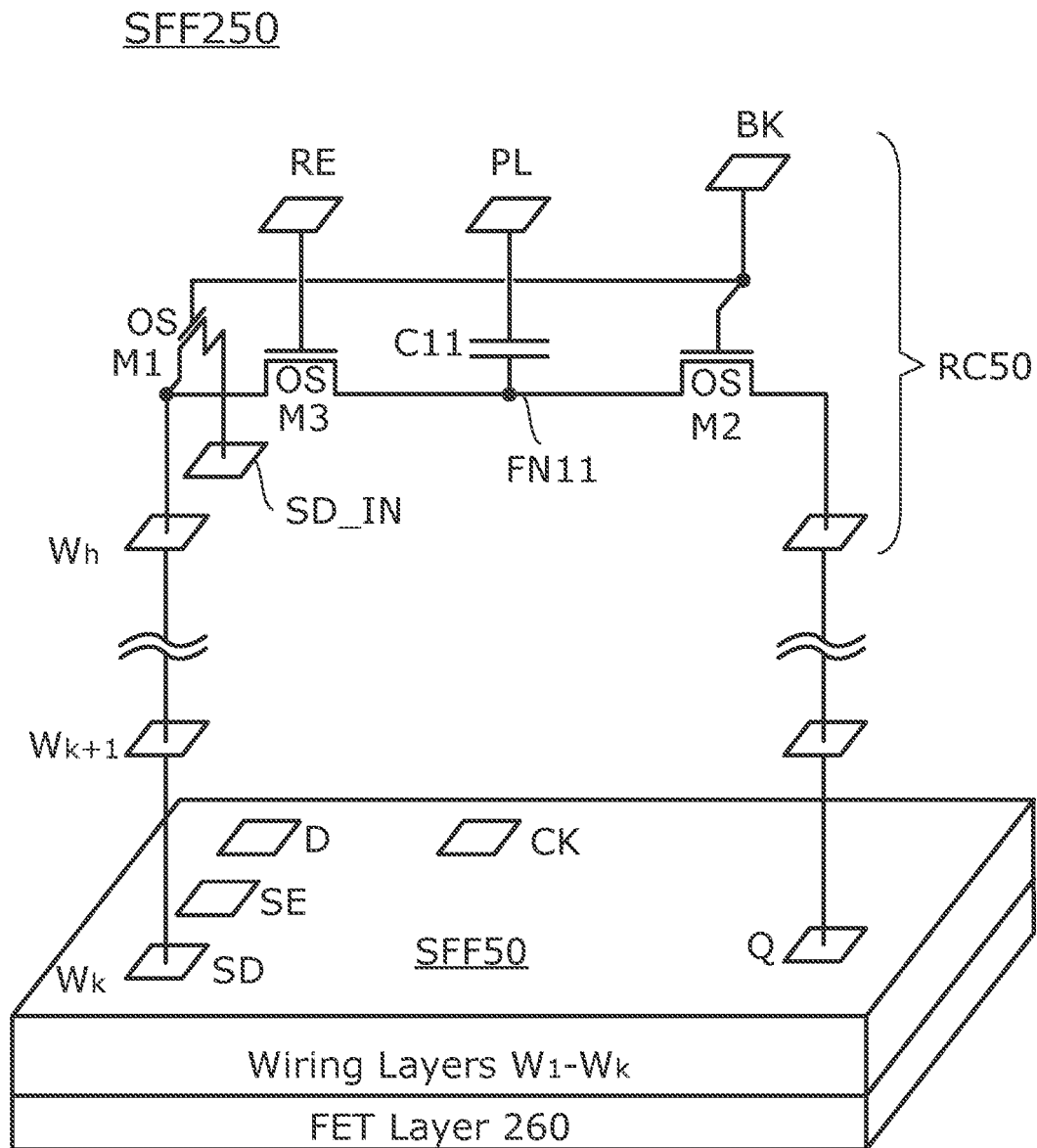
FIG. 14

Processor Core
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FIG. 15



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FIG. 16A

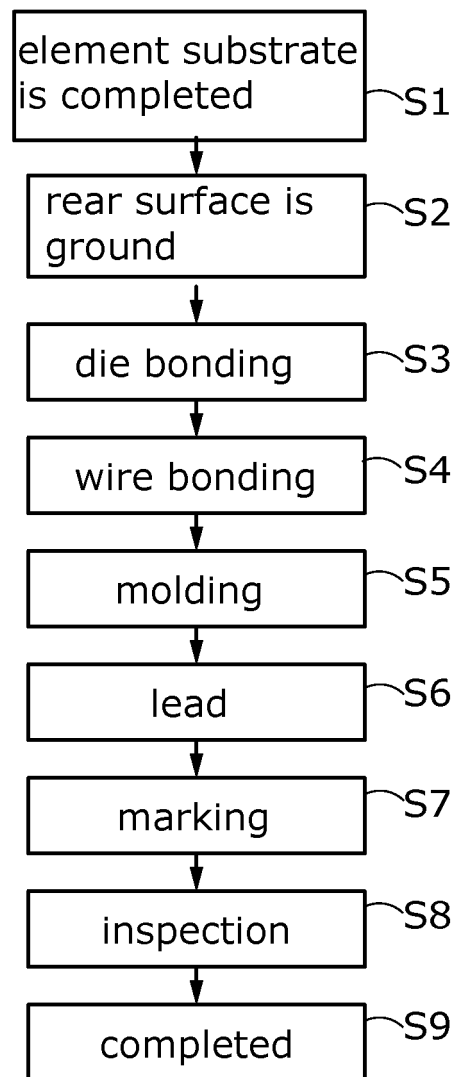
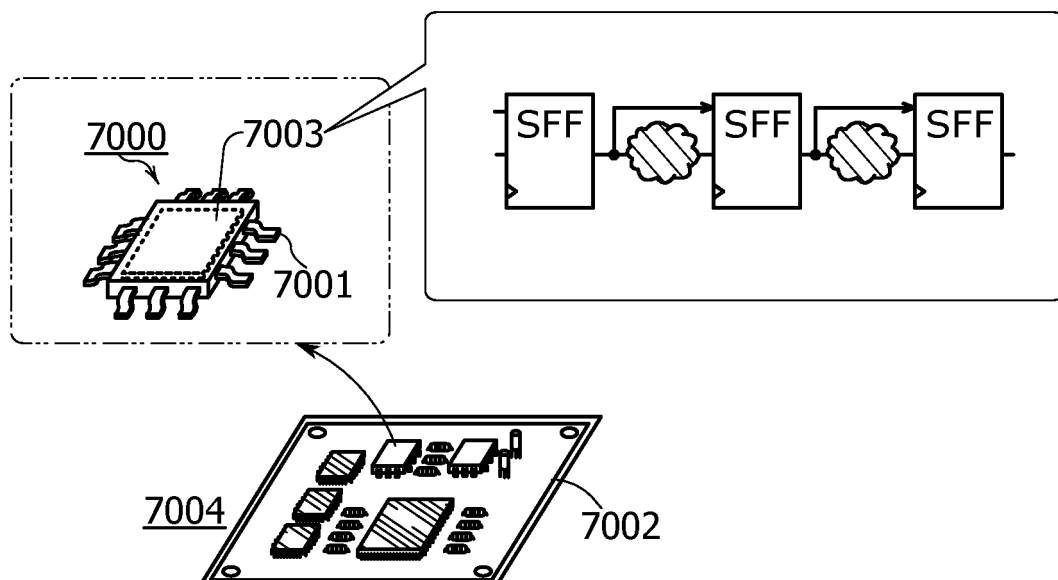


FIG. 16B



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FIG. 17A

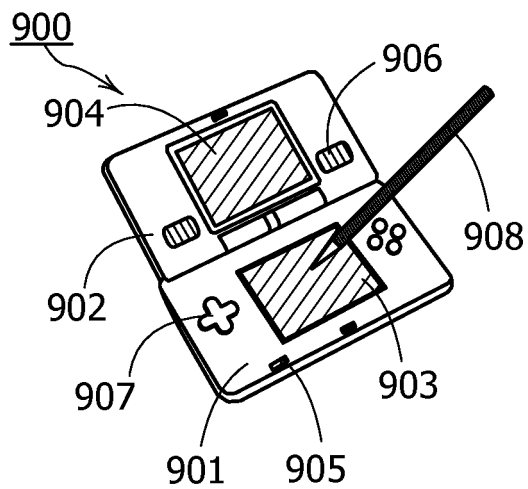


FIG. 17B

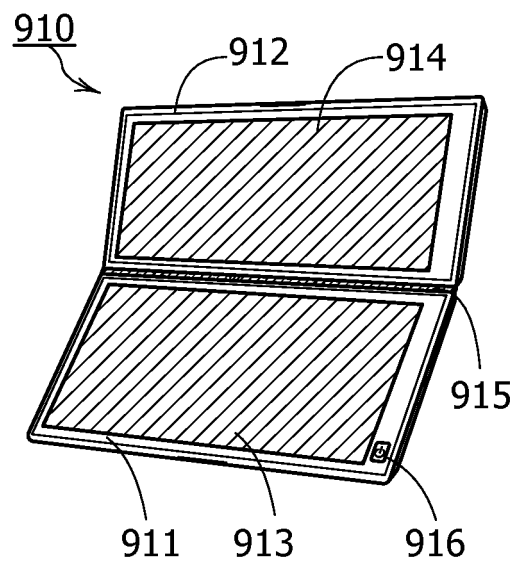


FIG. 17C

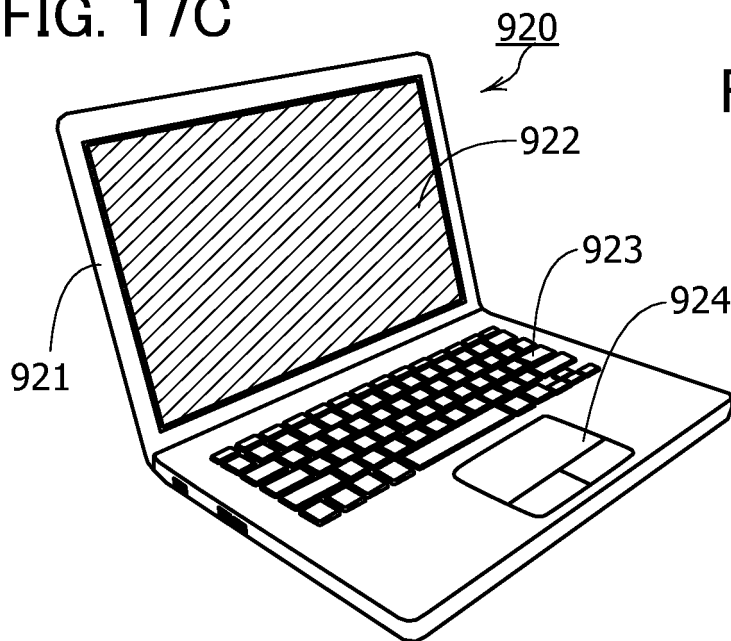


FIG. 17D

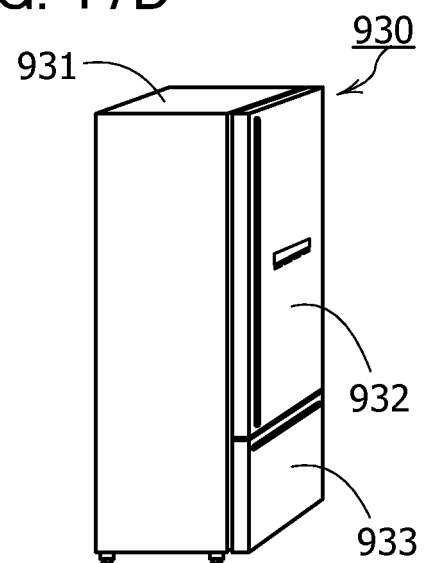


FIG. 17E

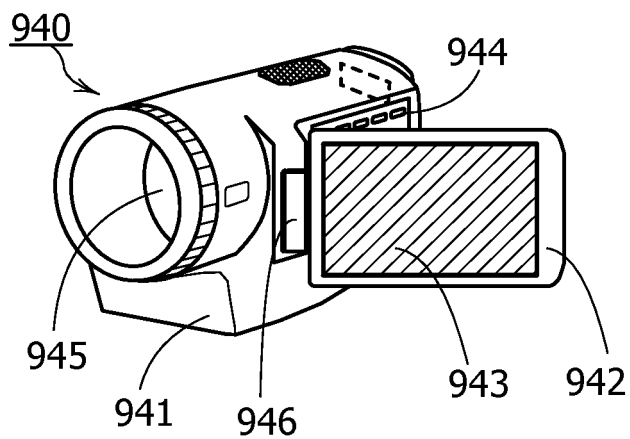
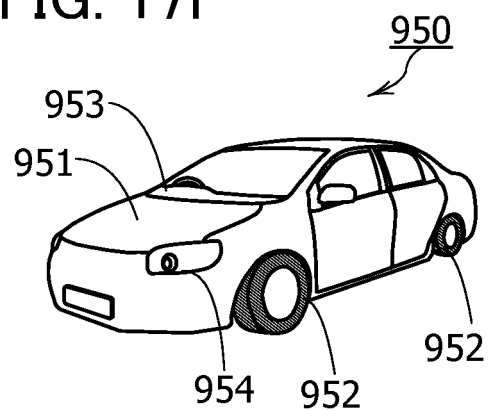


FIG. 17F



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FIG. 18A

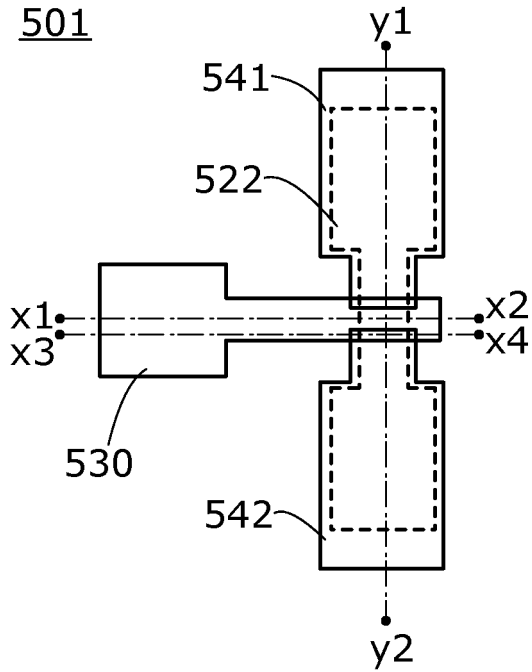


FIG. 18B

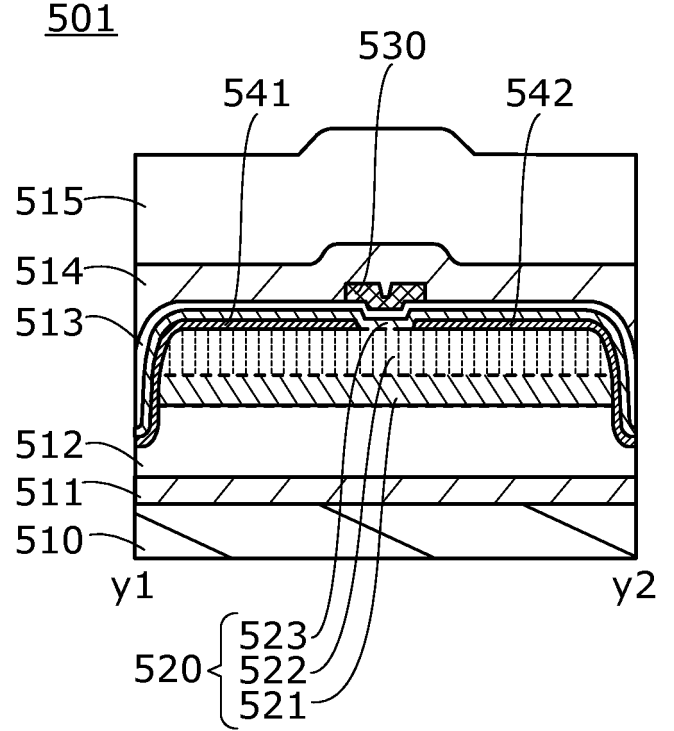


FIG. 18C

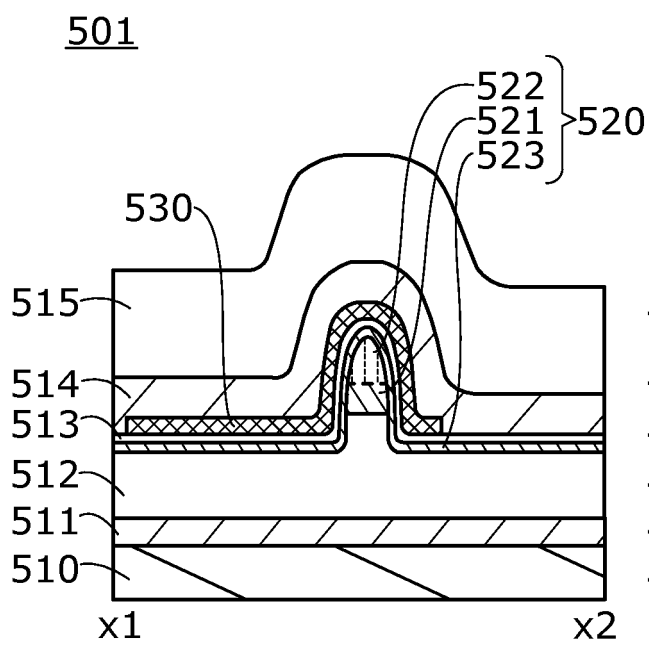


FIG. 18D

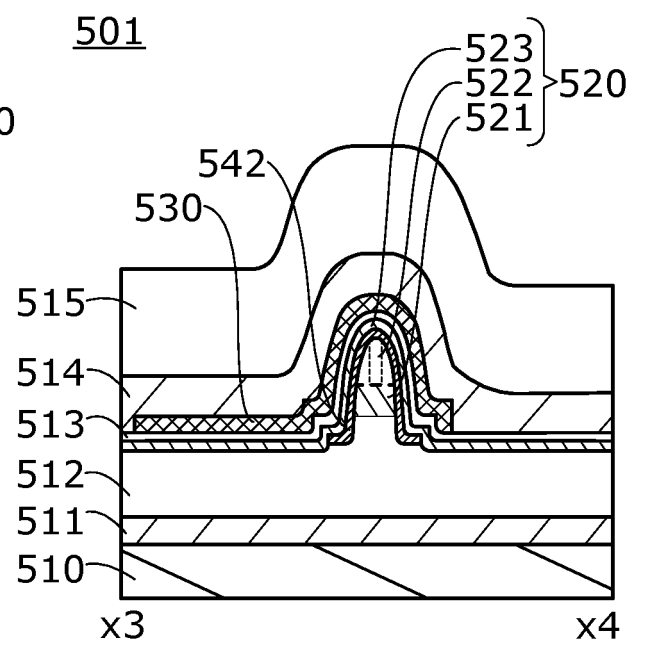


FIG. 19A

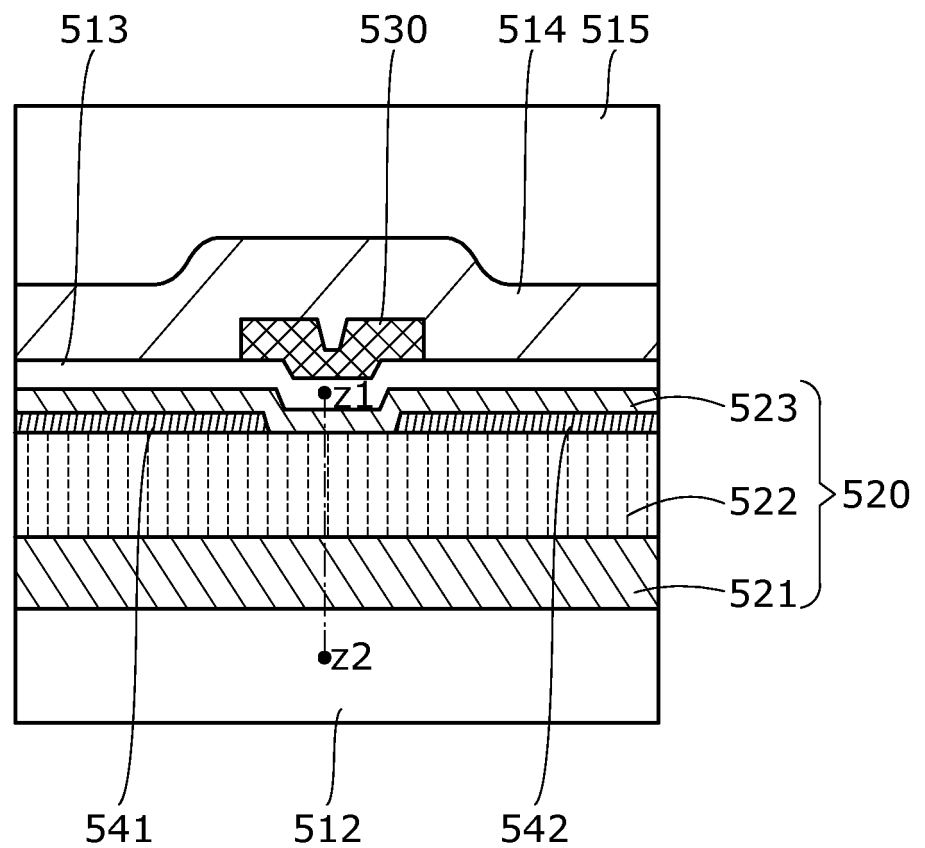


FIG. 19B

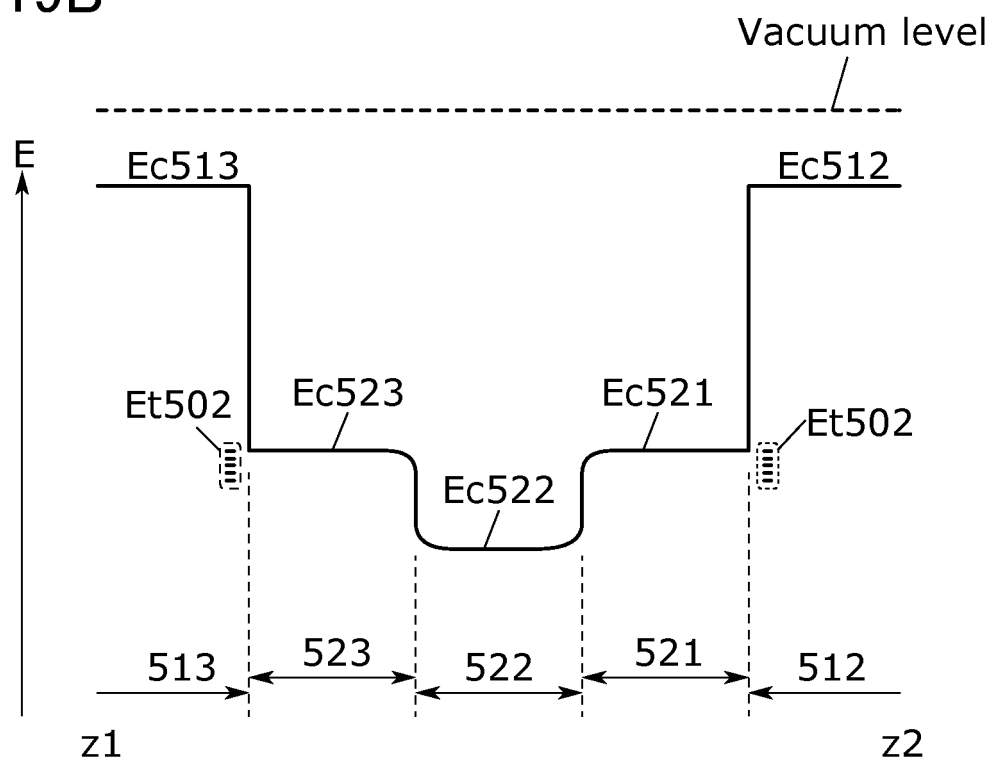


FIG. 20A

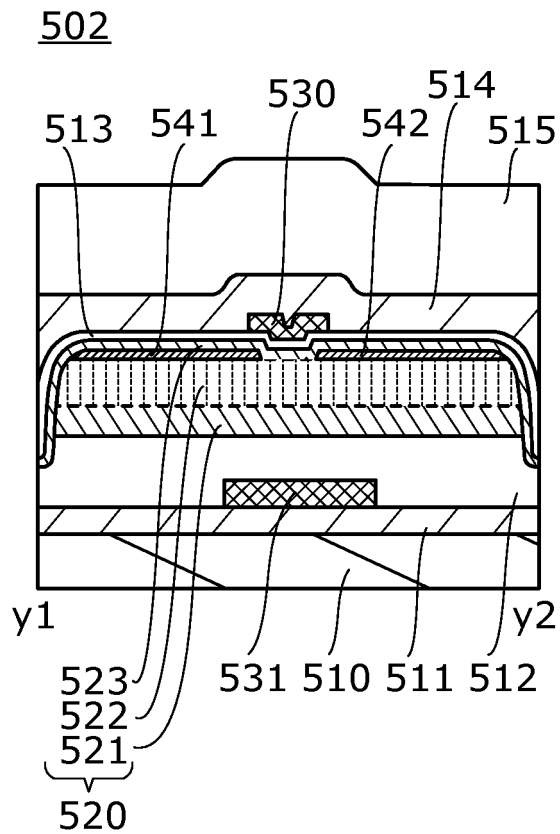


FIG. 20B

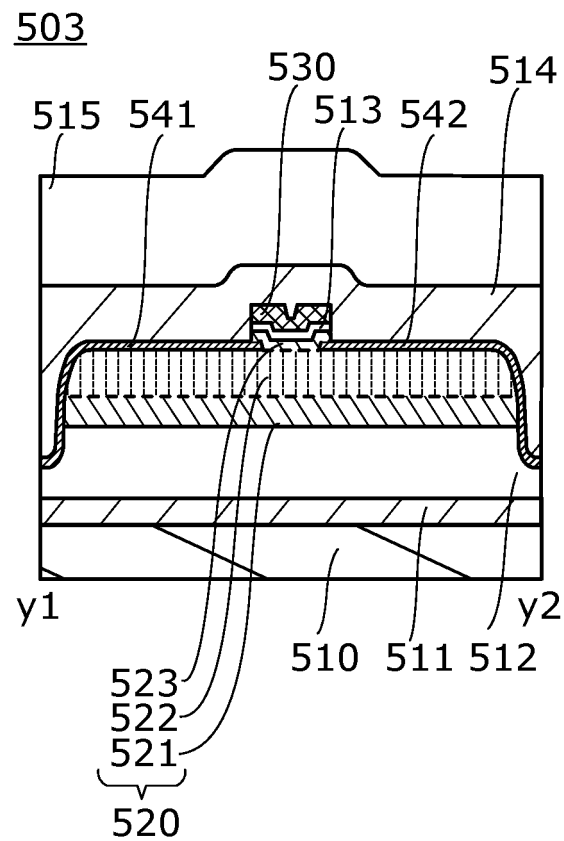


FIG. 20C

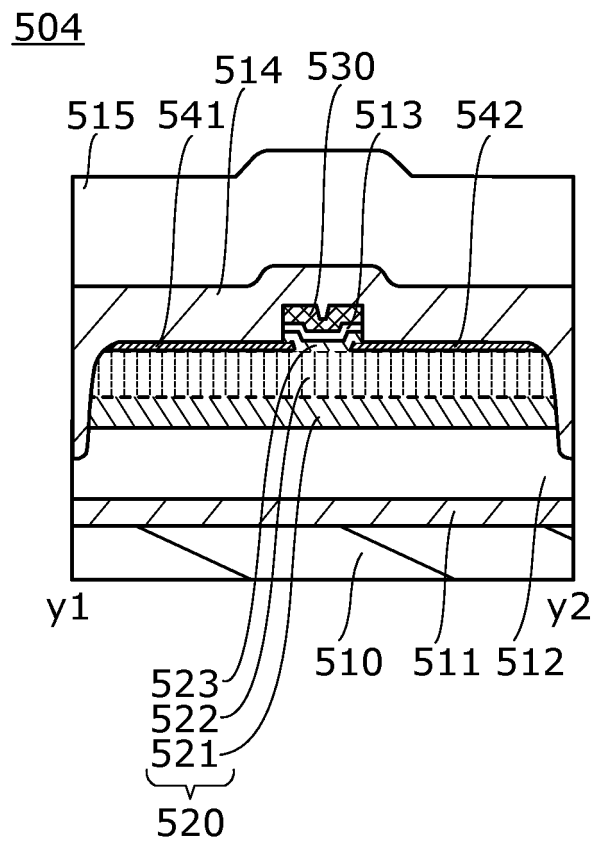


FIG. 21A

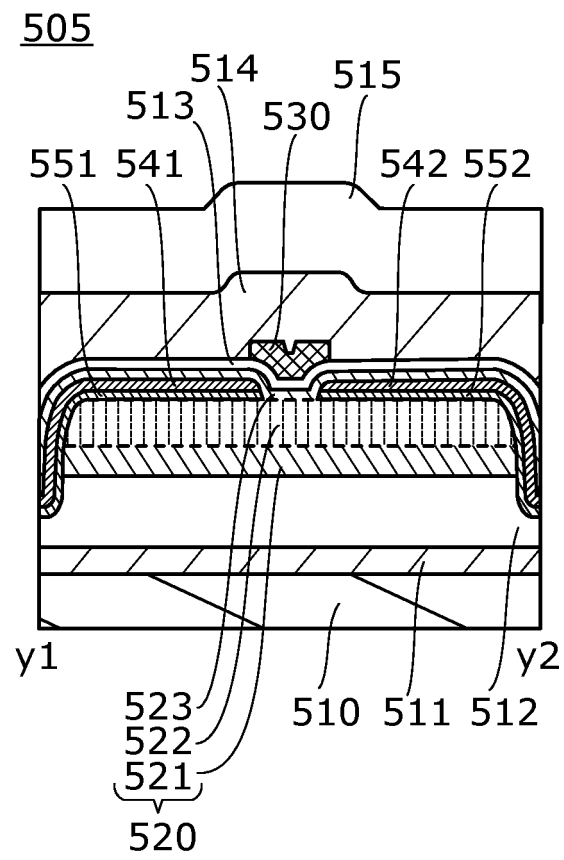
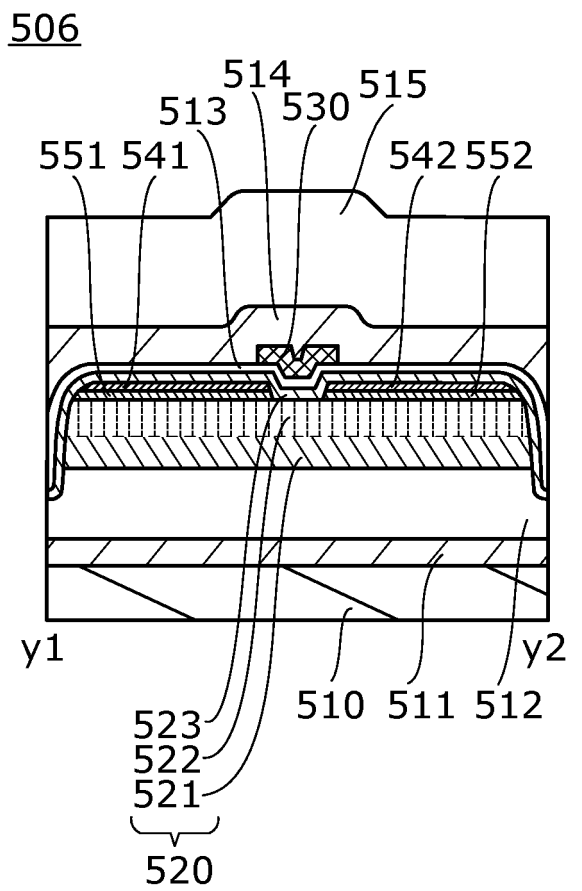


FIG. 21B



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FIG. 22

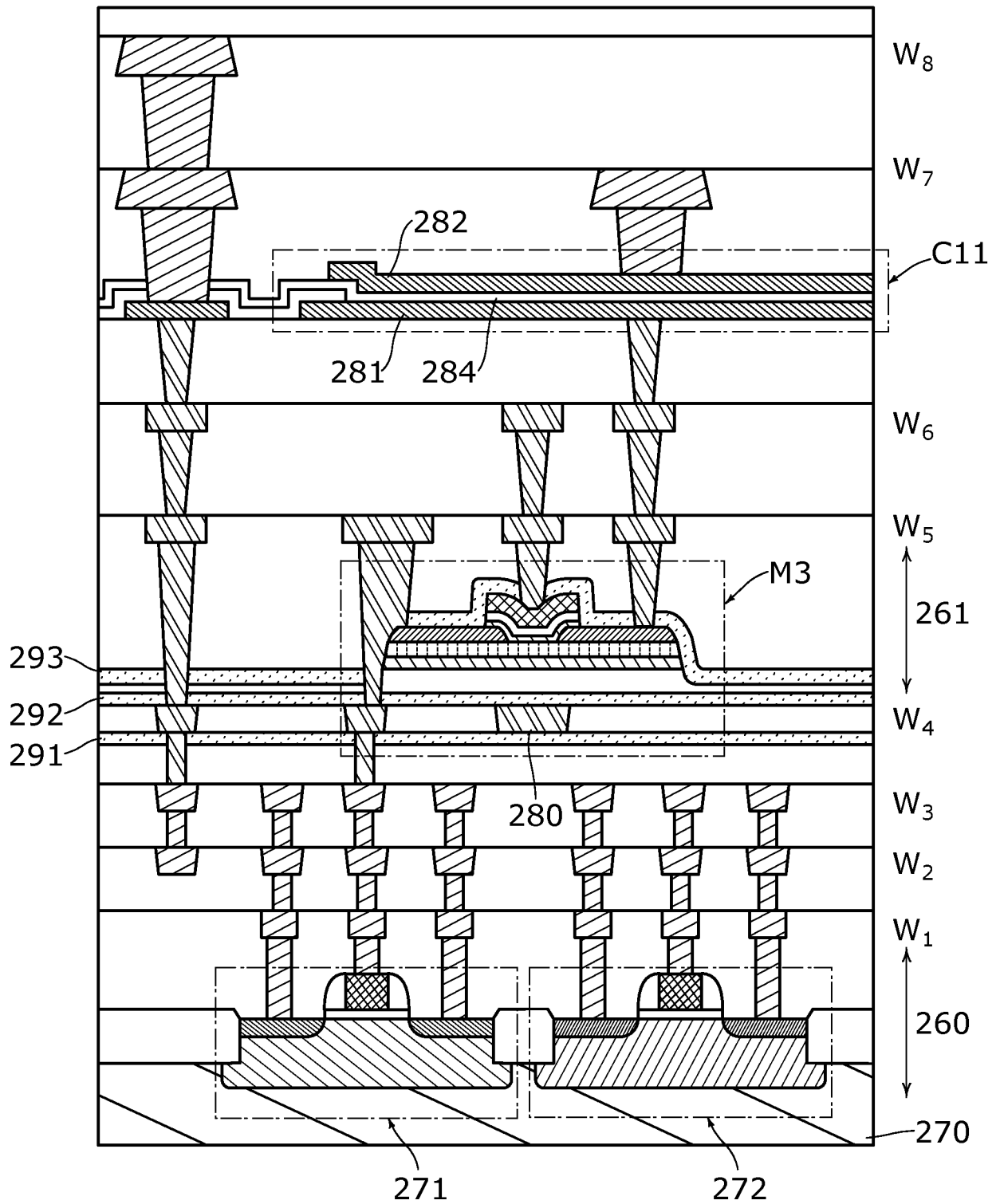


FIG. 23

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