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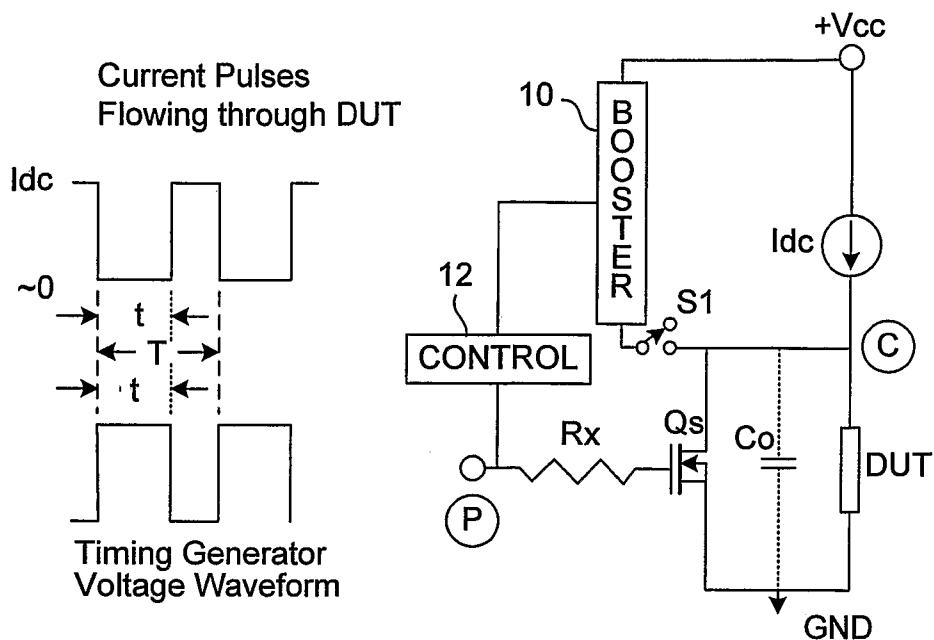
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(54) Title: PULSED CURRENT GENERATOR CIRCUIT WITH CHARGE BOOSTER



(57) Abstract: A pulsed current generator circuit for providing pulses to a device under test includes a current source for applying a current to the device under test and a controlled current shunt for shunting current from the device under test. A booster circuit (10) is provided for supplying a booster current to the device under test when the controlled current shunt (Qs) is opened and current again flows through the device under test, thereby facilitating recharge of a parasitic capacitance associated with the device under test.

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