

METHOD AND SYSTEM FOR PROGRAMMING FPGAS ON PC-CARDS WITHOUT
ADDITIONAL HARDWARE

ABSTRACT

A method and a circuit arrangement are provided for programming hardware electronic circuits without manually accessing them. The circuit comprises an EEPROM (12) a FPGA (16) accessible via a computer bus and a MUX (14). A PROM (10) comprises control data for proper recognition of the FPGA together with logic usable for programming said EEPROM with an EEPROM-FPGA interface. The MUX selects either said PROM or said EEPROM or said FPGA for reading data from said devices, in order to properly connect said FPGA to said bus and to initialize a configuration of said FPGA with the contents comprised of said EEPROM. The FPGA is used to program the EEPROM with schema received from disk, the MUX is switched to read from the EEPROM and feed the developed schema into the EPGA.

FIG 1