

## SEMICONDUCTOR DEVICE HOUSING TRAY

### ABSTRACT

An improved general-purpose semiconductor device housing tray having a simple construction which is hardly deformed (warped or the like), and suitable for testing front and back sides of the semiconductor device, and capable of handling many kinds of semiconductor devices, wherein flat type semiconductor devices are arranged on and contained in the tray, and a plurality of protrusions contacting the outer periphery of the semiconductor device are integrated with and provided on a supporting surface for supporting the semiconductor devices so as to surround the semiconductor device.

Figure. 3