PLASMA DRIVER CIRCUIT CAPABLE OF SUPPRESSING SURGE CURRENT OF PLASMA DISPLAY CHANNEL

ABSTRACT OF THE DISCLOSURE

In a plasma addressed display device, rush currents through the respective channels during discharge starting time period are suppressed. The said device comprises : plurality of parallel arranged data electrodes on a first substrate; plurality of discharge electrodes on a second substrate each having an anode/cathode electrode parallel arranged and perpendicular to data electrodes; liquid crystal layer between the said substrates; plurality of discharge channels between the liquid crystal layer and second substrate, and containing ionized gas, each discharge channel having at least a pair of anode/cathode electrode; plurality of switching means for sequentially selecting discharge channels, the switching means being provided in one-to-one correspondence with discharge channels; current supplying means for supplying drive current to discharge channels through switching means; and control means for controlling current supplying means in synchronization with switching timing of switching means, the drive current supplied intermittently to discharge channels by control means.

Figure 4