

10

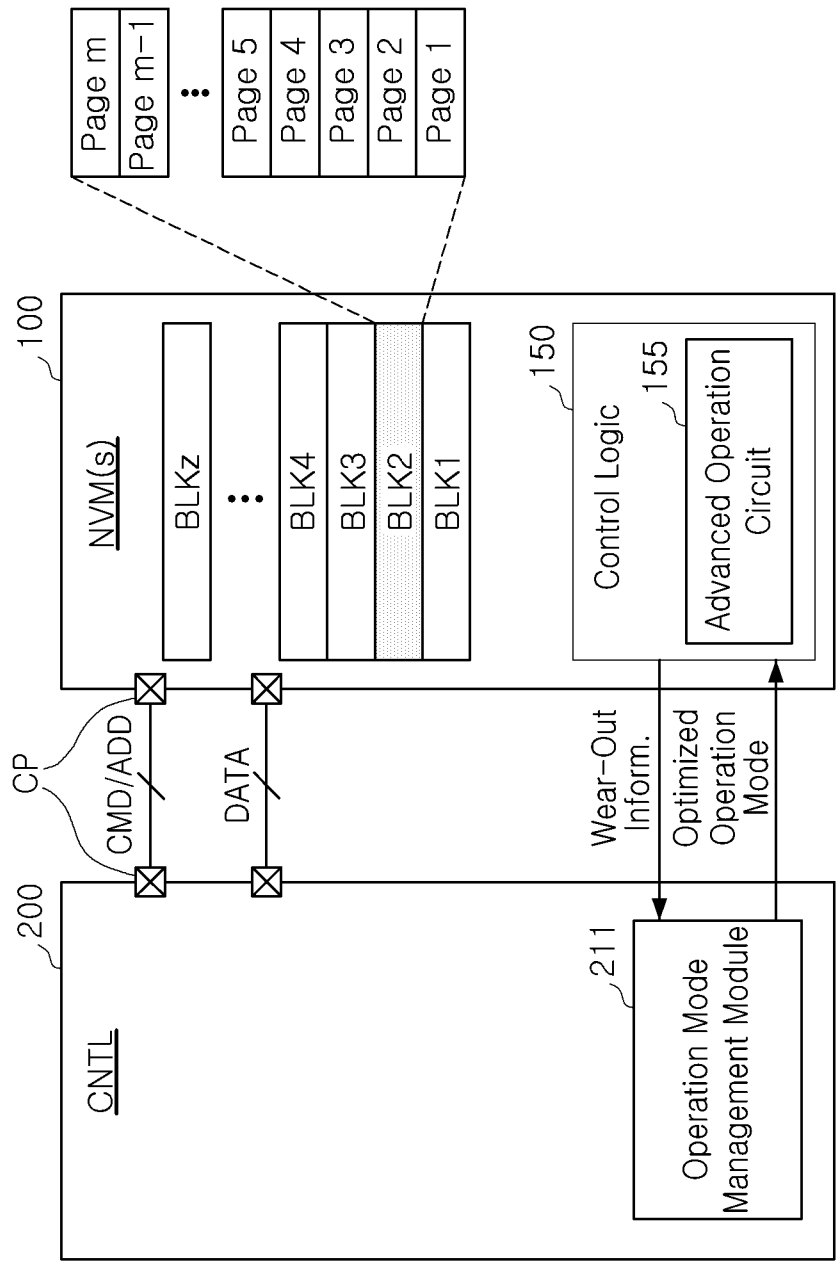


FIG. 1

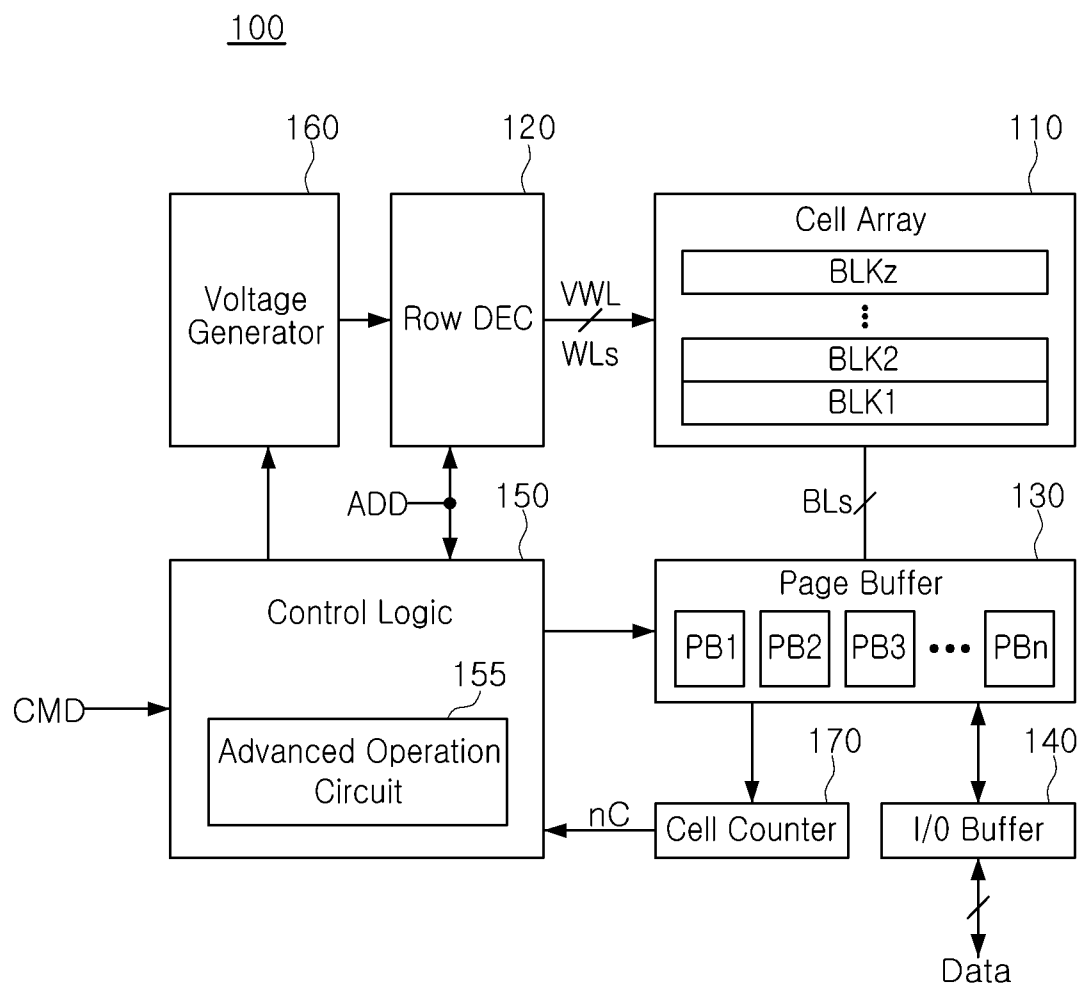


FIG. 2

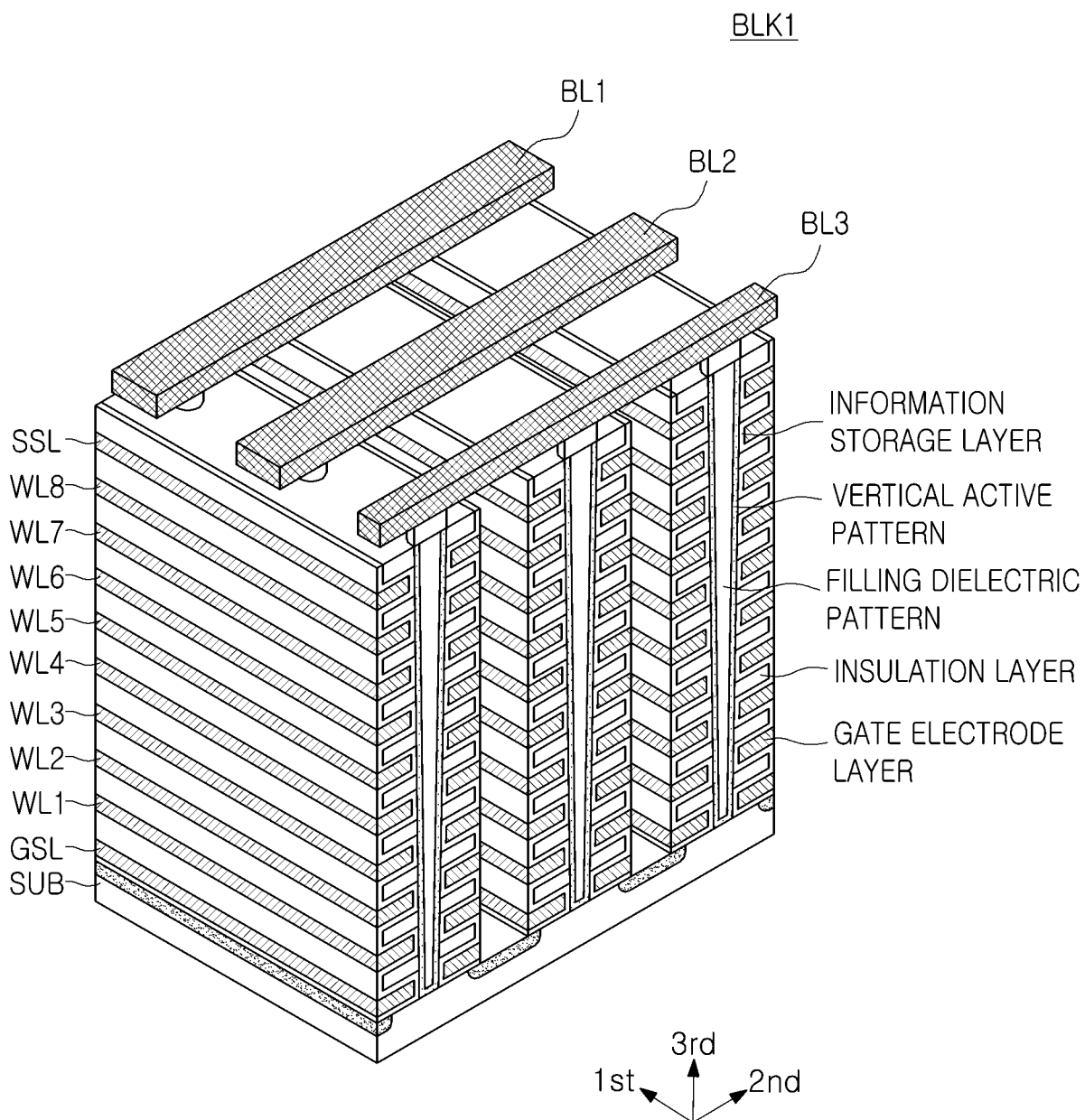


FIG. 3A

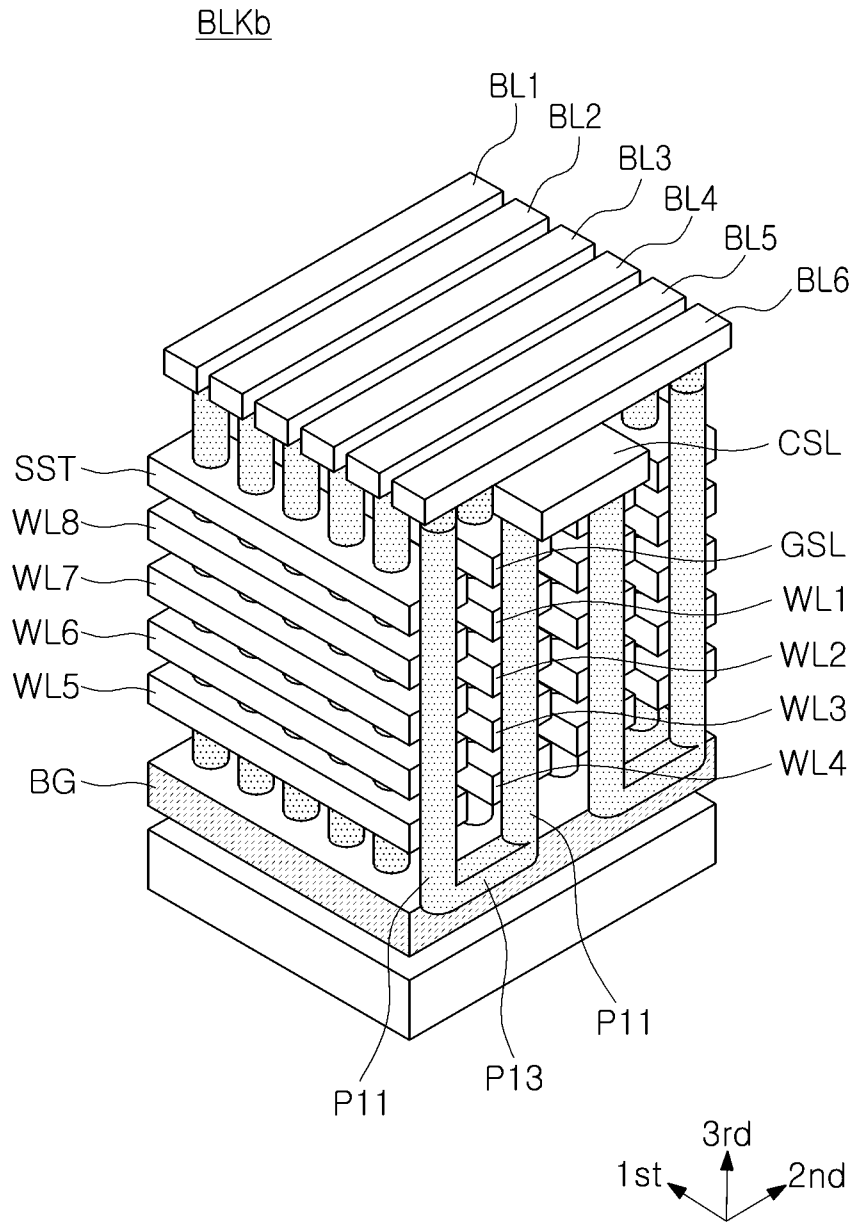


FIG. 3B

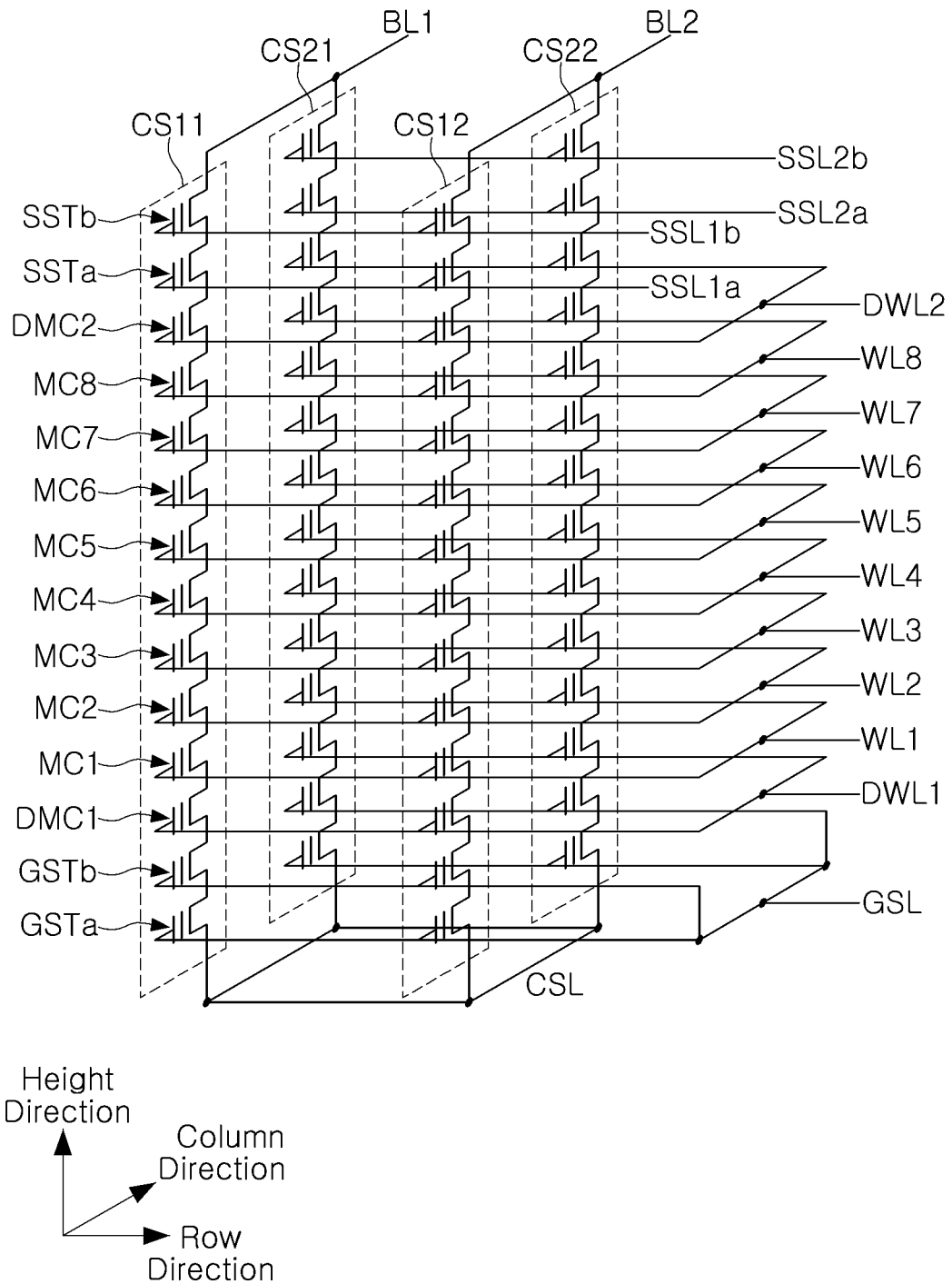


FIG. 4

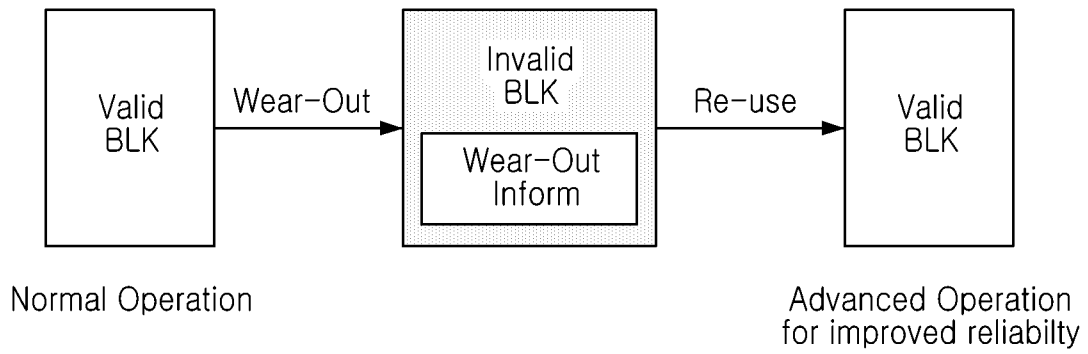


FIG. 5

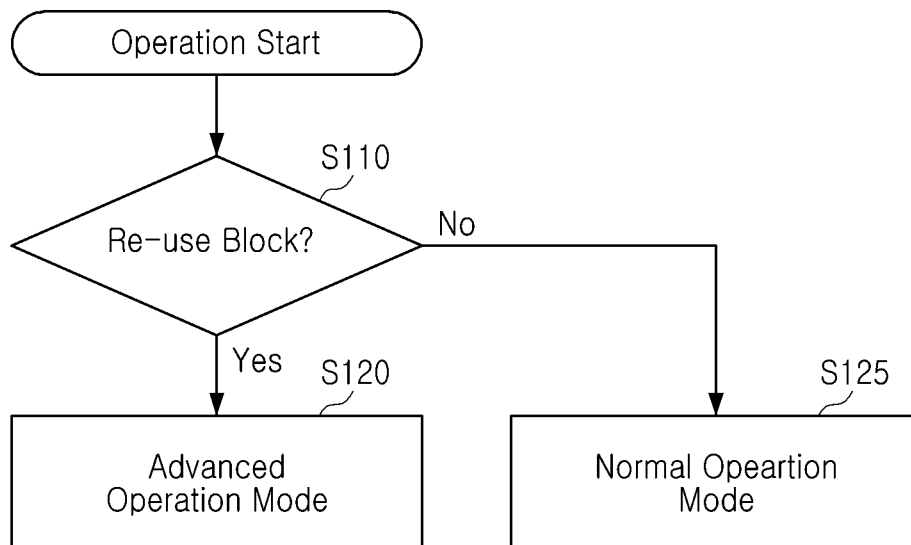


FIG. 6

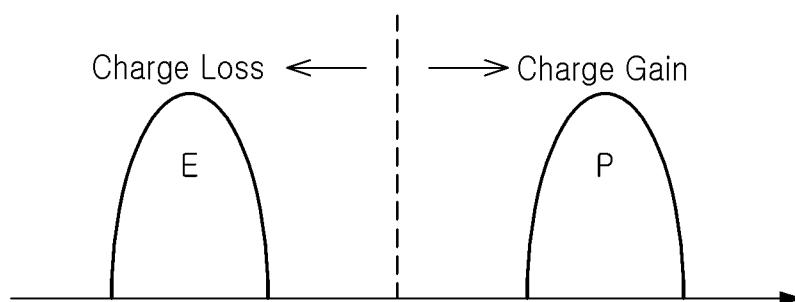


FIG. 7A

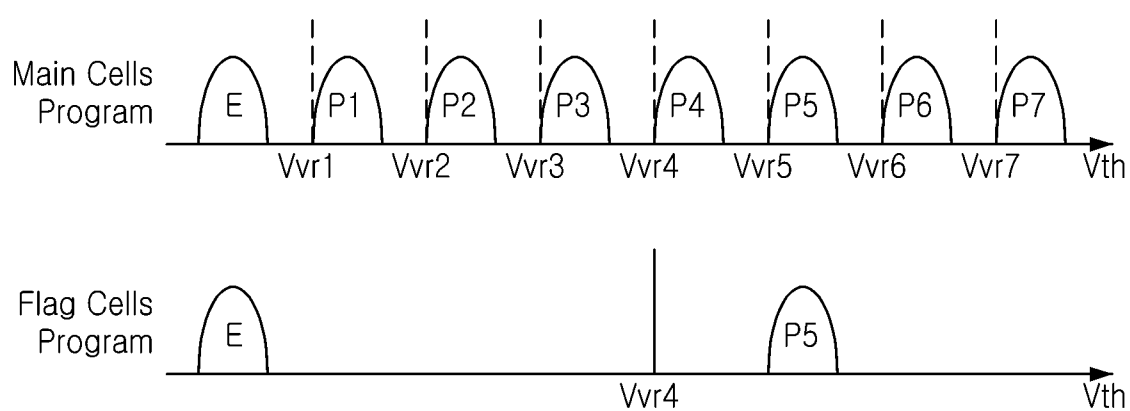


FIG. 7B

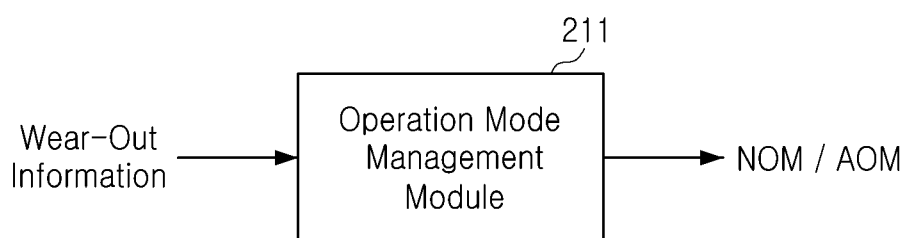


FIG. 8A

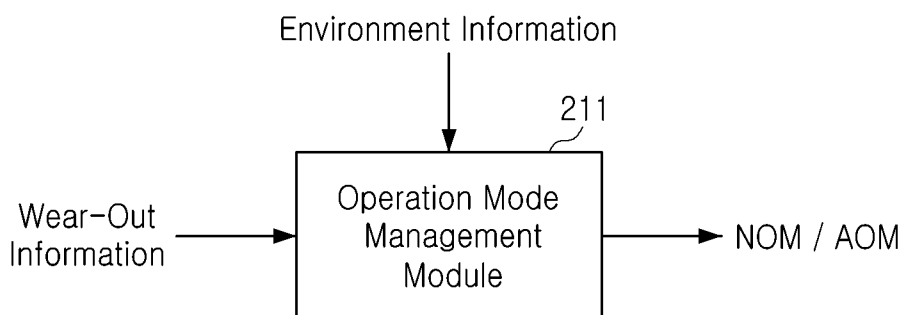


FIG. 8B

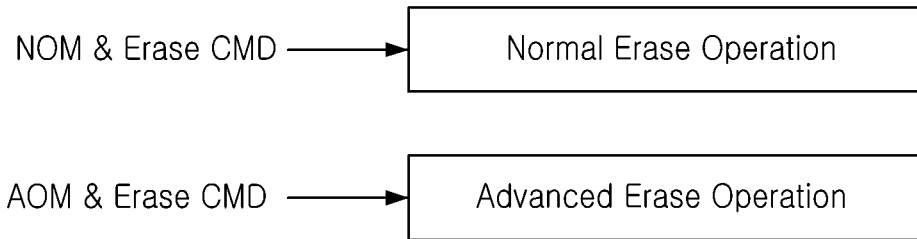


FIG. 9A

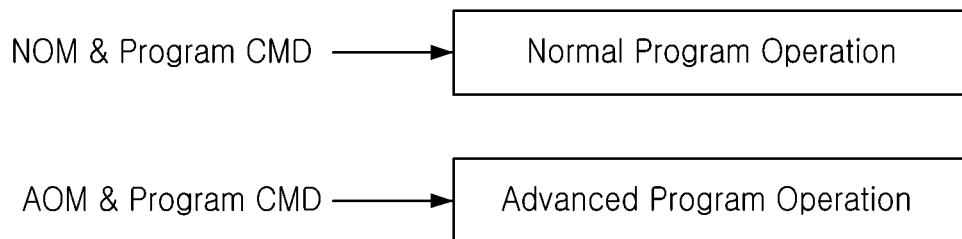


FIG. 9B

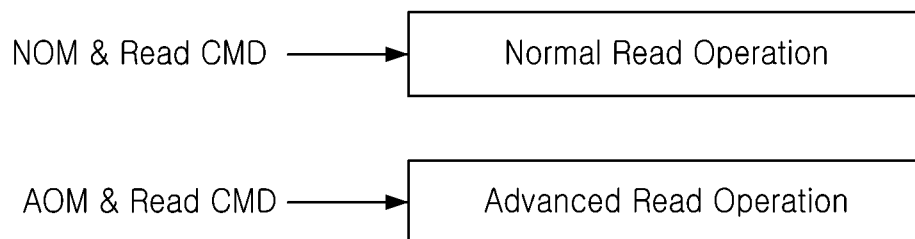


FIG. 9C

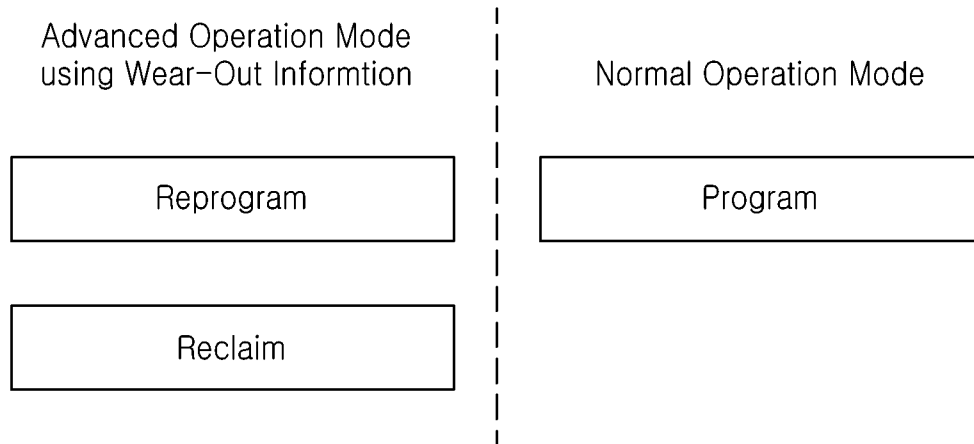


FIG. 10

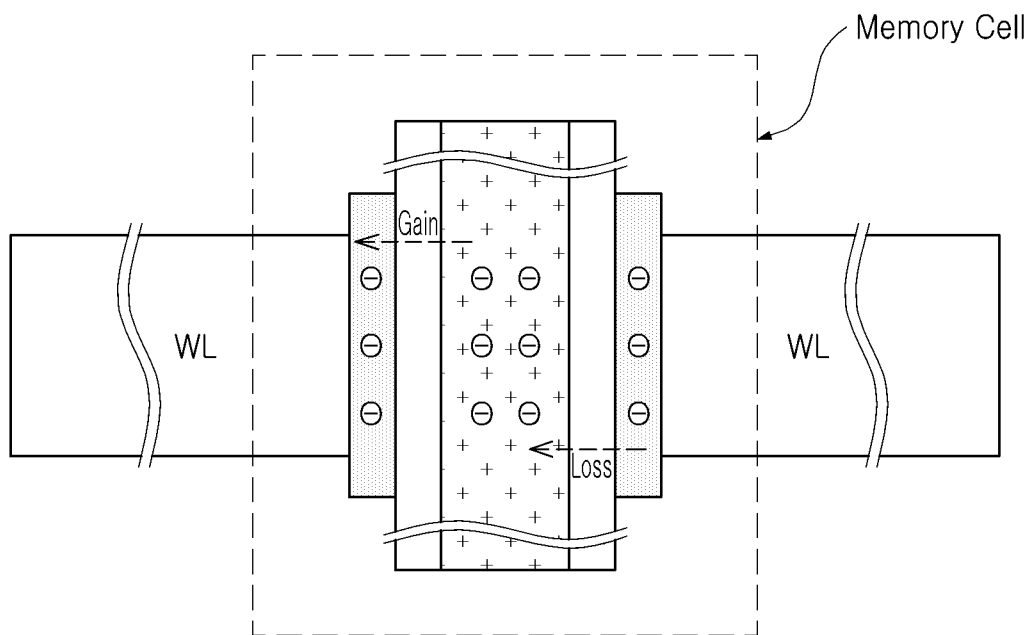


FIG. 11A

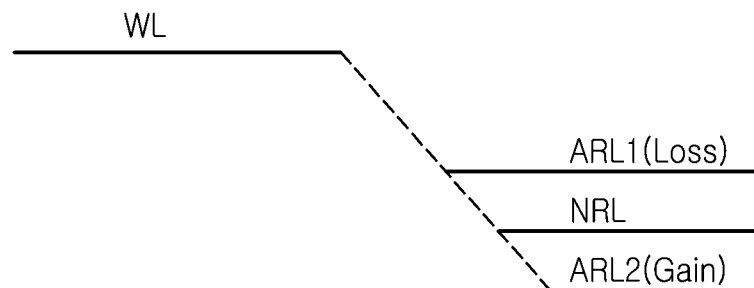


FIG. 11B

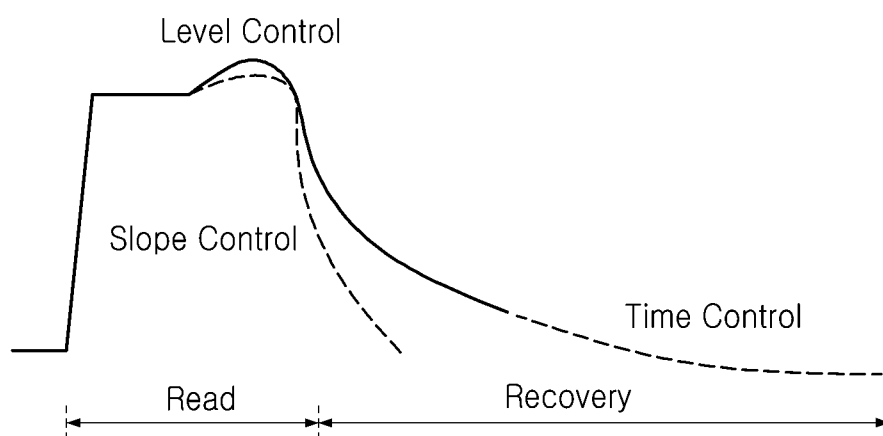


FIG. 12

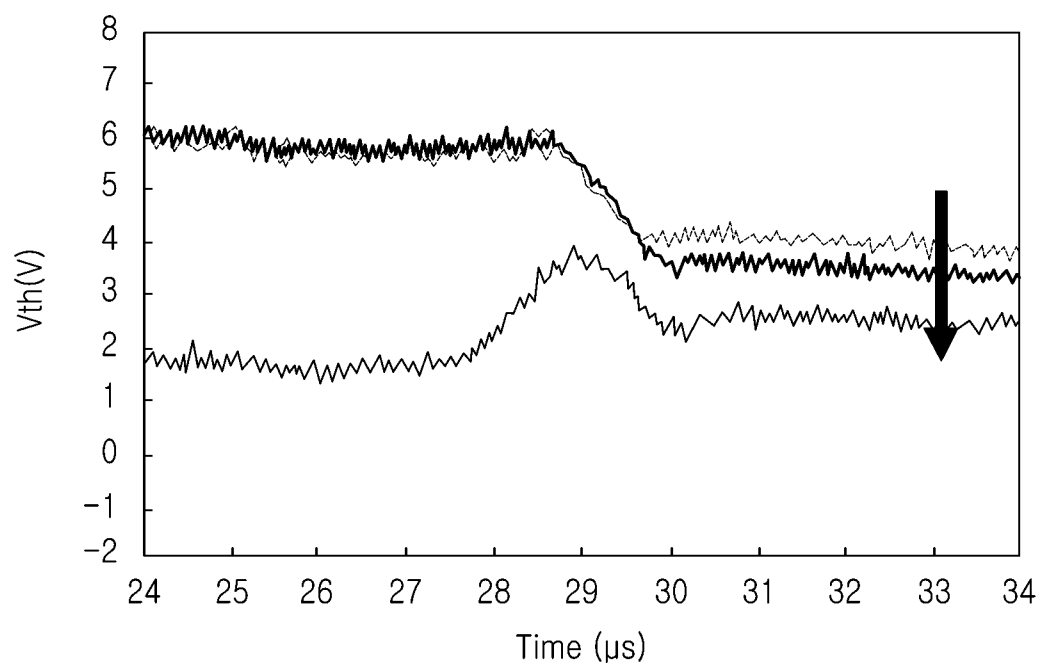


FIG. 13A

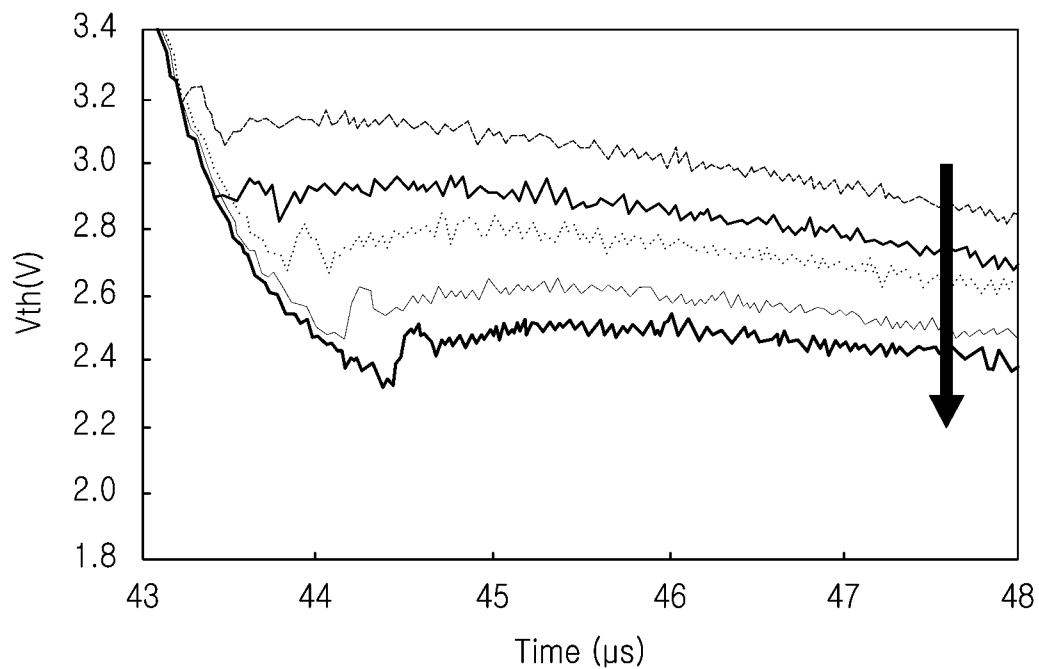


FIG. 13B

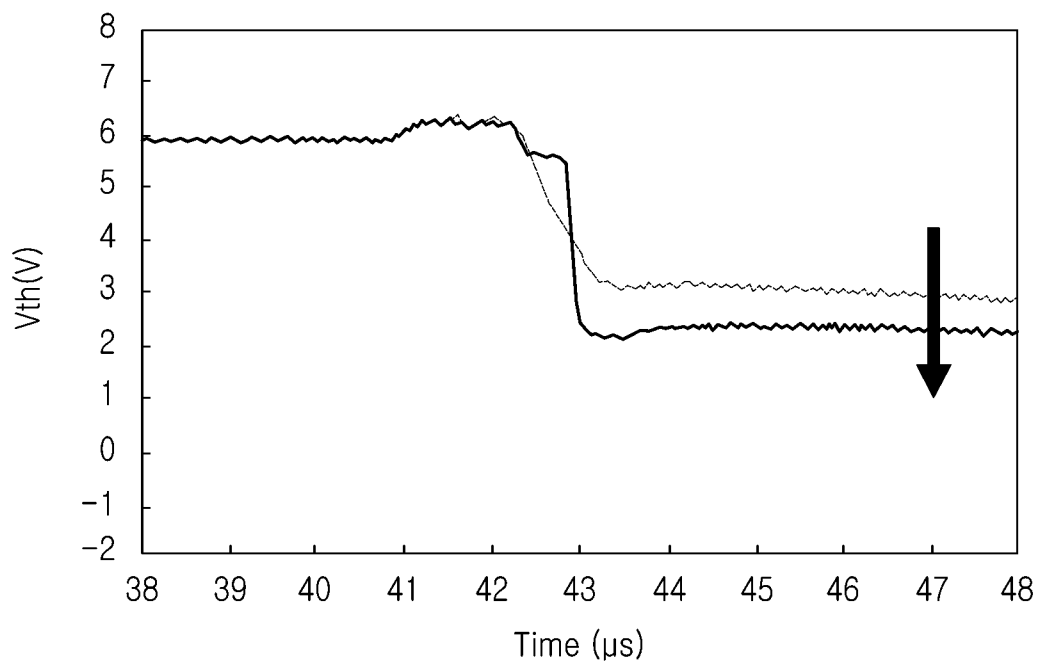


FIG. 13C

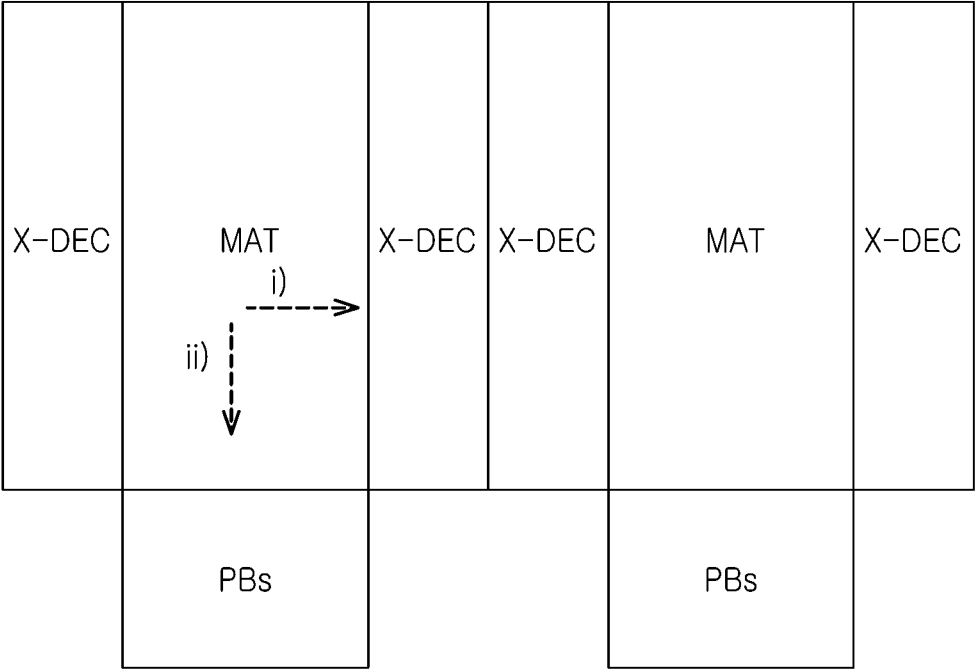


FIG. 14

Write	Time	Path	LV
LV1	LV1	...	...
LV2	LV2	LV3	LV3
LV3	LV3	LV2	LV2
...	...	LV1	LV1

FIG. 15

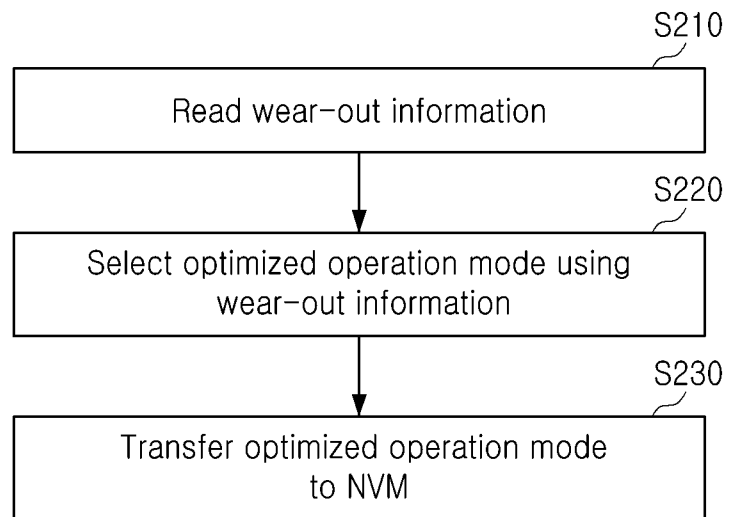


FIG. 16

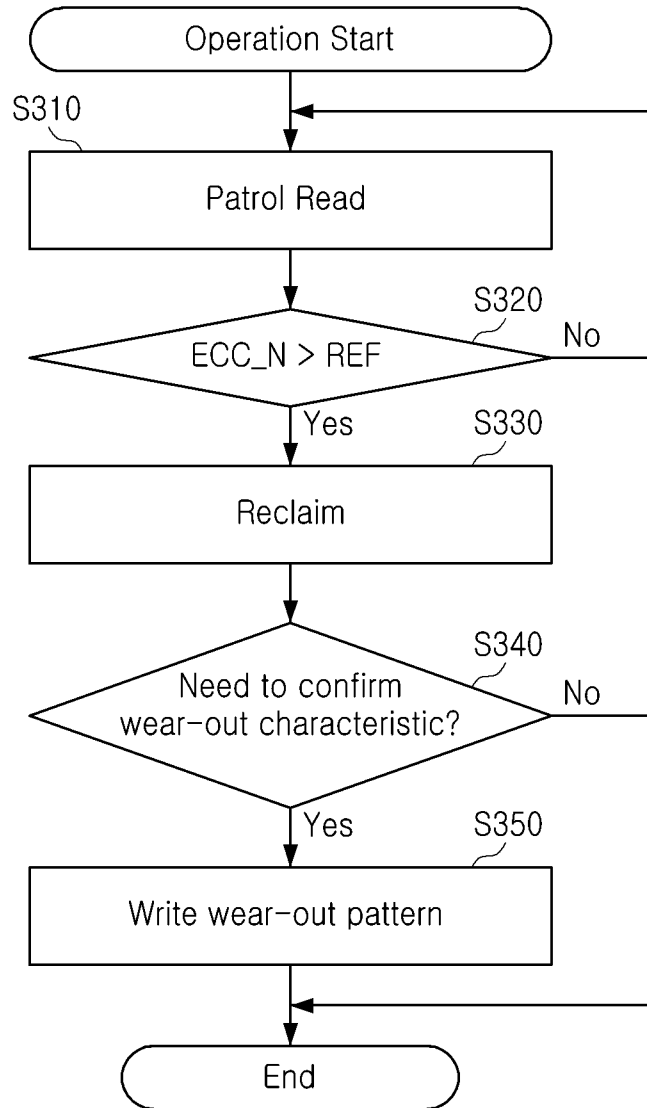


FIG. 17A

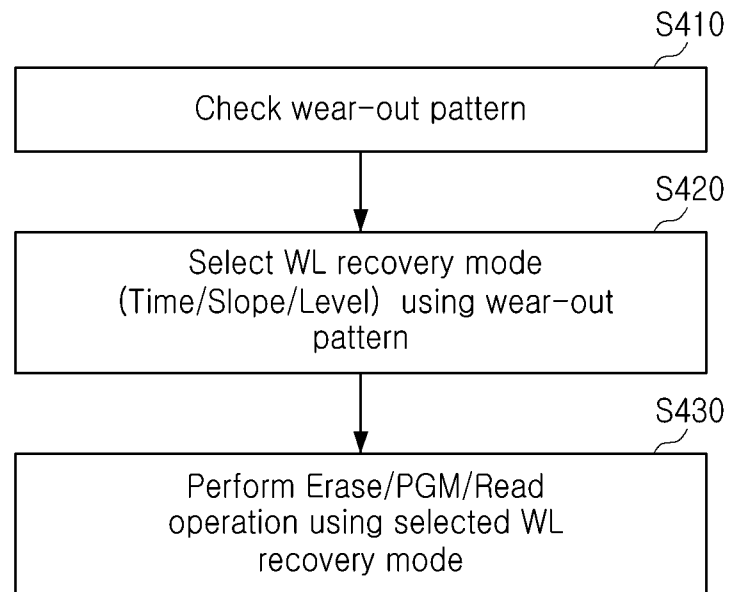
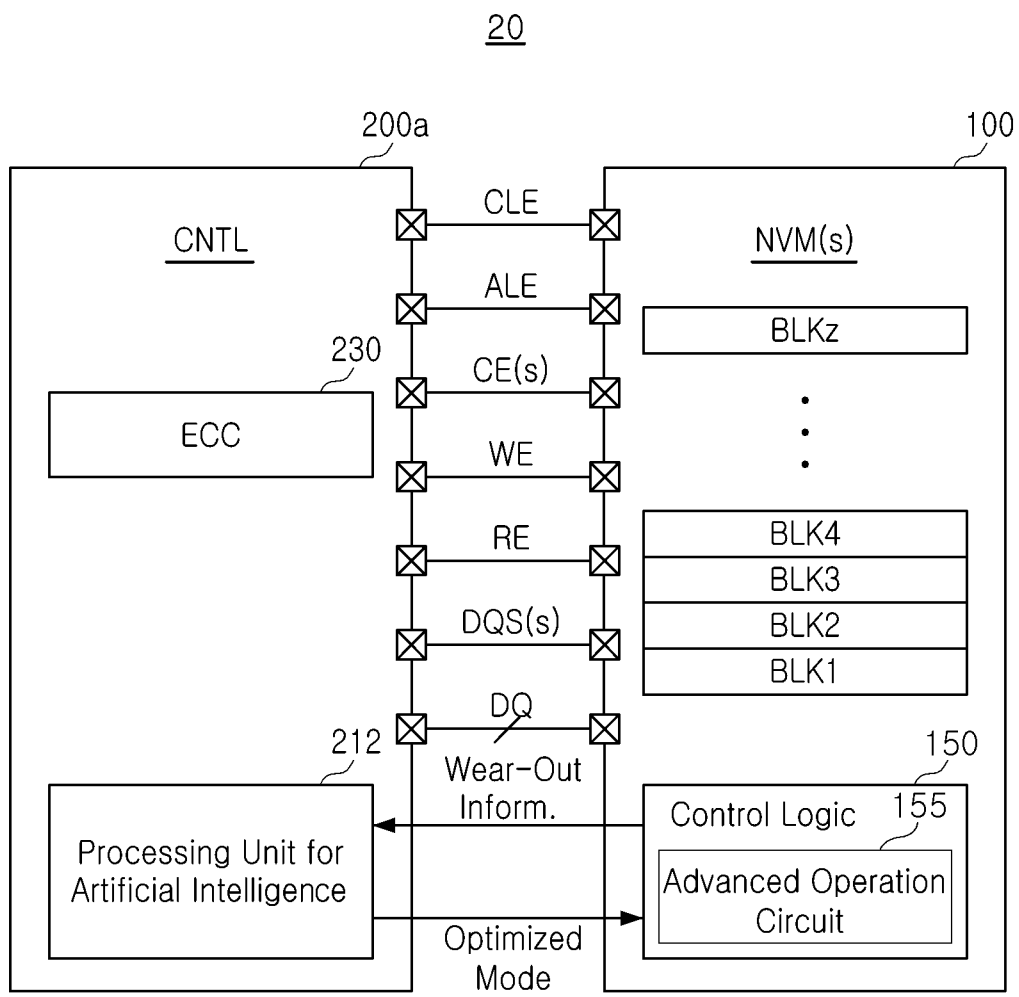


FIG. 17B



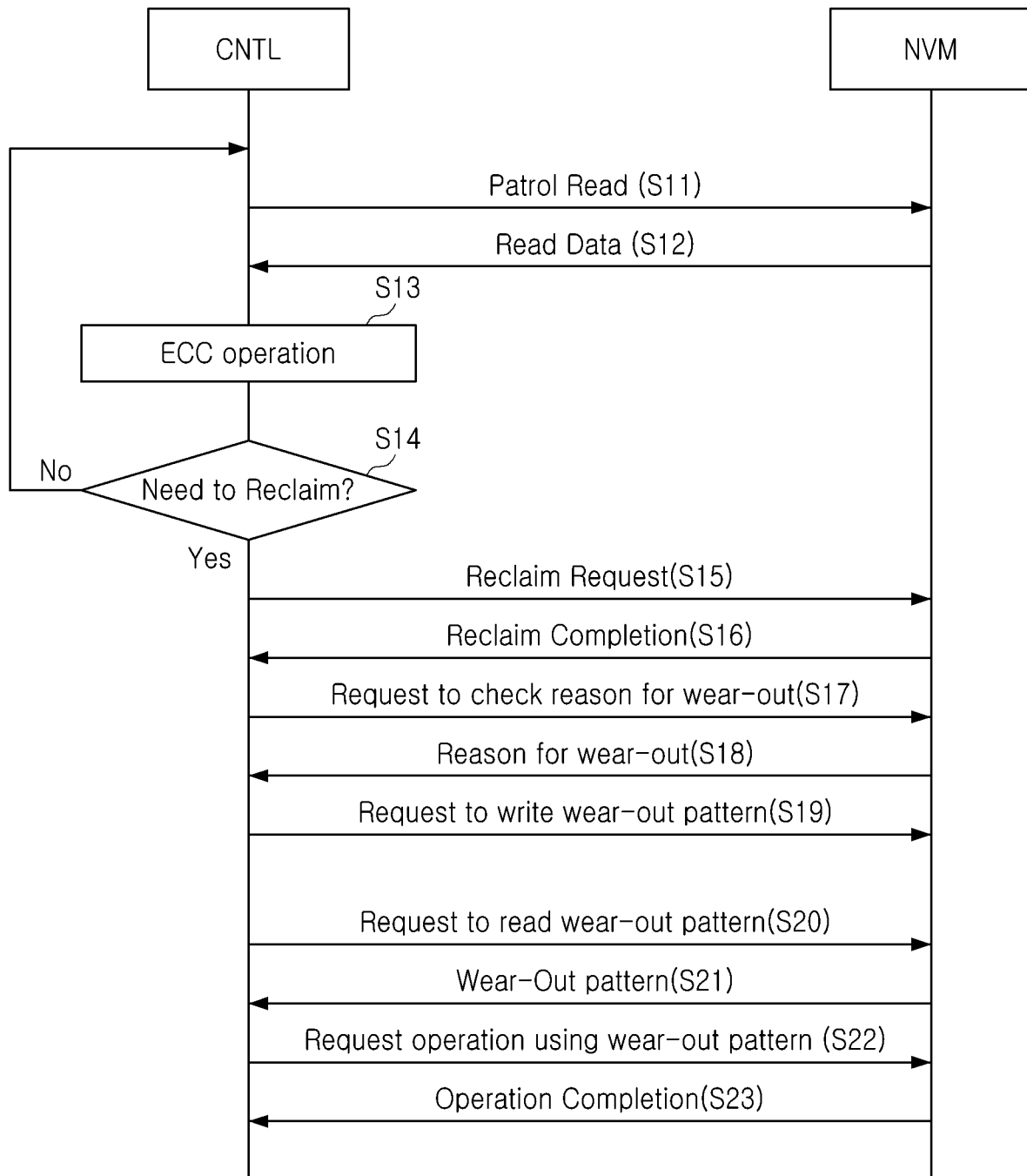


FIG. 19

