FIG. 1

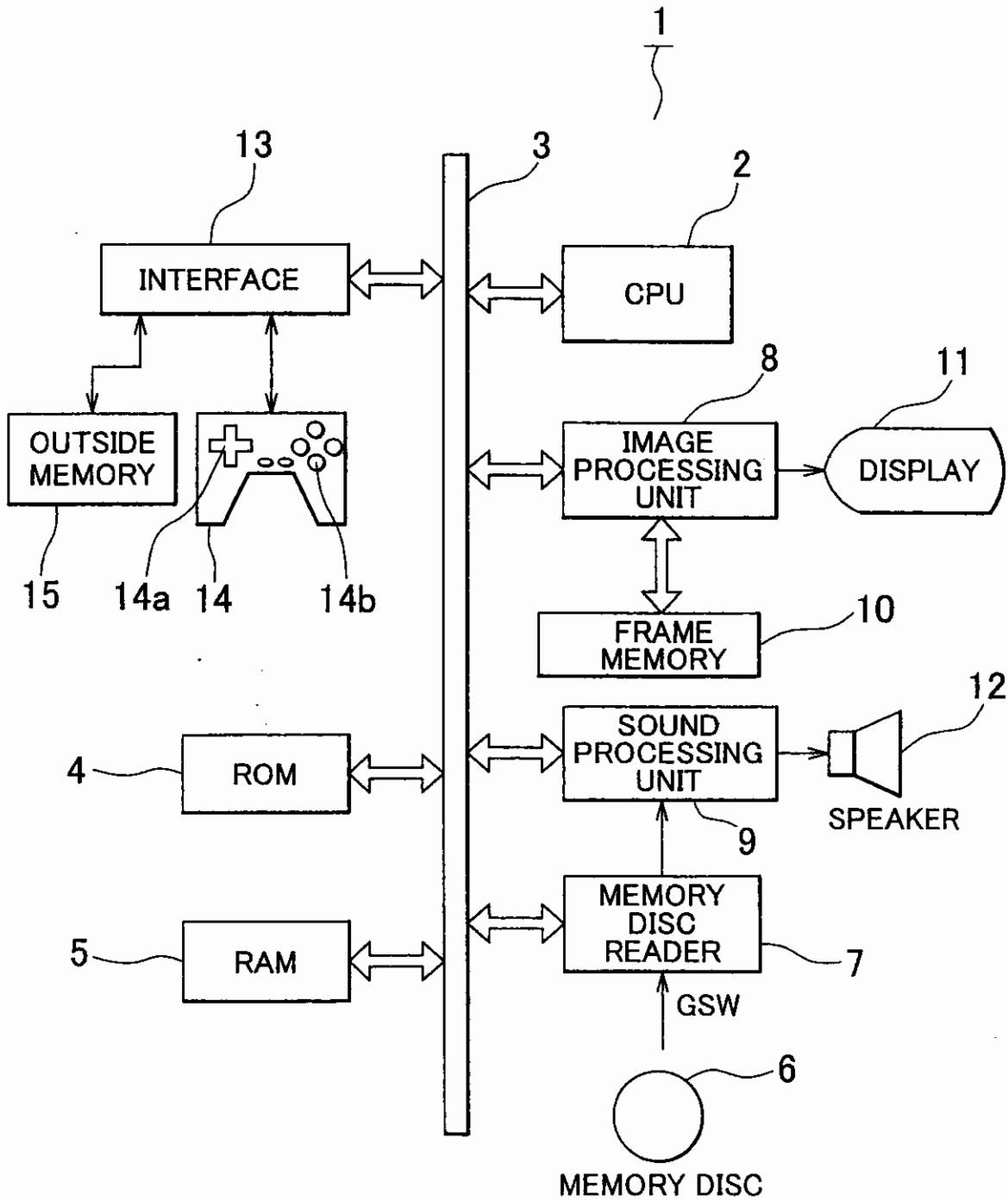

1
13
3
2
INTERFACE
CPU
8
11
OUTSIDE MEMORY
IMAGE PROCESSING UNIT
DISPLAY
15
14a
14
14b
FRAME MEMORY
10
12
4
ROM
SOUND PROCESSING UNIT
SPEAKER
9
MEMORY DISC READER
5
RAM
7
GSW
6
MEMORY DISC

FIG. 2

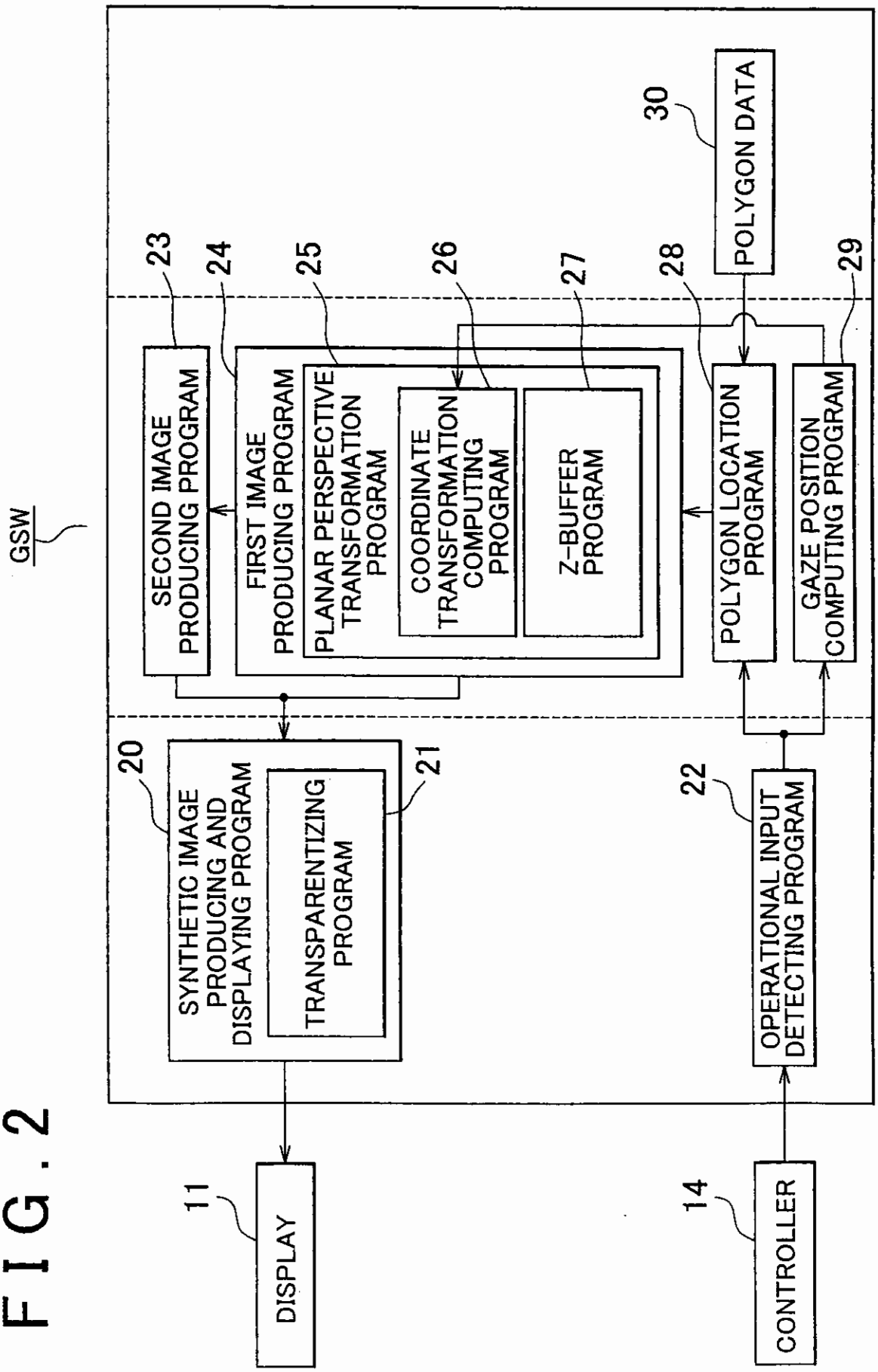

GSW
SECOND IMAGE PRODUCING PROGRAM
23
FIRST IMAGE PRODUCING PROGRAM
24
PLANAR PERSPECTIVE TRANSFORMATION PROGRAM
25
COORDINATE TRANSFORMATION COMPUTING PROGRAM
26
Z-BUFFER PROGRAM
27
POLYGON LOCATION PROGRAM
28
GAZE POSITION COMPUTING PROGRAM
29
POLYGON DATA
30
SYNTHETIC IMAGE PRODUCING AND DISPLAYING PROGRAM
20
TRANSPARENTIZING PROGRAM
21
OPERATIONAL INPUT DETECTING PROGRAM
22
DISPLAY
11
CONTROLLER
14

FIG.3

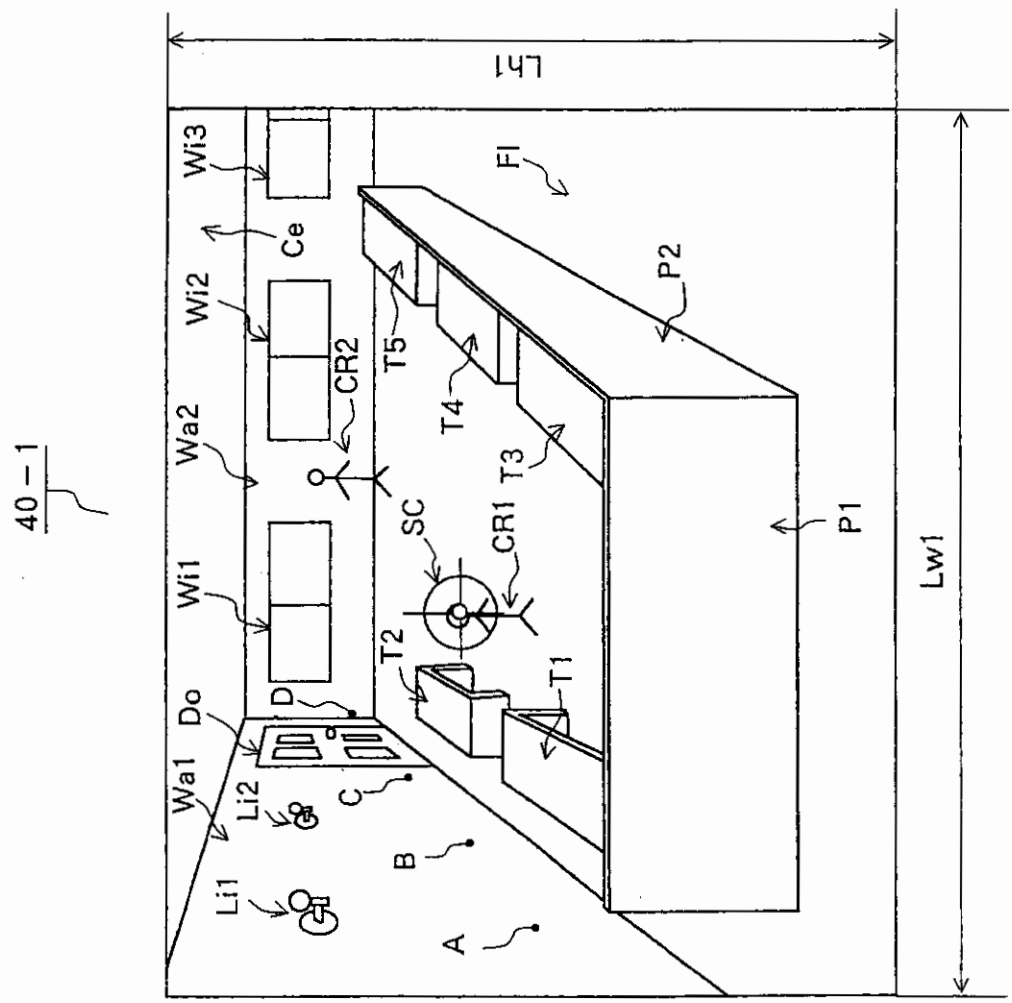

40－1
Ll1
Wi3
Ce
Wi2
CR2
Wa2
Wi1
Do
D
Wa1
Li2
Li1
C
B
A
T5
T4
T3
T2
T1
SC
CR1
Fl
P2
P1
Lw1

FIG.4

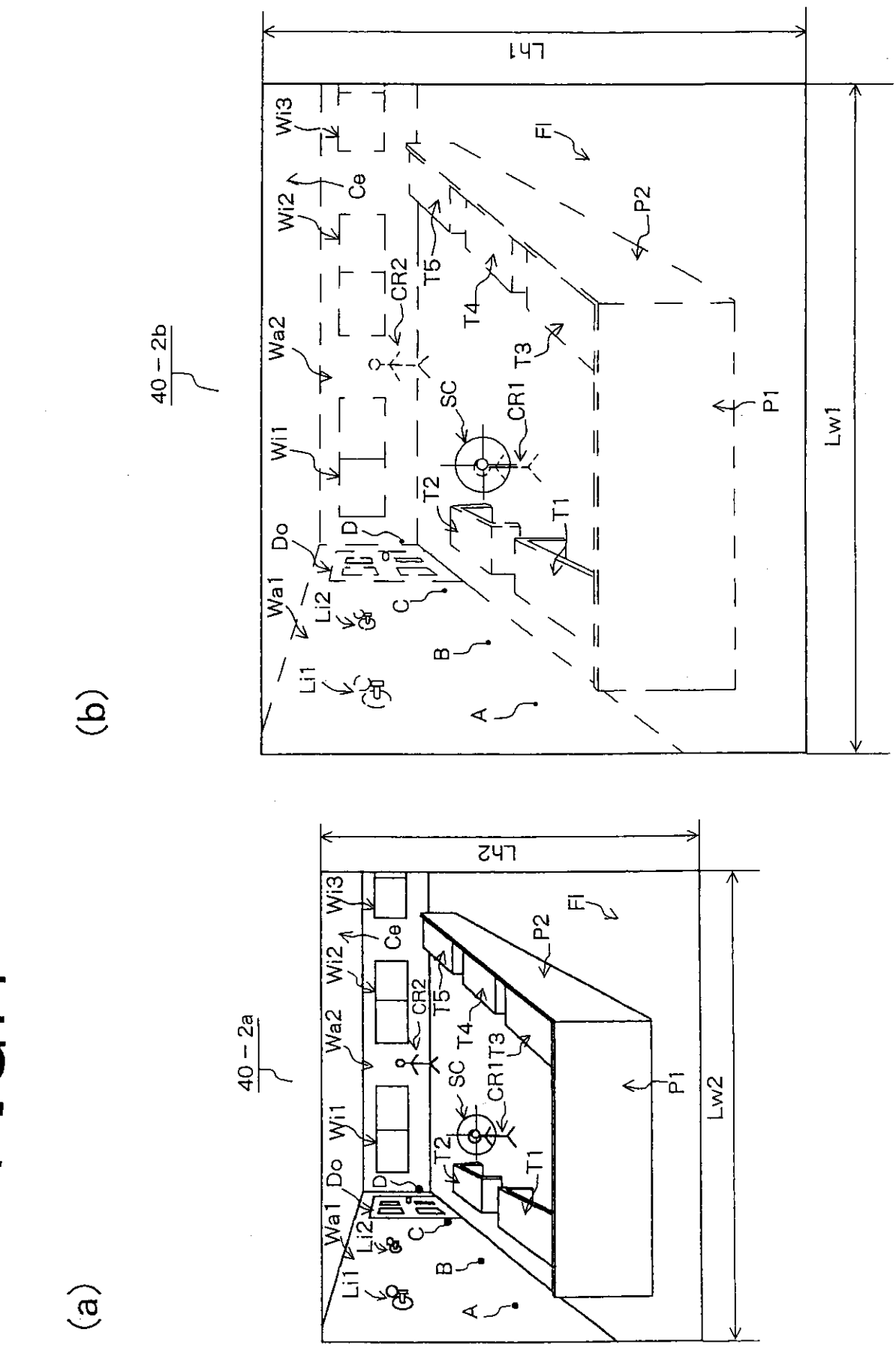
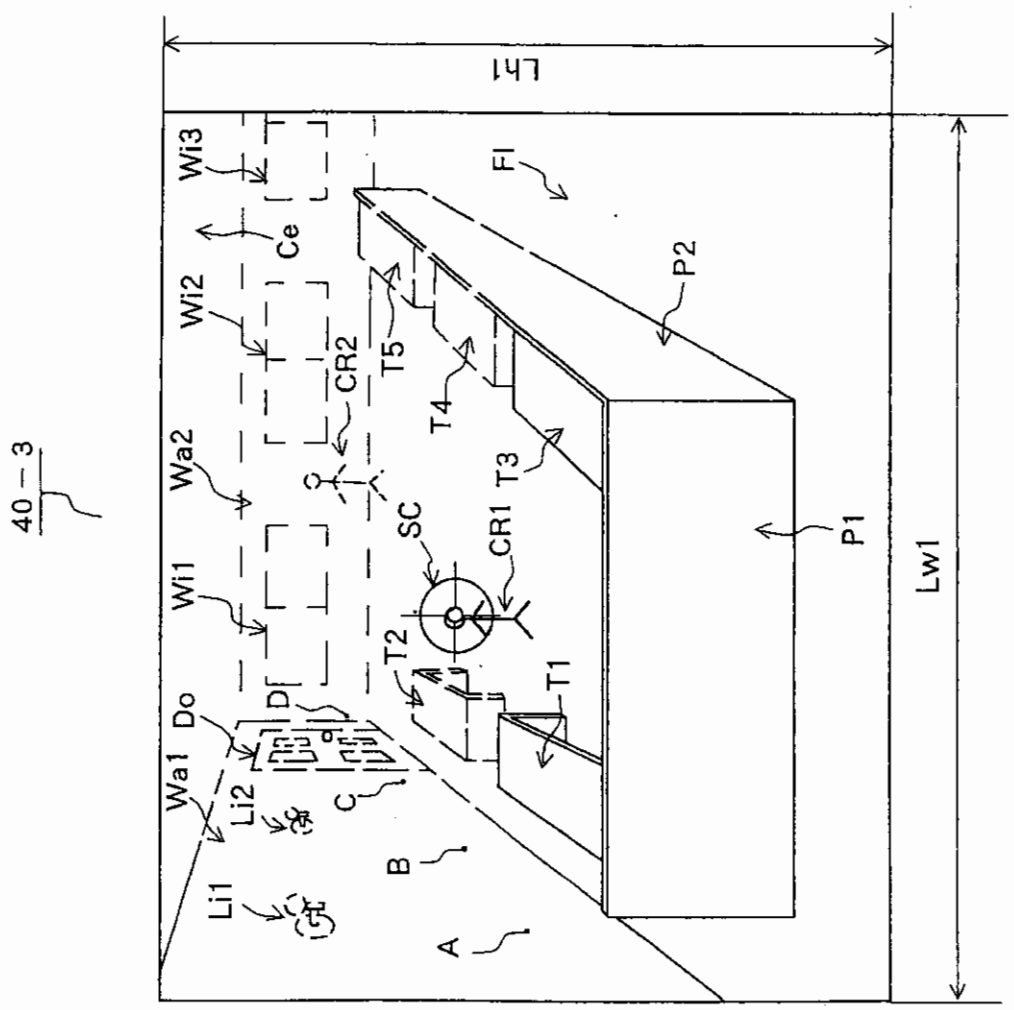

(a)
40－2a
Lw2
Ll2
Wi3
Ce
Wi2
Wa2
Wi1
Do
Wa1
Li2
Li1
D
d
C
B
A
CR2
SC
CR1
T1
T2
T3
T4
T5
P1
P2
Fl
(b)
40－2b
Lw1
Ll1

FIG.5


40-3
Ll
Wi3
Ce
Wi2
CR2
Wa2
Wi1
Do
D
Wa1
Li2
Li1
C
B
A
SC
CR1
T2
T1
T3
T4
T5
Fl
P2
P1
Lw1