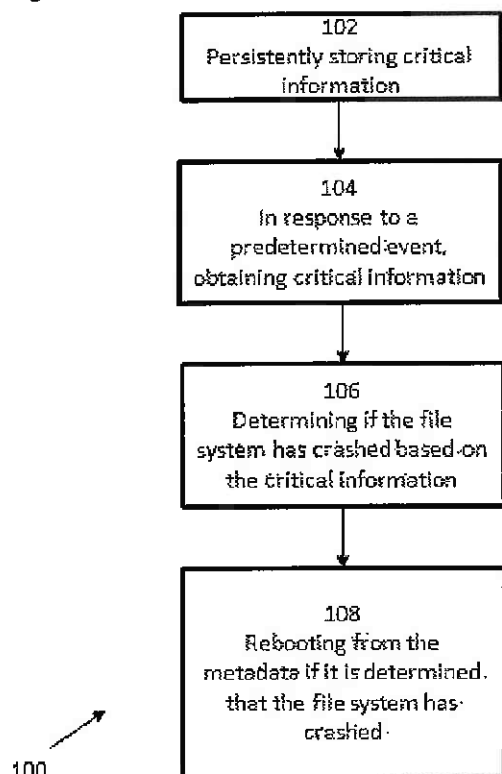
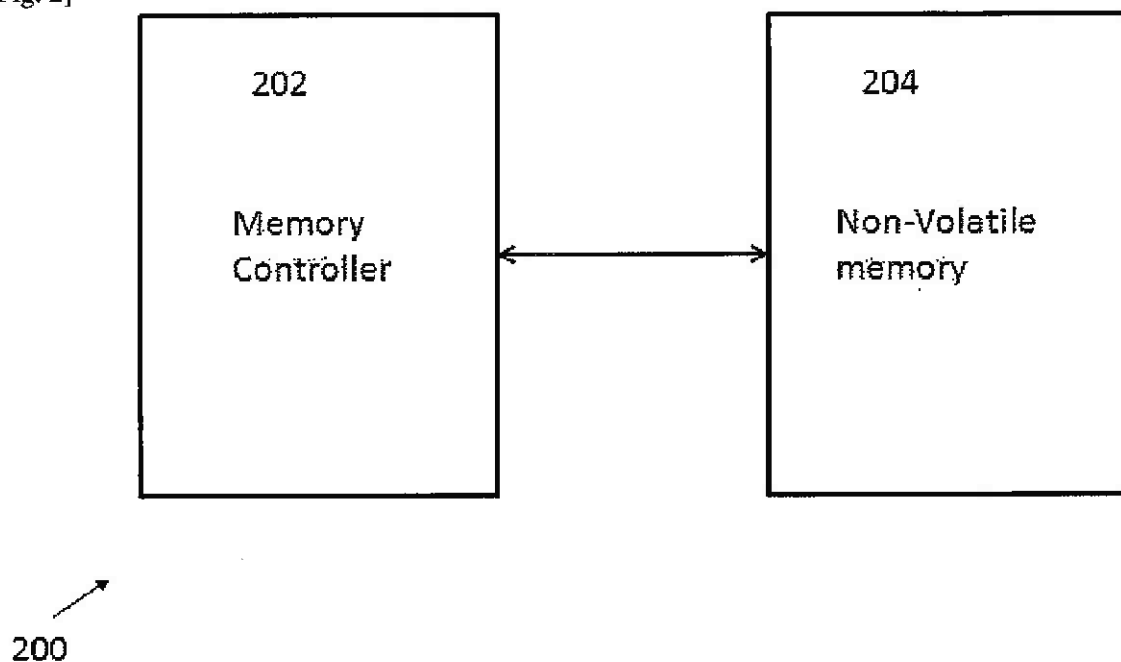


[Fig. 1]



[Fig. 2]



[Fig. 3]

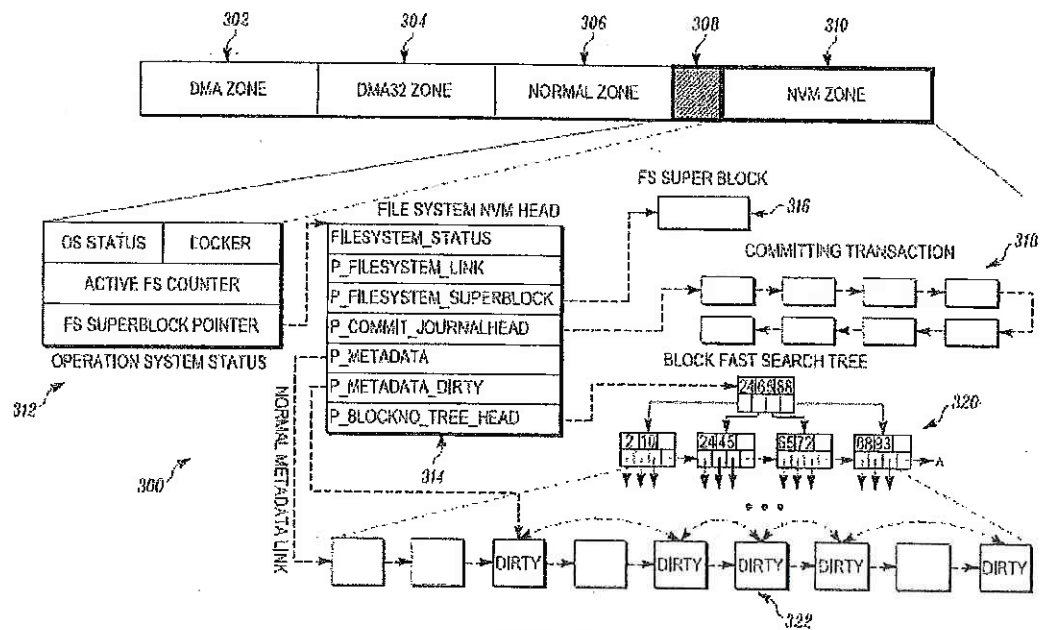


FIG. 3

[Fig. 4]

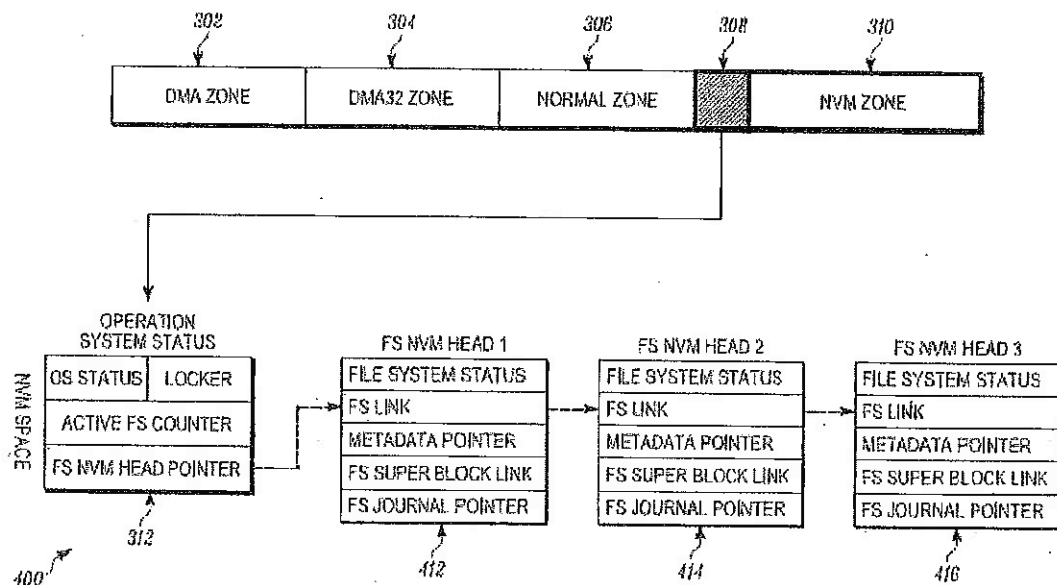


FIG. 4

[Fig. 5]

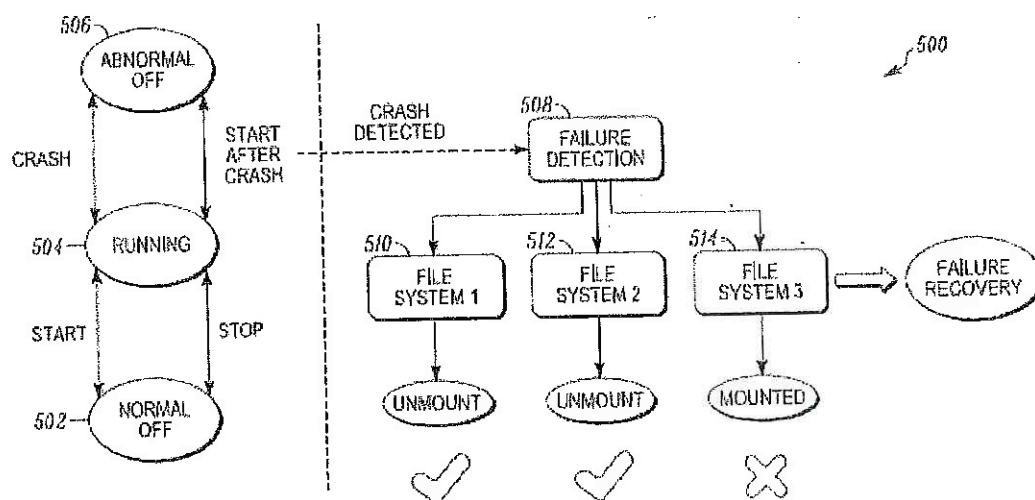


FIG. 5

[Fig. 6]

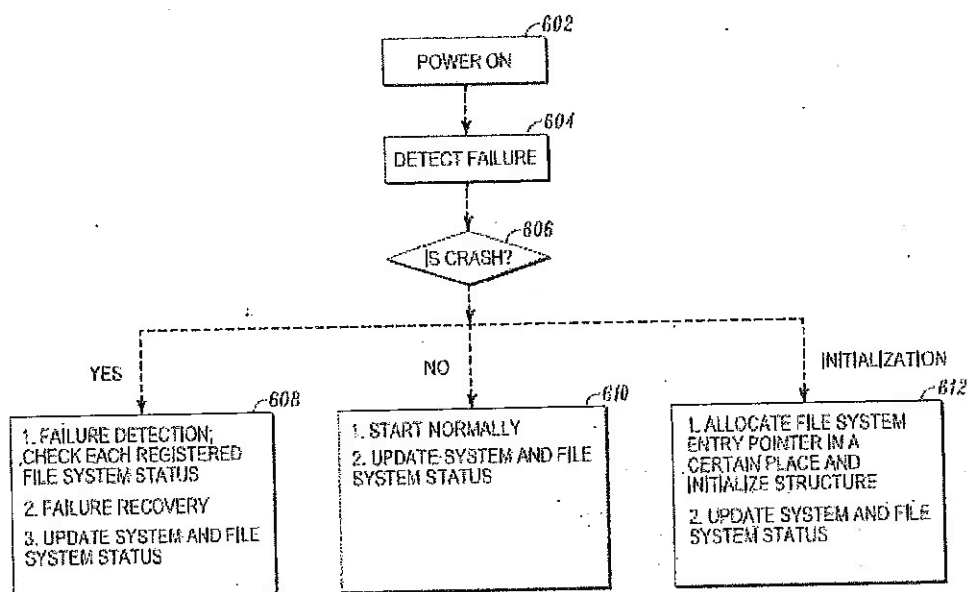


FIG. 6

[Fig. 7]

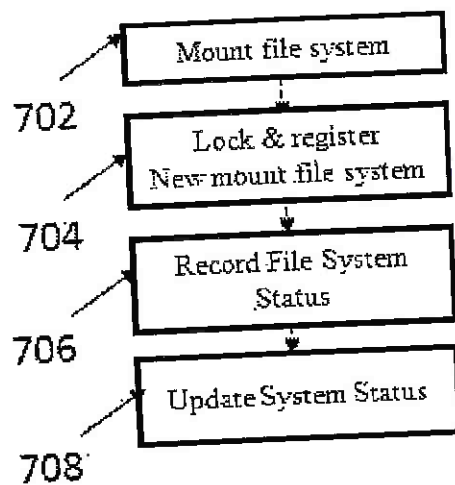


FIG. 7

[Fig. 8]

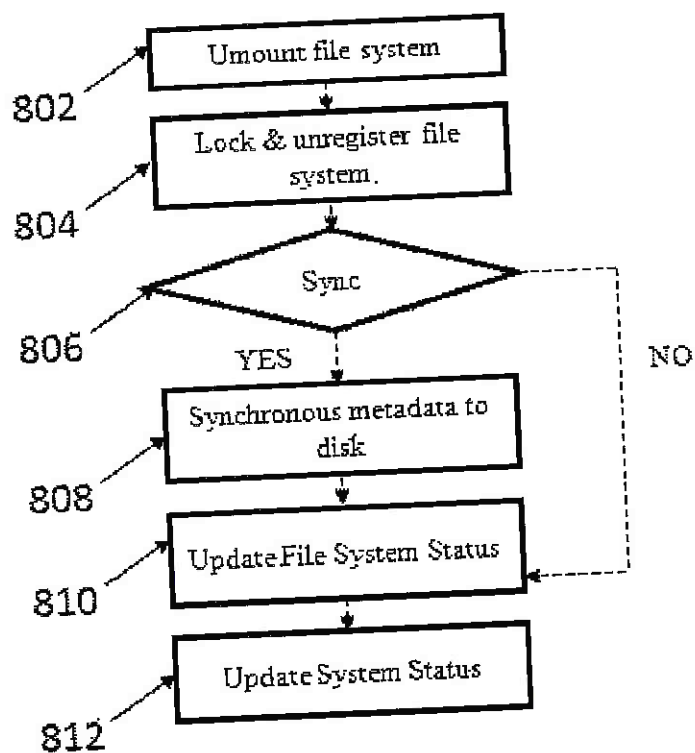


FIG. 8

[Fig. 9]

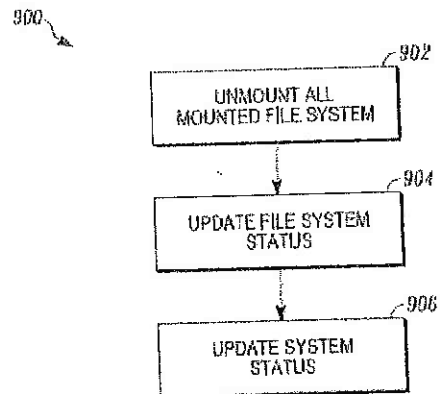


FIG. 9

[Fig. 10]

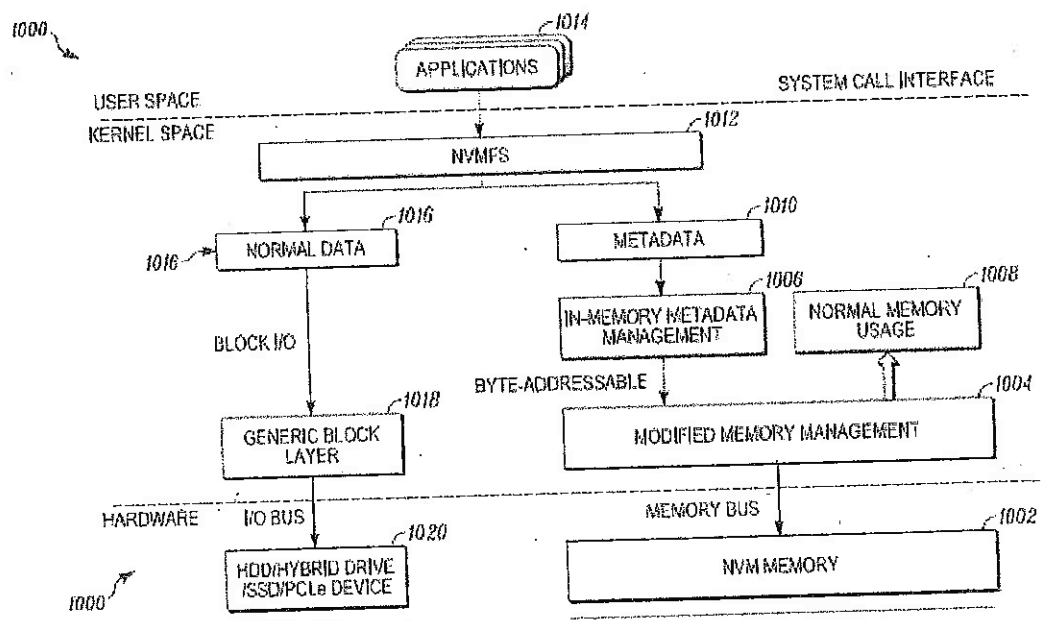


FIG. 10

[Fig. 11]

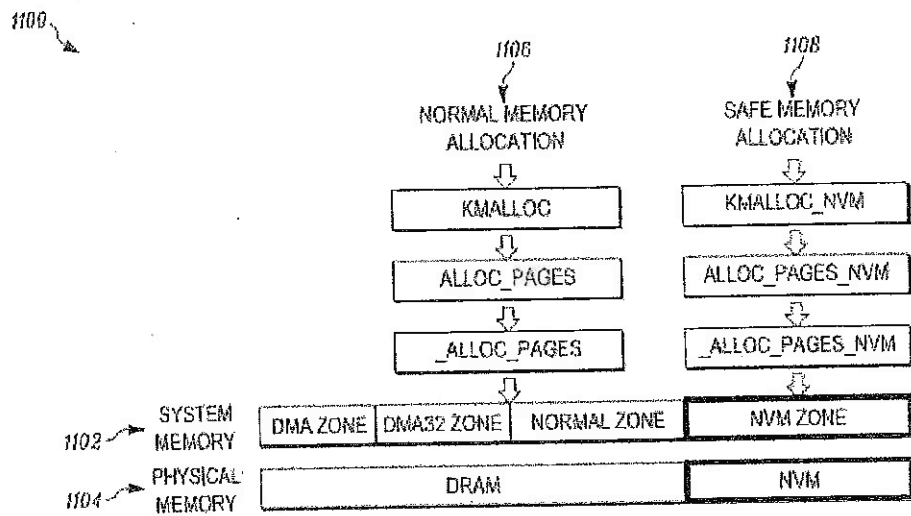


FIG. 11

[Fig. 12]

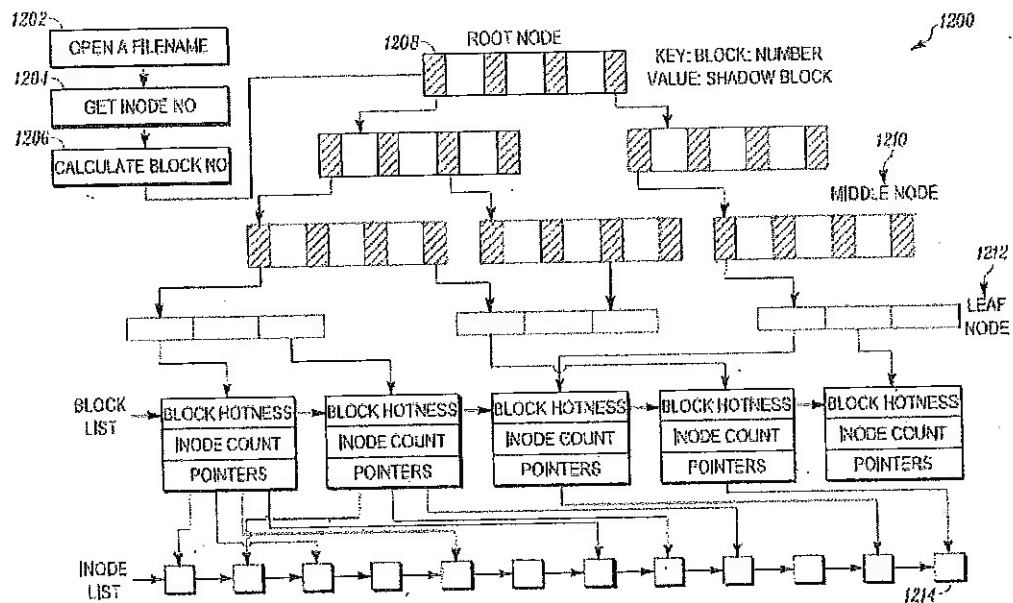


FIG. 12

[Fig. 13]

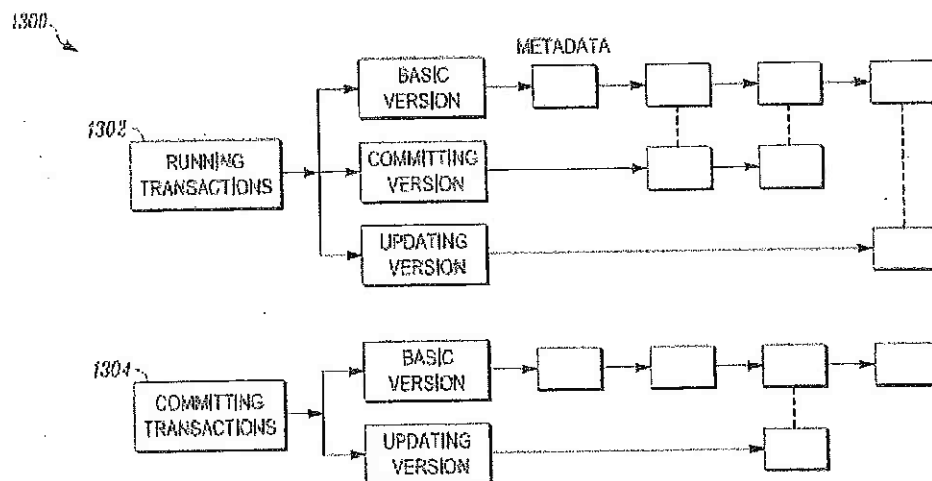


FIG. 13

[Fig. 14]

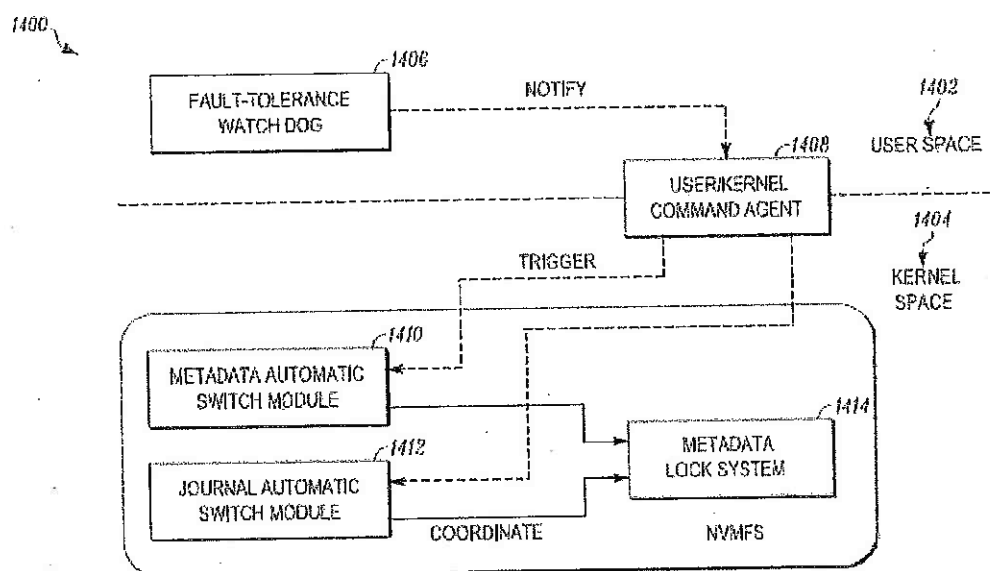


FIG. 14

[Fig. 15]

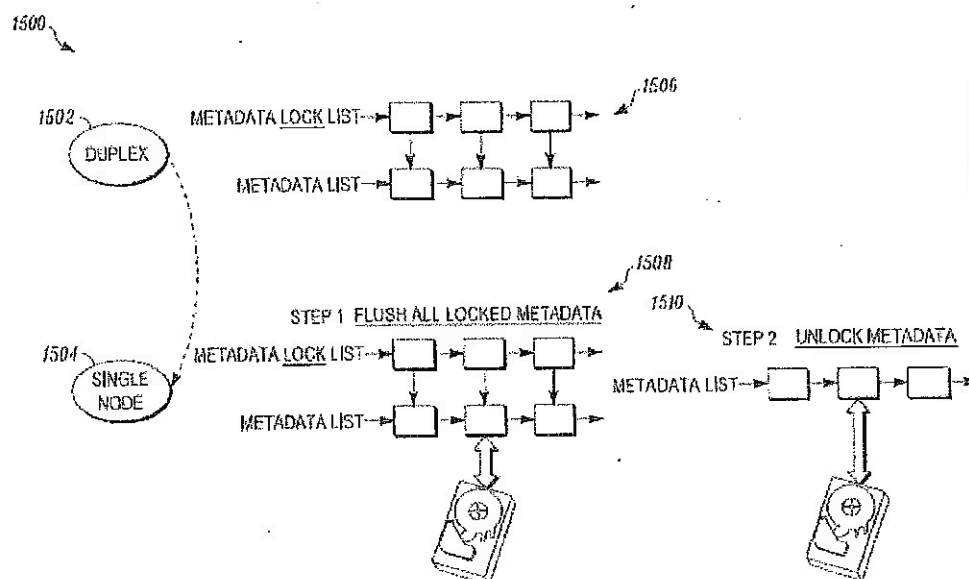


FIG. 15

[Fig. 16]

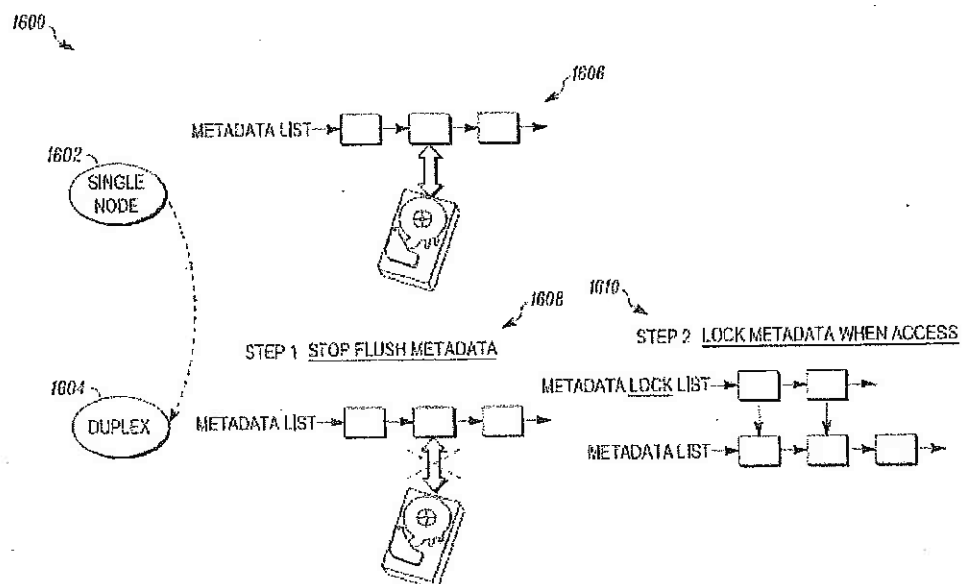


FIG. 16

[Fig. 17]

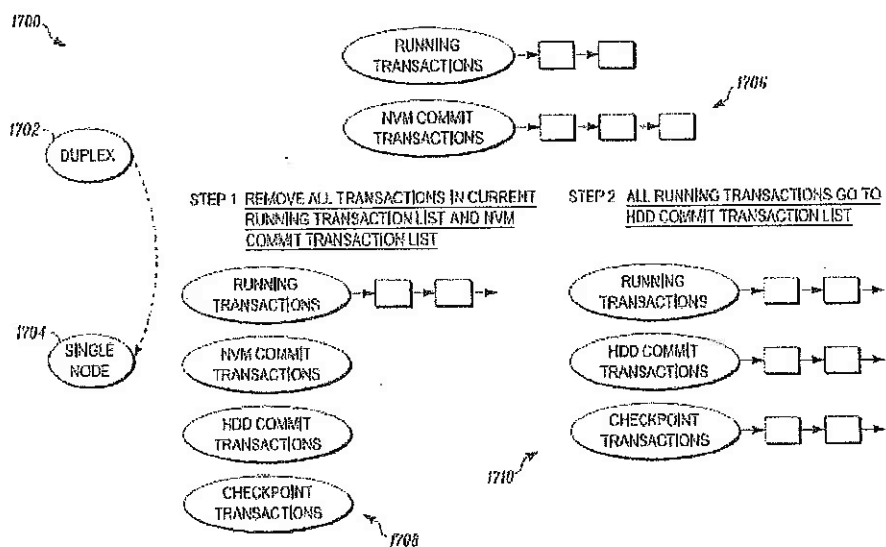


FIG. 17

[Fig. 18]

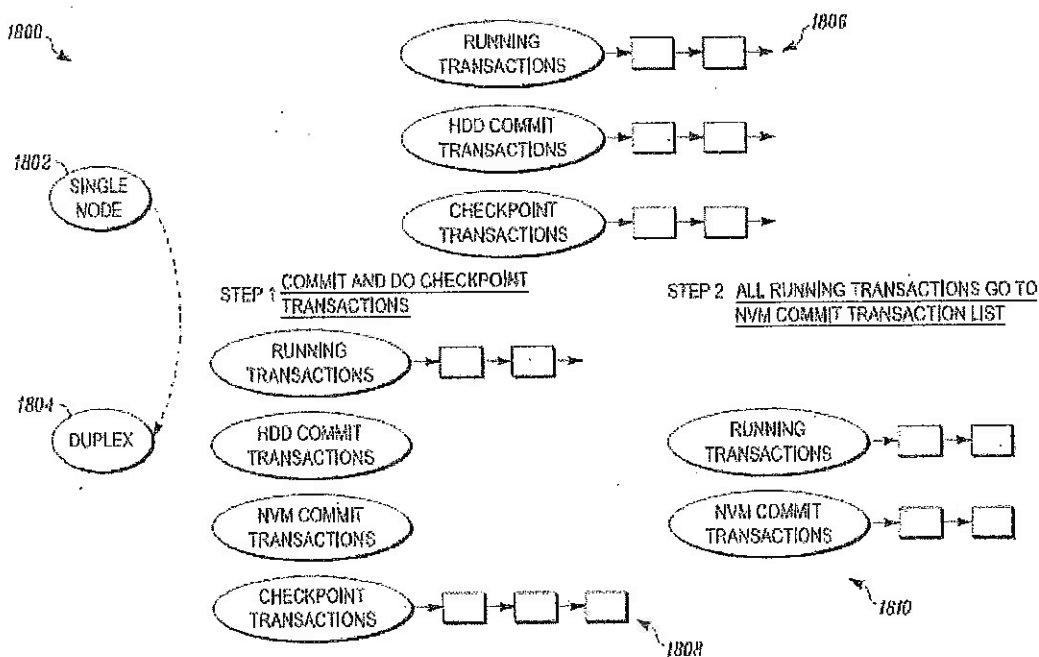


FIG. 18

[Fig. 19A]

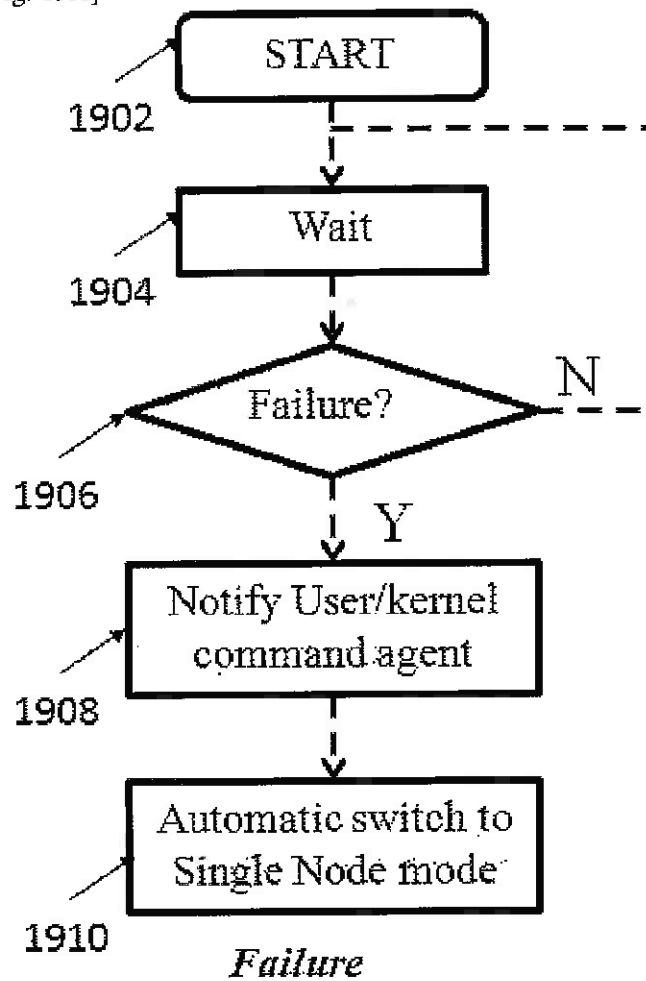


FIG. 19A

1900

[Fig. 19B]

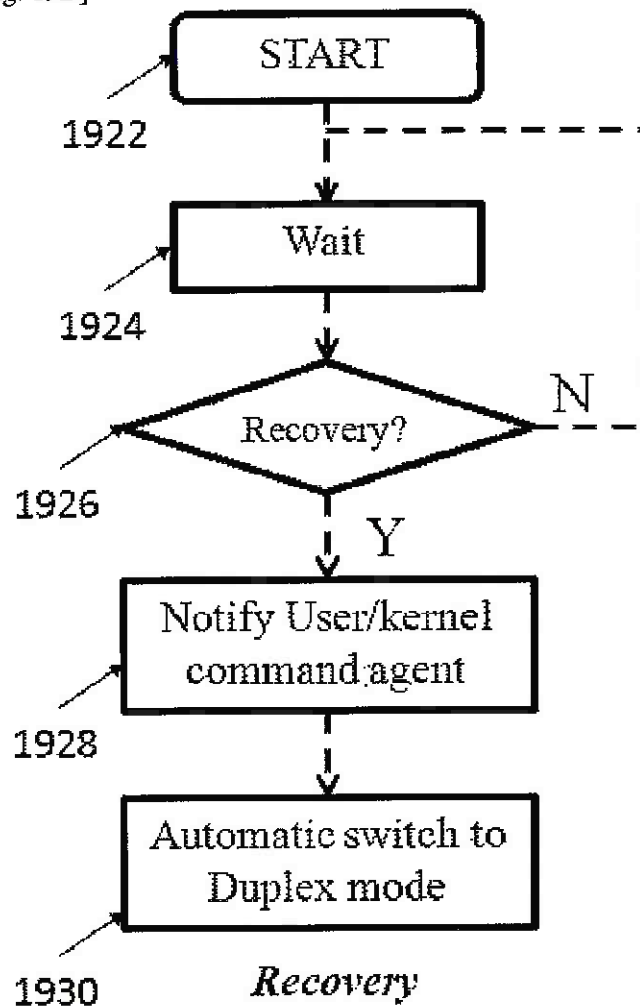
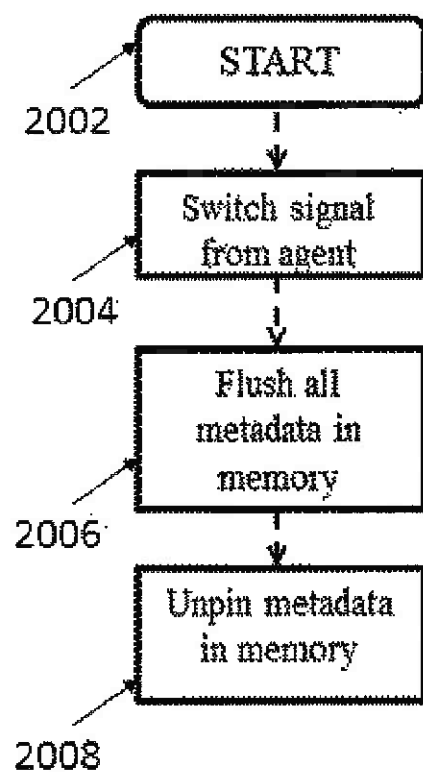


FIG. 19B

1920

[Fig. 20A]



*Duplex Node mode to Single mode*

FIG. 20A

2000

[Fig. 20B]

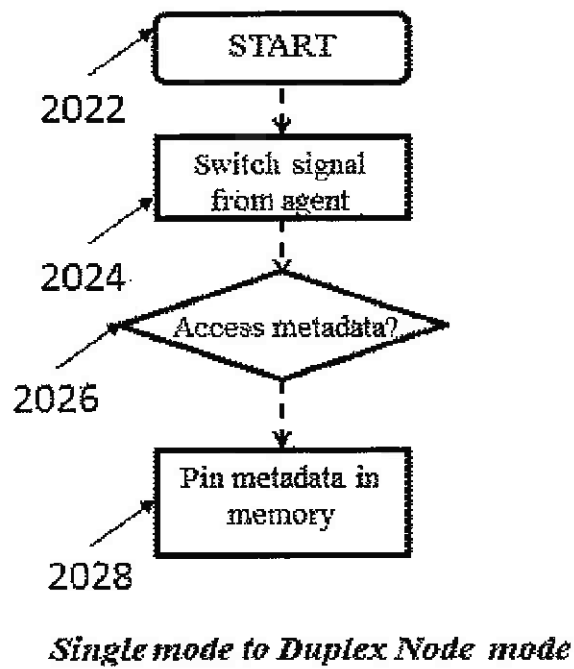
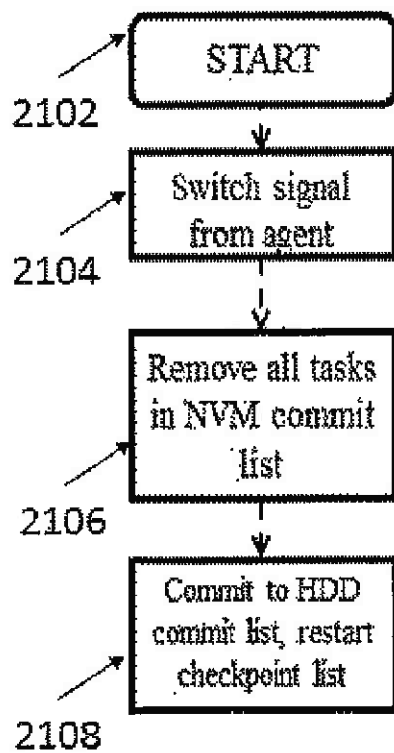


FIG. 20B

2020

[Fig. 21A]

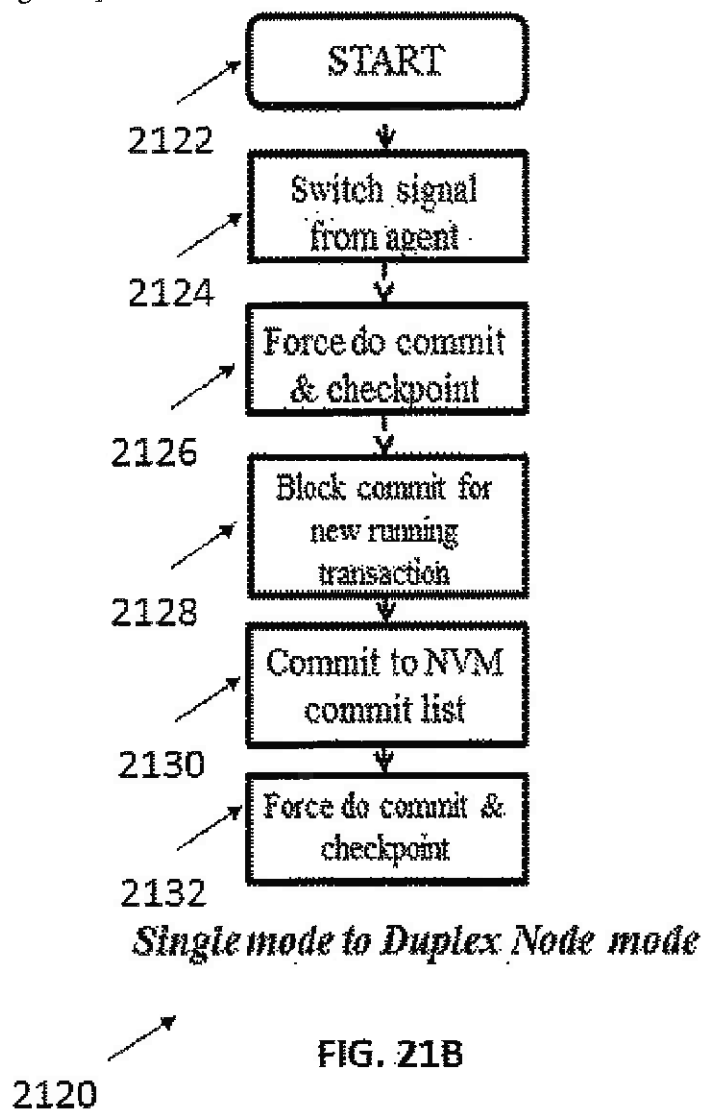


*Duplex Node mode to Single mode*

FIG. 21A

2100

[Fig. 21B]



[Fig. 22]

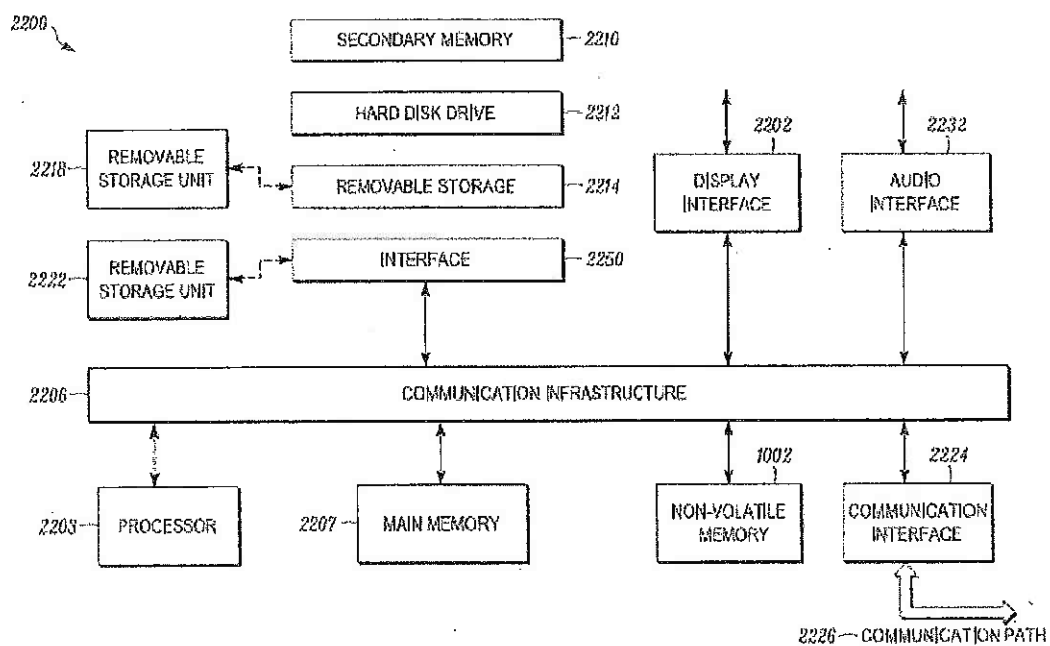


FIG. 22

[Fig. 23A]

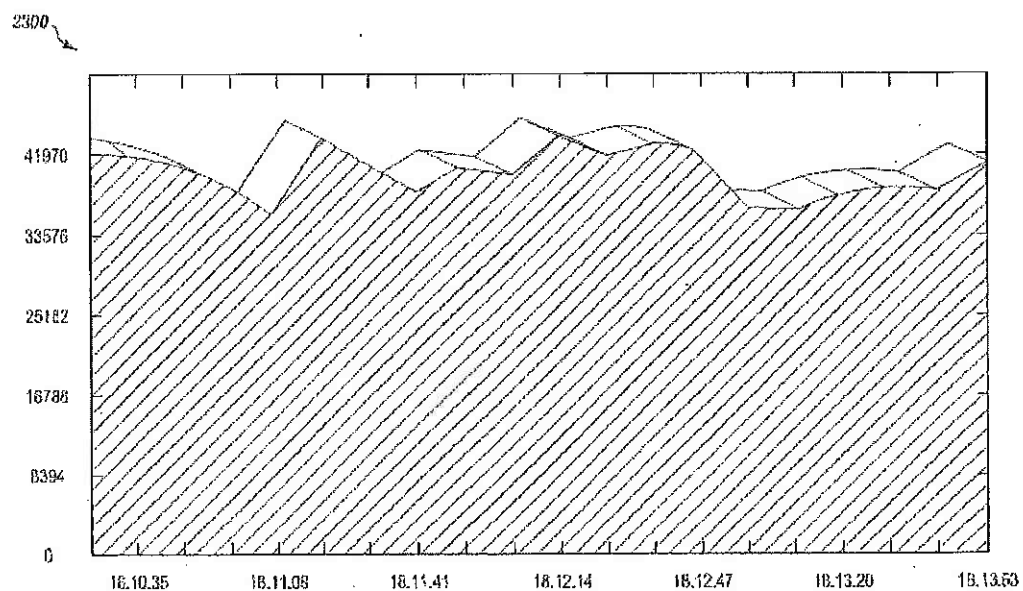
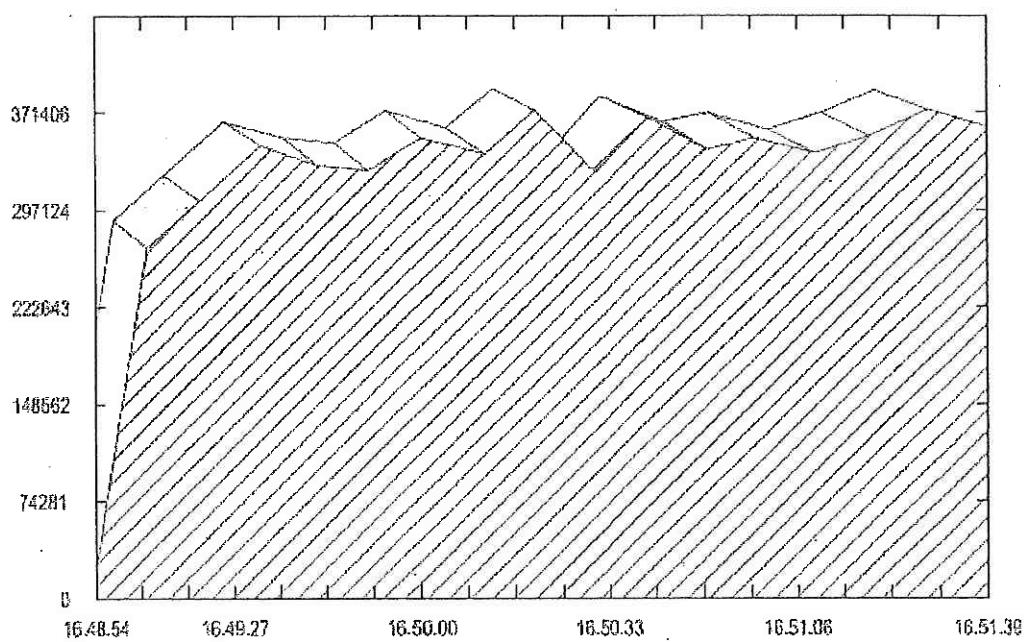


FIG. 23A

[Fig. 23B]

*FIG. 23B*