

METHODS FOR GROWING A SINGLE CRYSTAL SILICON INGOT USING CONTINUOUS CZOCHRALSKI METHOD

CROSS REFERENCE TO RELATED APPLICATION(S)

[0001] This application claims the benefit of priority to U.S. provisional application Ser. No. 62/835,735, filed 18 April 2019, the disclosure of which is incorporated by reference as if set forth in its entirety.

FIELD OF THE DISCLOSURE

[0002] The field of the disclosure relates to a method to grow a single crystal silicon ingot using the continuous Czochralski method.

BACKGROUND

[0003] Single crystal silicon, which is the starting material for most processes for the fabrication of semiconductor electronic components, is commonly prepared by the Czochralski ("Cz") method. In this method, polycrystalline silicon ("polysilicon") is charged to a crucible and melted, a seed crystal is brought into contact with the molten silicon, and a single crystal is grown by slow extraction. After formation of a neck is complete, the diameter of the crystal is enlarged by, for example, decreasing the pulling rate and/or the melt temperature until the desired or target diameter is reached. The cylindrical main body of the crystal which has an approximately constant diameter is then grown by controlling the pull rate and the melt temperature while compensating for the decreasing melt level. Near the end of the growth process but before the crucible is emptied of molten silicon, the crystal diameter is typically reduced gradually to form a tail end in the form of an end-cone. The end-cone usually is formed by increasing the crystal pull rate and heat supplied to the crucible. When the diameter becomes small enough, the crystal is then separated from the melt.

[0004] Czochralski growing techniques include the batch Czochralski method and the continuous Czochralski method. In batch CZ, a single polycrystalline charge is loaded into the crucible, the single charge being sufficient to grow a single crystal silicon ingot, after which the crucible is essentially depleted of silicon melt. In continuous

Czochralski (CCZ) growth, polycrystalline silicon may be continually or periodically added to the molten silicon to replenish the melt during the growth process and, as a result, multiple ingots can be pulled from a single crucible during a growth process.

[0005] To carry out the CCZ process, the traditional batch Czochralski growth chamber and apparatus are modified to include a means for feeding additional polycrystalline silicon to the melt in a continuous or semi-continuous fashion without adversely affecting the properties of the growing ingot. As the seed crystal is continuously grown from the melt, solid polycrystalline silicon such as granular polycrystalline silicon is added to the melt to replenish the melt. The feed rate of the additional solid polycrystalline silicon added to the melt is typically controlled to maintain process parameters. In order to reduce the adverse effects of this replenishing activity on simultaneous crystal growth, the traditional quartz crucible is often modified to provide an outer or annular melt zone into which the added material is delivered along with an inner growth zone from which the silicon ingot is pulled. These zones are in fluid flow communication with one another.

[0006] The continuously shrinking size of the modern microelectronic device imposes challenging restrictions on the quality of the silicon substrate, which is essentially determined by the size and the distribution of the grown-in microdefects. Most of the microdefects formed in silicon crystals grown by the Czochralski (CZ) process and the Float Zone (FZ) process are the agglomerates of intrinsic point defects of silicon – vacancies and self-interstitials (or, simply, interstitials).

[0007] A series of studies have established that the interstitial agglomerates exist in two forms – globular interstitial clusters, termed B swirl defect (or B-defects), and the dislocation loops, termed A swirl defect (or A-defects). Later discovered vacancy agglomerates, known as D-defects, have been identified as octahedral voids. Voronkov provided the well-accepted explanation for the microdefect distributions observed in silicon crystals on the basis of the crystal growth conditions. According to Voronkov's model, or theory, the temperature field in the vicinity of the melt/crystal interface drives the recombination of the point defects providing driving forces for their diffusion from the melt/crystal interface - where they exist at their respective equilibrium

concentrations - into the crystal bulk. The interplay between the transport of the point defects, both by the diffusion and the convection, and their recombination establishes the point defect concentration beyond a short distance away from the interface, termed the recombination length. Typically, the difference between the vacancy concentration and the interstitial concentration beyond the recombination length, termed the excess point defect concentration, remains essentially fixed away from the lateral surface of the crystal. In a rapidly pulled crystal, the spatial redistribution of the point defects by their diffusion beyond the recombination length is generally not important - with the exception of a region close to the lateral surface of the crystal that acts as a sink or a source of the point defects. Therefore, if the excess point defect concentration beyond the recombination length is positive, vacancies remain in excess, and agglomerate to form D-defects at lower temperatures. If the excess point defect concentration is negative, interstitials remain the dominant point defects, and agglomerate to form A-defects and B-defects. If the excess point defect concentration is below some detection threshold, no detectable microdefects are formed. Thus, typically, the type of grown-in microdefects is determined simply by the excess point defect concentration established beyond the recombination length. The process of establishing the excess point defect concentration is termed the initial incorporation and the dominant point defect species is termed the incorporated dominant point defect. The type of the incorporated point defects is determined by the ratio of the crystal pull-rate (v) to the magnitude of the axial temperature gradient in the vicinity of the interface (G). At a higher v/G , the convection of the point defects dominates their diffusion, and vacancies remain the incorporated dominant point defects, as the vacancy concentration at the interface is higher than the interstitial concentration. At a lower v/G , the diffusion dominates the convection, allowing the incorporation of the fast diffusing interstitials as the dominant point points. At a v/G close to its critical value, both the point defects are incorporated in very low and comparable concentrations, mutually annihilating each other and thus suppressing the potential formation of any microdefects at lower temperatures. The observed spatial microdefect distribution can be typically explained by the variation of v/G , caused by a radial non-uniformity of G and by an axial variation of v . A striking feature of the radial microdefect distribution is the oxide particles formed through the interaction of oxygen with vacancies in the regions of relatively lower incorporated vacancy concentration - at

a small range of v/G marginally above the critical v/G . These particles form a narrow spatial band that can be revealed by thermal oxidation as the OSF (oxidation-induced stacking faults) ring. Quite often, the OSF ring marks the boundary between adjacent crystal regions that are vacancy-dominated and interstitial-dominated, known as the V/I boundary.

[0008] The microdefect distributions in CZ crystals grown at lower rates in many modern processes, however, are influenced by the diffusion of the point defects in the crystal bulk, including the diffusion induced by the lateral surfaces of the crystals. Therefore, an accurate quantification of the microdefect distributions in CZ crystals preferably incorporates the 2-dimensional point defect diffusion, both axially and radially. Quantifying only the point defect concentration field can qualitatively capture the microdefect distribution in a CZ crystal, as the type of the microdefects formed is directly determined by it. For a more accurate quantification of the microdefect distribution, however, capturing the agglomeration of the point defects is necessary. Traditionally, the microdefect distribution is quantified by decoupling the initial incorporation of the point defects and the subsequent formation of the microdefects. This approach ignores the diffusion of the dominant point defects in the vicinity of the nucleation region, from the regions at higher temperatures (where the microdefect density is negligible) to the regions at lower temperatures (where the microdefects exist in higher densities and consume the point defects). Alternatively, a rigorous numerical simulation based on predicting the size distributions of the microdefect populations at every location in the crystal is numerically expensive.

[0009] The transition between vacancy and interstitial dominated material occurs at a critical value of v/G , which currently appears to be about $2.5 \times 10^{-5} \text{ cm}^2/\text{sK}$. If the value of v/G exceeds the critical value, vacancies are the predominant intrinsic point defect, with their concentration increasing with increasing v/G . If the value of v/G is less than the critical value, silicon self-interstitials are the predominant intrinsic point defect, with their concentration increasing with decreasing v/G . Accordingly, process conditions, such as growth rate (which affect v), as well as hot zone configurations (which affect G), can be controlled to determine whether the intrinsic point defects within the single crystal silicon will be predominantly vacancies (where v/G is generally

greater than the critical value) or self-interstitials (where v/G is generally less than the critical value).

[0010] Agglomerated defect formation generally occurs in two steps. First, defect "nucleation" occurs, which is the result of the intrinsic point defects being supersaturated at a given temperature; above this "nucleation threshold" temperature, intrinsic point defects remain soluble in the silicon lattice. The nucleation temperature for agglomerated intrinsic point defects is greater than about 1000 °C.

[0011] Once this "nucleation threshold" temperature is reached, intrinsic point defects agglomerate; that is, precipitation of these point defects out of the "solid solution" of the silicon lattice occurs. The intrinsic point defects will continue to diffuse through the silicon lattice as long as the temperature of the portion of the ingot in which they are present remains above a second threshold temperature (i.e., a "diffusivity threshold"). Below this "diffusivity threshold" temperature, intrinsic point defects are no longer mobile within commercially practical periods of time.

[0012] While the ingot remains above the "diffusivity threshold" temperature, vacancy or interstitial intrinsic point defects diffuse through the silicon lattice to sites where agglomerated vacancy defects or interstitial defects, respectively, are already present, causing a given agglomerated defect to grow in size. Growth occurs because these agglomerated defect sites essentially act as "sinks," attracting and collecting intrinsic point defects because of the more favorable energy state of the agglomeration.

[0013] Vacancy-type defects are recognized to be the origin of such observable crystal defects as D-defects, Flow Pattern Defects (FPDs), Gate Oxide Integrity (GOI) Defects, Crystal Originated Particle (COP) Defects, crystal originated Light Point Defects (LPDs), as well as certain classes of bulk defects observed by infrared light scattering techniques such as Scanning Infrared Microscopy and Laser Scanning Tomography. Also present in regions of excess vacancies are clusters of oxygen or silicon dioxide. Some of these clusters remain small and relatively strain-free, causing essentially no harm to a majority of devices prepared from such silicon. Some of these clusters are large enough to act as the nuclei for ring oxidation induced stacking faults

(OISF). It is speculated that this particular defect is facilitated by previously nucleated oxygen agglomerates catalyzed by the presence of excess vacancies. The oxide clusters are primarily formed in CZ growth below 1000 °C in the presence of moderate vacancy concentration.

[0014] Defects relating to self-interstitials are less well studied. They are generally regarded as being low densities of interstitial-type dislocation loops or networks. Such defects are not responsible for gate oxide integrity failures, an important wafer performance criterion, but they are widely recognized to be the cause of other types of device failures usually associated with current leakage problems.

[0015] In this regard it is to be noted that, generally speaking, oxygen in interstitial form in the silicon lattice is typically considered to be a point defect of silicon, but not an intrinsic point defect, whereas silicon lattice vacancies and silicon self-interstitials (or, simply, interstitials) are typically considered to be intrinsic point defects. Accordingly, essentially all microdefects may be generally described as agglomerated point defects, while D-defects (or voids), as well as A-defects and B-defects (i.e., interstitial defects) may be more specifically described as agglomerated intrinsic point defects. Oxygen clusters are formed by absorbing vacancies; hence, oxygen clusters can be regarded as agglomerates of both vacancies and oxygen.

[0016] It is to be further noted that the density of such vacancy and self-interstitial agglomerated point defects in Czochralski silicon historically has been within the range of about $1 \times 10^3/\text{cm}^3$ to about $1 \times 10^7/\text{cm}^3$, whereas the density of oxygen clusters varies between around $1 \times 10^8/\text{cm}^3$ to $1 \times 10^{10}/\text{cm}^3$. Agglomerated intrinsic point defects are therefore of rapidly increasing importance to device manufacturers, because such defects can severely impact the yield potential of the single crystal silicon material in the production of complex and highly integrated circuits.

[0017] In view of the foregoing, in many applications it is preferred that a portion or all of the silicon crystal, which is subsequently sliced into silicon wafers, be substantially free of these agglomerated intrinsic point defects. To-date, several approaches for growing substantially defect-free silicon crystals have been reported.

Generally speaking, all these approaches involve controlling the ratio v/G , in order to determine the initial type and concentration of intrinsic point defects present in the growing CZ single crystal silicon crystal. Additionally, however, such approaches may involve controlling the subsequent thermal history of the crystal to allow for prolonged diffusion time to suppress the concentration of intrinsic point defects therein, and thus substantially limit or avoid the formation of agglomerated intrinsic point defects in a portion or all of the crystal. (See, for example, U.S. Patent Nos.: 6,287,380; 6,254,672; 5,919,302; 6,312,516 and 6,328,795; the entire contents of which are hereby incorporated herein by reference.) Alternatively, however, such approaches may involve a rapidly cooled silicon (RCS) growth process, wherein the subsequent thermal history of the crystal is then controlled to rapidly cool at least a portion of the crystal through a target nucleation temperature, in order to control the formation of agglomerated intrinsic point defects in that portion. One or both of these approaches may also include allowing at least a portion of the grown crystal to remain above the nucleation temperature for a prolonged period of time, to reduce the concentration of intrinsic point defects prior to rapidly cooling this portion of the crystal through the target nucleation temperature, thus substantially limiting or avoiding the formation of agglomerated intrinsic point defects therein. (See, e.g., U.S. Patent Application Publication No. 2003/0196587, the entire disclosure of which is incorporated herein by reference.) Still further, methods have been developed to reduce or eliminate agglomerated point defects from the center of the ingot to the edge by the simultaneous control of the cooling rate of the solidified ingot and the radial variation of the axial temperature gradient in the vicinity of the interface (G). (See, e.g., U.S. Patent No. 8,673,248, the entire disclosure of which is incorporated herein by reference.)

[0018] Polished silicon wafers that meet manufacturer requirements for lack of agglomerated point defects, e.g., crystal originated pits (COP), may be referred to as Neutral Silicon or Perfect Silicon. Perfect Silicon wafers are preferred for many semiconductor applications as a lower cost polished wafer alternative to, for example, higher epitaxially deposited wafers. In the course of the last 20 years, many silicon wafer suppliers have developed Defect Free and COP Free wafer products in both 200mm and 300mm diameter for sale mostly to the Memory (DRAM/NAND/FLASH)

market which traditionally has been more sensitive to cost pressures within the market. The industrial standard for acceptable defectivity has evolved over this time frame in terms of acceptable level of light point scatterings (LLS) and Gate Oxide Intensity (GOI) as customer applications device nodes have shrunk. For example, an industrial COP free specification may have been, at one time, less than a few hundred at no more than 0.12um size. More current standards demand less than 20 COPs at no more than 0.026um size to qualify as perfect silicon. As another example, the past standard for GOI in a MOS transistor was 95% at $\leq 8\text{MV}$ (B-mode). Currently, specifications are moving to 99% at 10~12MV (D-mode). On top of this requirement, the need for improved radial oxygen precipitation across the wafer as measured traditionally by BMD density (bulk micro defects) and BMD size distribution are required as device nodes shrink in order to avoid substrate slip during processing or warp that may impact patterned overlay during device lithography. As these specifications have tightened (LLS, GOI, BMD uniformity, etc.), the control window for defect and COP free silicon growth has shrunk considerably, reducing crystal throughput of the process considerably. This is because the window of acceptable band structure (which can be directly translated to process window of operation) has shifted over time with the specifications.

[0019] This section is intended to introduce the reader to various aspects of art that may be related to various aspects of the disclosure, which are described and/or claimed below. This discussion is believed to be helpful in providing the reader with background information to facilitate a better understanding of the various aspects of the present disclosure. Accordingly, it should be understood that these statements are to be read in this light, and not as admissions of prior art.

SUMMARY

[0020] One aspect of the present disclosure is directed to a method of preparing a single crystal silicon ingot by the Continuous Czochralski method. The method comprises: adding an initial charge of polycrystalline silicon to a crucible; heating the crucible comprising the initial charge of polycrystalline silicon to cause a silicon melt to form in the crucible, the silicon melt comprising an initial volume of molten silicon and having an initial melt elevation level; contacting a silicon seed crystal with a silicon

melt; withdrawing the silicon seed crystal to grow a neck portion, wherein the silicon seed crystal is withdrawn at a neck portion pull rate during growth of the neck portion; withdrawing the silicon seed crystal to grow an outwardly flaring seed-cone adjacent the neck portion, wherein the silicon seed crystal is withdrawn at a seed-cone pull rate during growth of the outwardly flaring seed-cone; and withdrawing the silicon seed crystal to grow a main body of the single crystal silicon ingot adjacent the outwardly flaring seed-cone, wherein the silicon melt comprises a volume of molten silicon and a melt elevation level during growth of the main body of the single crystal silicon ingot; wherein the main body of the single crystal silicon ingot is grown at an initial variable main body pull rate and a constant main body pull rate, wherein the main body of the single crystal silicon ingot is grown at the initial variable main body pull rate for less than about 20% of a length of the main body of the single crystal silicon ingot and grown at the constant main body pull rate during growth for at least about 30% of the length of the main body of the single crystal silicon ingot; and further wherein polycrystalline is continuously feed to the crucible to thereby replenish a volume of molten silicon and melt elevation level in the crucible during growth of the main body of the single crystal silicon ingot.

[0021] Various refinements exist of the features noted in relation to the above-mentioned aspects of the present disclosure. Further features may also be incorporated in the above-mentioned aspects of the present disclosure as well. These refinements and additional features may exist individually or in any combination. For instance, various features discussed below in relation to any of the illustrated embodiments of the present disclosure may be incorporated into any of the above-described aspects of the present disclosure, alone or in any combination.

BRIEF DESCRIPTION OF THE DRAWINGS

[0022] FIGS. 1A (early body growth), 1B (middle body growth), and 1C (late body growth) illustrate the melt volume or depth and crucible position as a function of crystal length during an exemplary batch Czochralski process.

[0023] FIG. 2 is a graph depicting the constant change in pull rate required to achieve sufficient defectivity control in an ingot grown by an exemplary batch Cz process.

[0024] FIG. 3 is a graph depicting the pull rate profile required to achieve sufficient defectivity control in an ingot grown by an exemplary batch Cz process with an applied magnetic field.

[0025] FIG. 4 is a graph depicting the pull rate profile required to achieve sufficient defectivity control in an ingot grown by an exemplary continuous Cz process according to the method of the present invention.

[0026] FIG. 5 depicts an exemplary crucible configuration suitable for an exemplary continuous Cz process according to the method of the present invention.

[0027] FIGS. 6A, 6B, and 6C illustrate the melt level and ingot growth an exemplary continuous Cz process according to the method of the present invention.

[0028] FIG. 7A and 7B illustrate magnetic fields applied to the silicon melt during growth of an ingot by an exemplary continuous Cz process according to the method of the present invention.

[0029] Corresponding reference characters indicate corresponding parts throughout the drawings.

DETAILED DESCRIPTION

[0030] In the context of the present invention, “perfect silicon” refers to single crystal silicon wafers sliced from Czochralski grown single crystal silicon ingots grown under conditions to meet or exceed the standards of Perfect Silicon™ (SunEdison Semiconductor, Ltd.). These standards include an ingot that meets or exceeds industry specifications for agglomerated defects, DSOD (direct surface oxide defects), COP (crystal originated pits or particles), D-Defects, and I-defects, etc. For example, “perfect silicon” wafers may be characterized by non-detectable FPD (Flow Pattern Defect by Secco etching technique) and DSOD (Direct Surface Oxide Defect particle count after

electric breakdown) and zero I-defect (A-defect) by Secco-etching technique. The Secco etch comprises applying a dilute aqueous solution of an alkali dichromate and hydrofluoric acid, for suitably revealing dislocations and other lattice defects in the various crystallographic (100), (111), and (110) planes of silicon. The etch brings out both lineage (low angle grain boundaries) and slip lines. The method of the present invention enables the growth of a single crystal silicon ingot comprising perfect silicon over at least about 70% of the length of the main body of the single crystal silicon ingot, such as over at least about 80% of the length of the main body of the single crystal silicon ingot, or even over at least about 90% of the length of the main body of the single crystal silicon ingot. In some embodiments, wafers sliced from ingots grown over at least about 70% of the length of the main body of the single crystal silicon ingot, such as over at least about 80% of the length of the main body of the single crystal silicon ingot, or even over at least about 90% of the length of the main body of the single crystal silicon ingot are characterized by non-detectable FPD (Flow Pattern Defect by Secco etching technique) and DSOD (Direct Surface Oxide Defect particle count after electric breakdown) and zero I-defect (A-defect) by Secco-etching technique. In some embodiments, wafers sliced from ingots grown over at least about 70% of the length of the main body of the single crystal silicon ingot, such as over at least about 80% of the length of the main body of the single crystal silicon ingot, or even over at least about 90% of the length of the main body of the single crystal silicon ingot are characterized by GOI in a MOS transistor at 95% at $\leq 8\text{MV}$ (B-mode), preferably 99% at 10~12MV (D-mode).

[0031] In a conventional batch Czochralski process to grow a single crystal silicon ingot, the crystal melt/interface and the thermal condition both vary continuously as a function of increasing ingot length due to the consumption of silicon melt during ingot growth and variability of the crucible position. A depiction of the melt depletion and the movement of the crucible may be found in FIG. 1. FIGS. 1A (early body growth), 1B (middle body growth), and 1C (late body growth) illustrate the melt level and crucible height as a function of crystal length during a batch Cz process. Since the melt conditions, e.g., mass and elevation levels, and the crucible location are continuously changing during a batch process, the v/G ratio (growth rate V /axial

temperature gradient G) necessary to minimize defectivity is continuously changing over crystal length, shifting the quality of the crystal. In order to maintain the desired quality to meet specification within the process window, continuous control adjustment of several parameters is required by position. These parameters include crucible rotation rate (C/R), seed rotation rate (S/R), seed lift rate (S/L), heater power, reflector height, etc. See FIG. 2, which is a graph depicting the constant change in pull rate required to achieve sufficient defectivity control in an ingot grown by an exemplary batch Cz process. The line defined by the triangle (---▲---) is the constantly changing critical pull rate to achieve the critical v/G value. The pull rate may vary within the upper critical pull rate ("UCL") marked by the square (---■---) and the lower critical pull rate ("LCL") marked by the diamond (---◆---) and still achieve acceptable defectivity control. These three lines indicate the pull speed that can produce perfect silicon. Within the upper and lower pull speed boundaries, perfect silicon may be produced having varying dominant intrinsic point defects. For example, vacancy dominant ("Pv") silicon is produced when the applied pull speed is controlled between the lines labelled with the Triangle and square in FIG. 2 (and FIGS. 3 and 4), and interstitial dominant ("Pi") silicon is produced when the applied pull speed is controlled between the lines labeled with the Triangle and diamond in FIG. 2 (and FIGS. 3 and 4). In the example depicted in FIG. 2 (and FIGS. 3 and 4), early and middle portions of ingot growth, perfect silicon having vacancy dominant point defects may be grown, as shown by the representation of the ingot cross section labeled "Pv." FIG. 2 depicts that the ingot is vacancy dominant from center to edge. In the example depicted in FIG. 2, in later growth, the ingot is characterized by regions of vacancy dominant point defects and interstitial dominant point defects, which is represented in the cross section regions labeled "Pi." For example, an ingot may be grown that is interstitial dominant from center to a radial length less than the radius of the entire ingot, which is surrounded by a band of vacancy dominant material to the edge of the ingot. In a still later region, the cross section of the ingot shows vacancy dominant material in the center, surrounded by a band of interstitial dominant material, surrounded by another band of vacancy dominant material at the edge. These depictions are merely for illustration and are not intended to be limiting upon the method of the present invention. Perfect Silicon, i.e., ingots and wafers sliced therefrom that are free of detectable agglomerated point defects, results from the proper control of excess point

defects (vacancy and interstitial silicon) in part by the pull rate of the ingot. Intrinsic point defects, such as vacancies and interstitials, occur in ingots grown by the Czochralski method. Presence of such defects has no impact on whether the silicon is perfect. Rather, perfect silicon is characterized as being free of agglomerated point defects, such as COPs, DSODs, and I-defects.

[0032] FIG. 2 (and also FIGS. 3 and 4) also indicate the agglomerated defects which may result in the ingot as a function of pull speeds outside the upper and lower limits of critical v/G at any crystal length. The defects which may occur if the upper critical pull rate is exceeded (v/G is higher than a critical v/G) include DSOD (direct surface oxide defects) or COP (crystal originated pits or particles). Defects which may occur if the pull rate is below the lower critical pull rate (v/G is lower than a critical v/G) include I-defects. The bands in which these defects occur are also shown in the ingot cross sections. In regions wherein the dominant excess point defect is vacancies, agglomerations of vacancy defects form bulky defect such as COP, DSOD by agglomeration and precipitation of vacancies during the Cz silicon crystal growth and cooling. In regions wherein the dominant excess point defect is the interstitial silicon atoms, they agglomerate to form bulk defect such as I-defect by agglomeration/precipitation of interstitial silicon and punch out the dislocation. Any small deviation of v/G will form a defect by agglomeration of either vacancy or interstitial silicon atom.

[0033] Magnetic fields, e.g., horizontal or cusp, may be applied to alter the melt flow pattern within the crucible and enable enhanced control of the shape and height of the crystal/melt interface, which thereby enhances quality control. However, pull rates and other parameters must still be varied to achieve acceptable defectivity control, as exemplified in FIG. 3. FIG. 3 is a graph depicting the pull rate profiles required to achieve sufficient defectivity control in an ingot grown by an exemplary batch Cz process with an applied magnetic field. The line defined by the triangle (---▲---) is the critical pull rate to achieve the critical v/G value, which varies continuously during growth of the single crystal silicon ingot, as necessary. The pull rate may vary within the upper critical pull rate ("UCL") marked by the square (---■---) and the lower critical pull rate ("LCL") marked by the diamond (---◆---) and still achieve acceptable defectivity

control. The regions of intrinsic point defect and agglomerated point defect in the cross section of the single crystal silicon ingot are substantially as described above in the description of FIG. 2.

[0034] According to the method of the present invention, a single crystal silicon ingot is grown by the continuous Czochralski (CCZ) method under conditions that enable the melt depth (i.e., elevation level of the molten silicon) and thermal conditions to remain substantially constant during growth because melt is continuously replenished as it is consumed. In some embodiments, maintaining a substantially constant elevation level of the molten silicon enables maintaining the crucible in a fixed position. Once v/G is fixed with the appropriate hot zone configuration, the process window will be fixed (i.e., no control adjustment) over a substantial portion of the crystal length. Once the process conditions (S/R, power, gas flow and pressure, magnetic flux density, Cusp or HMCZ MGP position) are set, gas flow and/or chamber pressure and C/R are used to control oxygen content. Accordingly, the method of the present invention enables growth of a single crystal silicon ingot by the continuous Czochralski (CCZ) method in which the pull speed is constant during growth of a substantial length of the ingot, and the grown ingot according to this method has uniform and acceptable defectivity control, Oi uniformity, and BMD uniformity over a substantial portion of the axial length of the ingot.

[0035] After all conditions are set to steady state, the pull rate to produce a single crystal silicon ingot having the desired defectivity control in the entire crystal length will be constant, significantly reducing quality losses in production due to process control as compared to conventional Cz process. FIG. 4 is a graph of the pull rate as a function of axial length of the single crystal silicon ingot during an exemplary continuous Czochralski growth method according to the present invention. As shown in FIG. 4, a constant pull rate is maintained over a substantial portion of the growth of the main body portion of the single crystal silicon ingot. The line defined by the triangle (---▲---) is the critical pull rate to achieve the critical v/G value, which has an initial varying region, followed by a region of constant pull rate over a substantial portion of the growth of the main body portion of the single crystal silicon ingot. The pull rate may vary within the upper critical pull rate ("UCL") marked by the square (---■---) and the

lower critical pull rate (“LCL”) marked by the diamond (---◆---) and still achieve acceptable defectivity control. The regions of intrinsic point defect and agglomerated point defect in the cross section of the single crystal silicon ingot are substantially as described above in the description of FIG. 2.

[0036] In the CCZ process according to the method of the present invention, polycrystalline silicon feeding is continuous with the crystal growth, thereby the melt volume is maintained substantially the same as the initial charge melt depth regardless of crystal length. Since melt elevation is controlled by the mass balance between the growing crystal weight and the continuously fed polycrystalline silicon, the thermal condition in the melt and growing crystal is unchanged throughout the axial growth. Subsequently, once the desired crystal/melt interface is determined and fixed by parameters such as magnetic field, C/R and S/R, pull rate, heater power, etc., defect quality and Oi control will be maintained constant throughout the crystal axial growth. Further, because the thermal conditions and crystal/melt interface are fixed during the crystal growth, a constant pull rate for a given HZ and crystal/melt interface can be used over the entire crystal length in a quasi-steady state control.

[0037] According to the method of the present invention, the thermal condition suitable for achieving growth of the single crystal silicon ingot meeting the requirements for defectivity control is set by the hot zone configuration. The growth method is the continuous Czochralski method. Accordingly, the furnace chamber comprises means, e.g., a feeding tube, for continuous feeding of polycrystalline silicon. The solid polysilicon added to the crucible is typically granular polysilicon, although chunk polysilicon may be used, and it is fed into the crucible using a polysilicon feeder that is optimized for use with granular polysilicon. Chunk polysilicon typically has a size of between 3 and 45 millimeters (e.g., the largest dimension), and granular polysilicon typically has a size between 400 and 1400 microns. Granular polysilicon has several advantages including providing for easy and precise control of the feed rate due to the smaller size. However, the cost of granular polysilicon is typically higher than that of chunk polysilicon due to the chemical vapor deposition process or other manufacturing methods used in its production. Chunk polysilicon has the advantage of being cheaper and being capable of a higher feed rate given its larger size. The location of the heating

units, cooling jacket, and operation of the power control are adjusted to accommodate the polycrystalline silicon feeder.

[0038] A depiction of a crucible 10 having a plurality of weirs (20, 30, 40) or fluid barriers that separate the melt into different melt zones is shown in FIG. 5. In the illustrated embodiment, the crucible assembly 10 includes a first weir 20 (broadly, a fluid barrier) defining the inner melt zone 22 of the silicon melt. The inner melt zone 22 is the growth region from which the single crystal silicon ingot 50 is grown. A second weir 30 defines a middle melt zone 32 of the silicon melt. Finally, a third weir 40 defines an outer melt zone 42 of the silicon melt. A feeding tube (not shown) feeds polycrystalline silicon, which may be granular, chunk, or a combination of granular and chunk, into the outer melt zone 42 at a rate sufficient to maintain a substantially constant melt elevation level and volume during growth of the ingot. The first weir 20, second weir 30, and third weir 40 each have a generally annular shape, and have at least one opening defined therein to permit molten silicon to flow radially inward towards the growth region of the inner melt zone 22. The crucible configuration depicted in FIG. 5 is exemplary and suitable for carrying out the process of the present invention. Other configurations suitable for CCZ may be used without departing from the scope of the present invention. For example, the crucible 10 may lack the second weir 30 and/or may lack the third weir 40.

[0039] Generally, the melt from which the ingot is drawn is formed by loading polycrystalline silicon into a crucible to form an initial silicon charge. In general, an initial charge is between about 100 kilograms and about 200 kilograms of polycrystalline silicon, which may be granular, chunk, or a combination of granular and chunk. The mass of the initial charges depends on the desired crystal diameter and HZ design. Initial charge does not reflect the length of crystal, because polycrystalline silicon is continuously fed during crystal growth. For example, if polycrystalline silicon is fed continuously and the chamber height is tall enough, crystal length can be extended to 2000 mm, 3000 mm, or even 4000mm in length. The crucible may have the configuration depicted in FIG. 5, or another configuration suitable for CCZ growth. A variety of sources of polycrystalline silicon may be used including, for example, granular polycrystalline silicon produced by thermal decomposition of silane or a halosilane in a

fluidized bed reactor or polycrystalline silicon produced in a Siemens reactor. Once polycrystalline silicon is added to the crucible to form a charge, the charge is heated to a temperature above about the melting temperature of silicon (e.g., about 1412°C) to melt the charge, and thereby form a silicon melt comprising molten silicon. The silicon melt has an initial volume of molten silicon and has an initial melt elevation level, and these parameters are determined by the size of the initial charge. In some embodiments, the crucible comprising the silicon melt is heated to a temperature of at least about 1425°C, at least about 1450°C or even at least about 1500°C.

[0040] Once the charge is liquefied to form a silicon melt comprising molten silicon, the silicon seed crystal is lowered to contact the melt. The silicon seed crystal is then withdrawn from the melt with silicon being attached thereto (i.e., the seed crystal portion and neck 52, with reference to FIG. 5) thereby forming a melt/solid interface near or at the surface of the melt. Generally, the initial pull speed to form the neck portion is high. In some embodiments, the silicon seed crystal and neck portion is withdrawn at a neck portion pull rate of at least about 1.0 mm/minute, such as between about 1.5 mm/minute and about 6 mm/minute, such as between about 3 mm/minute and about 5 mm/minute. In some embodiments, the silicon seed crystal and the crucible are rotated in opposite directions, i.e., counter-rotation. Counter-rotation achieves convection in the silicon melt. Rotation of crystal is mainly used to provide a symmetric temperature profile, suppress angular variation of impurities and also to control crystal melt interface shape. In some embodiments, the silicon seed crystal is rotated at a rate of between about 5 rpm and about 30 rpm, or between about 5 rpm and about 20 rpm, or between about 8 rpm and about 20 rpm, or between about 10 rpm and about 20 rpm. In some embodiments, the crucible is rotated at a rate between about 0.5 rpm and about 10 rpm, or between about 1 rpm and about 10 rpm, or between about 4 rpm and about 10 rpm, or between about 5 rpm and about 10 rpm. In some embodiments, the seed crystal is rotated at a faster rate than the crucible. In some embodiments, the seed crystal is rotated at a rate that is at least 1 rpm higher than the rotation rate of the crucible, such as at least about 3 rpm higher, or at least about 5 rpm higher. In general, the neck portion 52 has a length between about 300 millimeters and about 700 millimeters, such as between about

450 millimeters and about 550 millimeters. However, the length of the neck portion 52 may vary outside these ranges.

[0041] After formation of the neck 52, the outwardly flaring seed-cone portion 54 adjacent the neck 52 is grown, with reference to FIG. 5. In general, the pull rate is decreased from the neck portion pull rate to a rate suitable for growing the outwardly flaring seed-cone portion 54. For example, the seed-cone pull rate during growth of the outwardly flaring seed-cone is between about 0.5 mm/min and about 2.0 mm/min, such as about 1.0 mm/min. In some embodiments, the outwardly flaring seed-cone 54 has a length between about 100 millimeters and about 400 millimeters, such as between about 150 millimeters and about 250 millimeters. The length of the outwardly flaring seed-cone 54 may vary outside these ranges. In some embodiments, the outwardly flaring seed-cone 54 is grown to a terminal diameter of about 150 mm, at least about 150 millimeters, about 200 mm, at least about 200 millimeters, about 300 mm, at least about 300 mm, about 450 mm, or even at least about 450 mm. The terminal diameter of the outwardly flaring seed-cone 54 is generally equivalent to the diameter of the constant diameter of the main body of the single crystal silicon ingot.

[0042] After formation of the neck 52 and the outwardly flaring seed-cone 54 adjacent the neck portion, the main ingot body 56 having a constant diameter adjacent the cone portion is then grown. The constant diameter portion of the main ingot body 56 has a circumferential edge, a central axis that is parallel to the circumferential edge, and a radius that extends from the central axis to the circumferential edge. The central axis also passes through the outwardly flaring seed-cone 54 and neck 52. The diameter of the main ingot body 56 may vary and, in some embodiments, the diameter may be about 150 mm, at least about 150 millimeters, about 200 mm, at least about 200 millimeters, about 300 mm, at least about 300 mm, about 450 mm, or even at least about 450 mm. The main ingot body 52 of the single crystal silicon ingot is eventually grown to be at least about 1000 millimeters long, such as at least 1400 millimeters long, such as at least 1500 millimeters long, or at least 2000 millimeters long, or at least 2200 millimeters, such as 2200 millimeters, or at least about 3000 millimeters long, or at least about 4000 millimeters long.

[0043] FIG. 4 illustrates an exemplary and non-limiting pull rate protocol for pulling the main body of the single crystal silicon ingot according to some embodiments of the method of the present invention. As is apparent from the exemplary illustration, the pull rate declines from a relatively high pull rate, to a minimum pull rate, and then rising to a constant pull rate for a significant portion of growth of the main body of the single crystal silicon ingot. According to the process of the present invention, the pull rates are selected to achieve perfect silicon, i.e., silicon characterized by a lack detectable agglomerated defects selected from among agglomerated defects, DSOD (direct surface oxidation defects), COP (crystal originated pits), D-Defects, and I-defects, etc. The initial high pull rate may be between about 0.5 mm/min and about 2.0 mm/min, such as about 1.0 mm/min, then decreasing to a pull rate that may be as low as about 0.4 mm/min or even as low as about 0.3 mm/min, before increasing to the constant pull rate between about 0.4 mm/min and about 0.8 mm/min, between about 0.4 mm/min and about 0.7 mm/min, or between about 0.4 mm/min and about 0.65 mm/min.

[0044] An exemplary and non-limiting illustration of a hot zone configuration within a furnace chamber 100 suitable for carrying out the method of the present invention is depicted in FIGS. 6A, 6B, and 6C. Other hot zone configurations are suitable for carrying out the method of the present invention. The hot zone configuration includes a cooling jacket 102, a reflector 104, a side heater 106, and a bottom heater 108. The growing crystal diameter and the shape and height of the meniscus is monitored by a camera (not pictured) located at the top window. Data obtained from the camera enables feedback to the side heater 106 and bottom heater 108. During crystal growth, the power distribution may be adjusted between the heaters to enable uniformity of the melt/solid interface, i.e., maintain the desired shape and height of the meniscus. The reflector 104 should reflect the heat flux from the hot part of the furnace including the heater and crucible to the melt. The reflector 104 reduces heat transfer from the hot part to the cold part (maintained by the cooling jacket 102) of the furnace and thereby maintains a separation between these two regions of the furnace. The reflector helps control the axial and radial temperature gradients, which drive the solidification and crystallization of the molten silicon into the growing ingot.

[0045] FIGS. 6A, 6B, and 6C depict a 1st step, 2nd step, and 3rd step, respectively, corresponding to the regions of pull rate shown in FIG. 4. That is, the 1st step depicted in FIG. 6A corresponds to the region wherein the pull rate is high and decreases to a minimum in FIG. 4. The 2nd step depicted in FIG. 6B corresponds to the region wherein the pull rate is at a minimum and increases to the constant pull rate in FIG. 4. The 3rd step depicted in FIG. 6C corresponds to the region of constant pull rate in FIG. 4. During each step, the melt 110 retains a constant melt volume and melt elevation level according to embodiments of the present invention since polycrystalline silicon is continuously fed into the melt (see FIG. 5) during growth of the ingot 112.

[0046] The initial region of growing the main body of the single crystal silicon ingot characterized by variable pull rate may encompass less than about 20% of the total length of the main body of the single crystal silicon ingot. In some embodiments, the variable pull rate regime may encompass between about 5% and about 20% of the length of the main body of the single crystal silicon ingot, such as between about 5% and about 15% of the length of the main body of the single crystal silicon ingot, or between about 10% and about 15% of the length of the main body of the single crystal silicon ingot. The percentage of the length of the main body of the single crystal silicon ingot grown under variable pull rate conditions depends in part on the total length of the main body of the ingot. For example, the length of the main body of the ingot pulled under variable rate conditions may vary between about 50 mm to about 200 mm between about 100 mm to about 200 mm, such as between about 150 mm to about 200 mm. If 200 mm is grown under variable rate conditions, and the total length of the main body of the ingot is 1400 mm, about 14% of the main body is grown under variable rate conditions, while only about 9% of the main body is grown under variable rate conditions for a total main body length of 2200 mm.

[0047] After the initial region of the main body is grown under variable pull rate conditions, the remainder of the main body is grown under a constant pull rate. In some embodiments, the main body of the ingot is grown at a constant main body pull rate during growth of at least about 30% of the length of the main body of the single crystal silicon ingot, such as at least about 50% of the length of the main body of the single crystal silicon ingot, at least about 70% of the length of the main body of the

single crystal silicon ingot, at least about 80% of the length of the main body of the single crystal silicon ingot, or even at least about 90% of the length of the main body of the single crystal silicon ingot. In some embodiments, the constant main body pull rate is between about 0.4 mm/min and about 0.8 mm/min, between about 0.4 mm/min and about 0.7 mm/min, or between about 0.4 mm/min and about 0.65 mm/min.

[0048] During growth of the main body of the single crystal silicon ingot, polycrystalline silicon, i.e., granular, chunk, or a combination of granular and chunk, is added to the molten silicon to thereby achieve a constant volume of molten silicon and constant melt elevation level. According to the method of the present invention, maintenance of a substantially constant melt volume during growth of a substantial portion of the axial length of the main body of the single crystal silicon ingot enables the achievement of high ingot quality over a substantial portion of the axial length of the main body of the single crystal silicon ingot at a constant pull rate. The constant melt volume regardless of the crystal length enables maintaining a constant crystal/melt interface and thus uniform crystal quality over a substantial portion of the main body of the ingot. Accordingly, in some embodiments, the volume of molten silicon varies by no more than about 1.0 volume % during growth of at least about 90% the main body of the single crystal silicon ingot, or by no more than about 0.5 volume % during growth of at least about 90% the main body of the single crystal silicon ingot, or even by no more than about 0.1 volume % during growth of at least about 90% the main body of the single crystal silicon ingot. Stated another way, in some embodiments, the melt elevation level varies by less than about +/- 0.5 millimeter during growth of at least about 90% the main body of the single crystal silicon ingot.

[0049] Additionally, according to the process of the present invention, a magnetic field may be applied to the crucible comprising the silicon melt. Either cusp or horizontal magnet field can be applied to set the appropriate crystal/melt interface, i.e., the shape and height of the meniscus. The magnetic field is used to fix a desired crystal/melt interface shape and height primarily, and control of the oxygen content, O_i , is a subordinate purpose.

[0050] Control of the melt flow and the shape of the melt/solid interface and therefore the quality of the ingot may be enhanced by the application of a magnetic field to the silicon melt during growth of the main body of the single crystal silicon ingot. In some embodiments, the applied magnetic field maintains a substantially constant melt/solid interface profile during at least about 70% of the growth of the main body of the single crystal silicon ingot, or between about 70% and about 90% of the growth of the main body of the single crystal silicon ingot. The magnetic field applies electromagnetic force, which affects the silicon melt flow, so the heat transfer in the melt is affected. It changes the profile of crystal/melt interface and the temperature of growing crystal, which are the key control parameter of perfect silicon.

[0051] The magnetic field impacts the oxygen content and uniformity in the ingot. The source of oxygen in the ingot is from dissolution of the quartz crucible wall, evaporation SiO_x (g) at the melt free surface (controlled by melt flow kinetics) and incorporation into growing crystal front. The magnet field impacts the convective melt flow during growth which can impact Oxygen evaporation and incorporation. The variation of oxygen incorporation into the single crystal silicon ingot by time increment is controlled by the diffusion and convection of oxygen in the melt according to the following equation:

$$\frac{\partial C}{\partial t} = \nabla C - v\rho + \text{SOURCE}.$$

[0052] C is the concentration of oxygen in the solidifying silicon, t is time, v is the convection velocity (melt flow velocity), ρ , is the density of silicon melt, ∇ is the gradient (d/dx). The applied magnetic field affects the melt velocity (v) and the gradient of oxygen concentration in the melt ($dC/dx = \nabla C$). Since magnetic field results in a steady state melt flow, the incorporating of oxygen, O_i , into the ingot is time constant, which enhances radial and axial oxygen concentration uniformity. The SOURCE term is derived from two parameters, the dissolution of quartz (SiO_2) crucible which is the generation of oxygen ($\text{Si (l)} + \text{SiO}_2(\text{s}) \rightarrow \text{SiO}_x(\text{g})$), and the evaporation which is the removal (disappearance) of oxygen ($\text{SiO}_x(\text{g})$) from melt. In a batch Cz process, this SOURCE term is not constant. Instead, it depends upon the crystal length since the melt mass decreases as the crystal is grown. When the ingot has grown a substantial portion

of its body length, the remaining melt volume is low, so that that amount of silicon melt in contact with the crucible is decreased, which therefore leads to lower concentrations of oxygen incorporated from the crucible into the melt. Therefore, the oxygen incorporated into solidifying silicon crystal is decreased, if other terms (diffusion, convection, evaporation) are constant. The melt free surface (contact surface between melt and gas) area affects the evaporation rate of SiOx(g) . A small melt mass in a batch Cz process has relatively smaller surface area due to shape of crucible as shown in Fig 1C. Less evaporation of SiOx(g) means more oxygen incorporation into the solidifying silicon crystal. According to the method of the present invention, the melt mass is maintained as constant since polysilicon is added as the crystal ingot grows. Accordingly, all source terms (generation of Oxygen by SiO_2 crucible dissolution into melt, and evaporation of SiOx(g) gas through melt free surface) are constant. Therefore, the diffusion and convection terms affect the oxygen of solidifying silicon crystal. The applied magnetic field makes melt flow more stable (i.e., melt flow is constant like as time independent steady condition), so incorporating Oxygen is uniform and stable in the axial and radial directions during growth of the entire length of the ingot. In some embodiments, interstitial oxygen may be incorporated into the ingot in a concentration between about 4 PPMA and about 18 PPMA. In some embodiments, interstitial oxygen may be incorporated into the ingot in a concentration between about 10 PPMA and about 35 PPMA. In some embodiments, the ingot comprises oxygen in a concentration of no greater than about 15 PPMA, or no greater than about 10 PPMA. Interstitial oxygen may be measured according to SEMI MF 1188-1105.

[0053] In some embodiments, a horizontal magnetic field is applied to the silicon melt during growth of the main body of the single crystal silicon ingot. Such a horizontal field is illustrated in FIG. 7A, which is a depiction of a horizontal magnetic field superimposed over the crucible and growing ingot as depicted in FIG. 5. Crystal growth in presence of a horizontal magnetic field is achieved by placing the crucible holding the silicon melt between the poles of a conventional electromagnet 200. In some embodiments, the horizontal magnetic field may have a magnetic flux density between about 0.2 Tesla and about 0.4 Tesla in the melt area. Magnetic field variation in the melt is less than +/- about 0.03 Tesla in a given strength. Application of a horizontal

magnetic field gives rise to Lorentz force along axial direction, in a direction opposite of fluid motion, opposing forces driving melt convection. The convection in the melt is thus suppressed, and the axial temperature gradient in the crystal near the interface increases. The melt-crystal interface then moves upward to the crystal side to accommodate the increased axial temperature gradient in the crystal near the interface and the contribution from the melt convection in the crucible decreases.

[0054] In some embodiments, a cusp magnetic field is applied to the silicon melt during growth of the main body of the single crystal silicon ingot. A cusp field is illustrated in FIG. 7B, which is a depiction of a cusp magnetic field superimposed over the crucible and growing ingot as depicted in FIG. 5. A cusp magnetic field has two controlling parameters, namely the magnetic flux density and magnetic field shape. A cusp magnetic field applies a horizontal (radial) magnetic field component at the nearest surface of the melt combined with a vertical (axial) magnetic field deeper in the melt near the axis of the ingot. The cusp magnetic field is generated using a pair of Helmholtz coil carrying current in opposite direction. As a result, at the position halfway between the two magnetic fields, vertically along the ingot axis, the magnetic fields cancel each other out to make a vertical magnetic field component at or near zero. For example, the cusp magnetic flux density is typically about zero to about 0.2 Tesla in the axial direction. The magnetic flux density in the radial direction is generally higher than the magnetic flux density in the vertical direction. For example, the cusp magnetic flux density is typically between about 0 and about 0.6T in the radial position, such as between about 0.2 and about 0.5T, dependent upon the radial position. The radial cusp magnetic field restrains the flow of the melt, to thereby stabilize the melt. In other words, application of a radial cusp magnetic field induces convection at a portion adjacent to the solid- liquid interface at which crystal growth occurs, and suppresses convection at the remaining portions of the melt, to thereby serve as an effective method for realizing uniform oxygen distribution. Thermal melt convection can be locally and independently controlled by the cusp magnetic field at the melt free surface and at the melt crucible interface at the same time. This enables to control the oxygen concentration in the growing crystal by magnetic flux density only, irrespective of crystal rotation speed. In presence of an axial or a radial magnetic field, control of oxygen concentration is

achieved via control of crystal rotation speed. Application of the cusp magnetic field may enable growth on an ingot comprising less oxygen content than an ingot grown without an applied magnetic field, such as no greater than about 15 PPMA, or no greater than about 10 PPMA. Interstitial oxygen may be measured according to SEMI MF 1188-1105.

[0055] The method of the present invention enables growth of single crystal silicon ingots by the continuous Czochralski method meeting or exceeding industry specifications for perfect silicon. Factors contributing to the growth of perfect silicon crystals include determining the critical pull speed for growing perfect silicon, maintaining a constant pull speed at the critical speed over a substantial portion of the growth of the main body of the single crystal silicon ingot, and applying a magnetic field to maintain the shape and height of the melt/solid interface. Once the configurations of pull speed and magnetic field are determined, the pull speed may be maintained at a constant rate due to the continuous addition of polycrystalline silicon to maintain a constant melt volume and melt elevation level. Accordingly, unlike conventional methods of growing ingots, the pull speed is constant over a substantial portion of the growth of the ingot. In view of the process controls enabled by the method disclosed herein, the method of the present invention enables the growth of a single crystal silicon ingot comprising perfect silicon over at least about 70% of the length of the main body of the single crystal silicon ingot, such as over at least about 80% of the length of the main body of the single crystal silicon ingot, or even over at least about 90% of the length of the main body of the single crystal silicon ingot.

[0056] As used herein, the terms “about,” “substantially,” “essentially” and “approximately” when used in conjunction with ranges of dimensions, concentrations, temperatures or other physical or chemical properties or characteristics is meant to cover variations that may exist in the upper and/or lower limits of the ranges of the properties or characteristics, including, for example, variations resulting from rounding, measurement methodology or other statistical variation.

[0057] When introducing elements of the present disclosure or the embodiment(s) thereof, the articles “a”, “an”, “the” and “said” are intended to mean that

there are one or more of the elements. The terms "comprising," "including," "containing" and "having" are intended to be inclusive and mean that there may be additional elements other than the listed elements. The use of terms indicating a particular orientation (e.g., "top", "bottom", "side", etc.) is for convenience of description and does not require any particular orientation of the item described.

[0058] As various changes could be made in the above constructions and methods without departing from the scope of the disclosure, it is intended that all matter contained in the above description and shown in the accompanying drawing[s] shall be interpreted as illustrative and not in a limiting sense.

WHAT IS CLAIMED IS:

1. A method of preparing a single crystal silicon ingot by the Continuous Czochralski method, the method comprising:

adding an initial charge of polycrystalline silicon to a crucible;

heating the crucible comprising the initial charge of polycrystalline silicon to cause a silicon melt to form in the crucible, the silicon melt comprising an initial volume of molten silicon and having an initial melt elevation level;

contacting a silicon seed crystal with a silicon melt;

withdrawing the silicon seed crystal to grow a neck portion, wherein the silicon seed crystal is withdrawn at a neck portion pull rate during growth of the neck portion;

withdrawing the silicon seed crystal to grow an outwardly flaring seed-cone adjacent the neck portion, wherein the silicon seed crystal is withdrawn at a seed-cone pull rate during growth of the outwardly flaring seed-cone; and

withdrawing the silicon seed crystal to grow a main body of the single crystal silicon ingot adjacent the outwardly flaring seed-cone, wherein the silicon melt comprises a volume of molten silicon and a melt elevation level during growth of the main body of the single crystal silicon ingot;

wherein the main body of the single crystal silicon ingot is grown at an initial variable main body pull rate and a constant main body pull rate, wherein the main body of the single crystal silicon ingot is grown at the initial variable main body pull rate for less than about 20% of a length of the main body of the single crystal silicon ingot and grown at the constant main body pull rate during growth for at least about 30% of the length of the main body of the single crystal silicon ingot; and

further wherein polycrystalline is continuously feed to the crucible to thereby replenish a volume of molten silicon and melt elevation level in the crucible during growth of the main body of the single crystal silicon ingot.

2. The method of claim 1 wherein a magnetic field is applied to the silicon melt during growth of the main body of the single crystal silicon ingot.

3. The method of claim 2 wherein a horizontal magnetic field is applied to the silicon melt during growth of the main body of the single crystal silicon ingot.
4. The method of claim 2 wherein a cusp magnetic field is applied to the silicon melt during growth of the main body of the single crystal silicon ingot.
5. The method of claim 2 wherein the applied magnetic field maintains a substantially constant melt/solid interface profile during at least about 70% of the growth of the main body of the single crystal silicon ingot.
6. The method of claim 2 wherein the applied magnetic field maintains a substantially constant melt/solid interface profile during between about 70% and about 90% of the growth of the main body of the single crystal silicon ingot.
7. The method of any one of claims 1 through 6 wherein the main body of the single crystal silicon ingot is at least about 1000 millimeters long, at least 1400 millimeters long, or at least 1500 millimeters long.
8. The method of any one of claims 1 through 6 wherein the main body of the single crystal silicon ingot is at least 2000 millimeters long, at least 2200 millimeters long, at least about 3000 millimeters long, or at least about 4000 millimeters long.
9. The method of any one of claims 1 through 8 wherein the main body of the single crystal silicon ingot has a diameter of at least about 150 millimeters, or at least about 200 millimeters.
10. The method of any one of claims 1 through 8 wherein the main body of the single crystal silicon ingot has a diameter of at least about 300 millimeters, or at least about 450 millimeters.
11. The method of any one of claims 1 through 10 wherein the constant main body pull rate is between about 0.4 mm/min and about 0.8 mm/min, between about 0.4 mm/min and about 0.7 mm/min, or between about 0.4 mm/min and about 0.65 mm/min.

12. The method of any one of claims 1 through 11 wherein the main body of the single crystal silicon ingot is grown at the initial variable main body pull rate for between about 5% and about 20% of the length of the main body of the single crystal silicon ingot.

13. The method of any one of claims 1 through 12 wherein the main body of the single crystal silicon ingot is grown at the constant main body pull rate during growth for at least about 50% of the length of the main body of the single crystal silicon ingot.

14. The method of any one of claims 1 through 12 wherein the main body of the single crystal silicon ingot is grown at the constant main body pull rate during growth for at least about 70% of the length of the main body of the single crystal silicon ingot.

15. The method of any one of claims 1 through 12 wherein the main body of the single crystal silicon ingot is grown at the constant main body pull rate during growth for at least about 80% of the length of the main body of the single crystal silicon ingot.

16. The method of any one of claims 1 through 12 wherein the main body of the single crystal silicon ingot is grown at the constant main body pull rate during growth for at least about 90% of the length of the main body of the single crystal silicon ingot.

17. The method of any one of claims 1 through 16 wherein the main body pull rate is a constant critical pull rate sufficient to avoid agglomerated point defects over at least 70% of the length of the main body of the single crystal silicon ingot.

18. The method of any one of claims 1 through 16 wherein the main body pull rate is a constant critical pull rate sufficient to avoid agglomerated point defects over at least 90% of the length of the main body of the single crystal silicon ingot.

19. The method of any one of claims 1 through 18 wherein the volume of molten silicon varies by no more than about 1.0 volume % during growth of at least about 90% the main body of the single crystal silicon ingot.

20. The method of any one of claims 1 through 18 wherein the volume of molten silicon varies by no more than about 0.5 volume % during growth of at least about 90% the main body of the single crystal silicon ingot.

21. The method of any one of claims 1 through 18 wherein the volume of molten silicon varies by no more than about 0.1 volume % during growth of at least about 90% the main body of the single crystal silicon ingot.

22. The method of any one of claims 1 through 18 wherein the melt elevation level varies by less than about ± 0.5 millimeter during growth of at least about 90% the main body of the single crystal silicon ingot.

23. The method of any one of claims 1 through 22 wherein the main body of the single crystal silicon ingot comprises perfect silicon over at least about 70% of the length of the main body of the single crystal silicon ingot.

24. The method of any one of claims 1 through 22 wherein the main body of the single crystal silicon ingot comprises perfect silicon over at least about 80% of the length of the main body of the single crystal silicon ingot.

25. The method of any one of claims 1 through 22 wherein the main body of the single crystal silicon ingot comprises perfect silicon over at least about 90% of the length of the main body of the single crystal silicon ingot.