

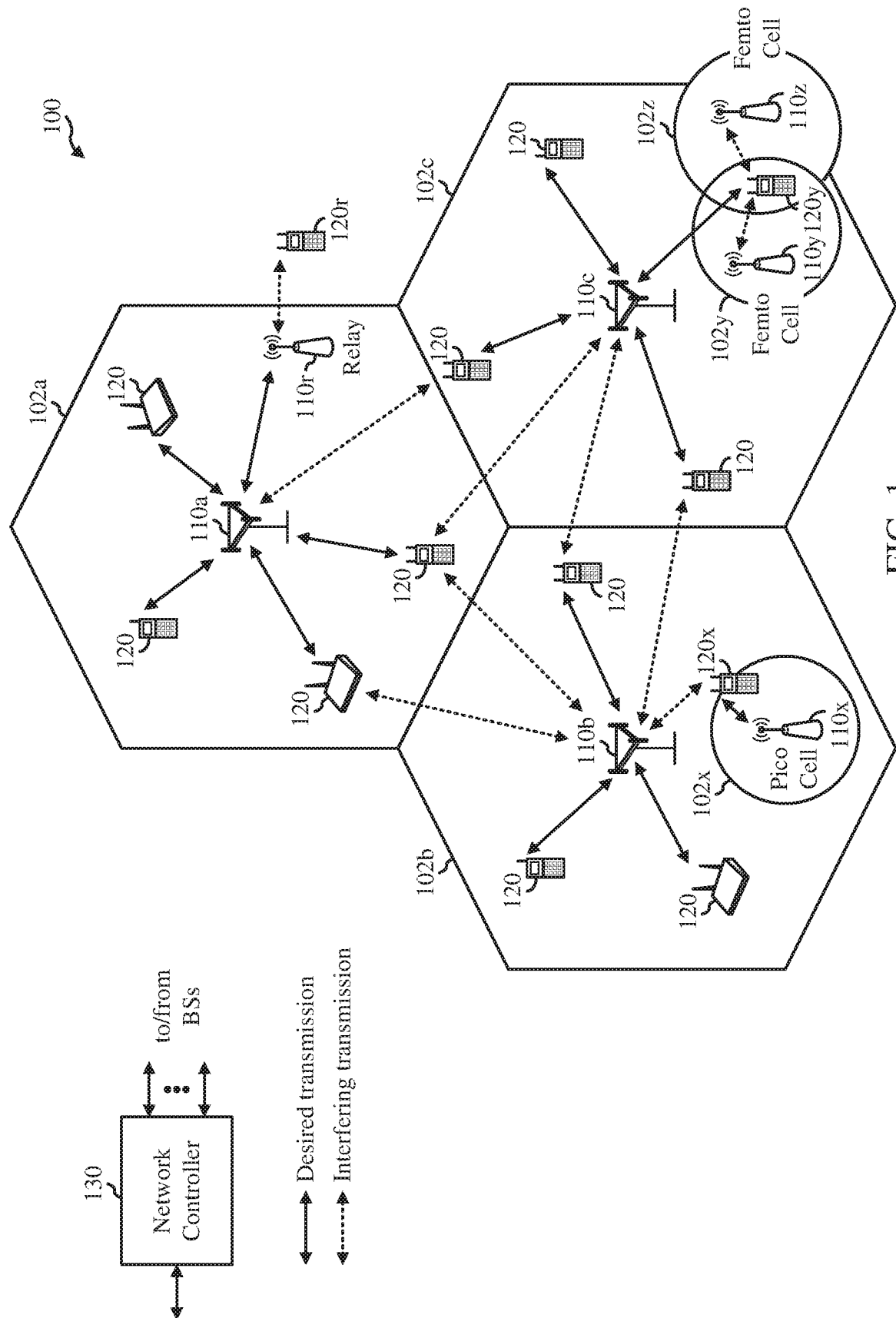


FIG. 1

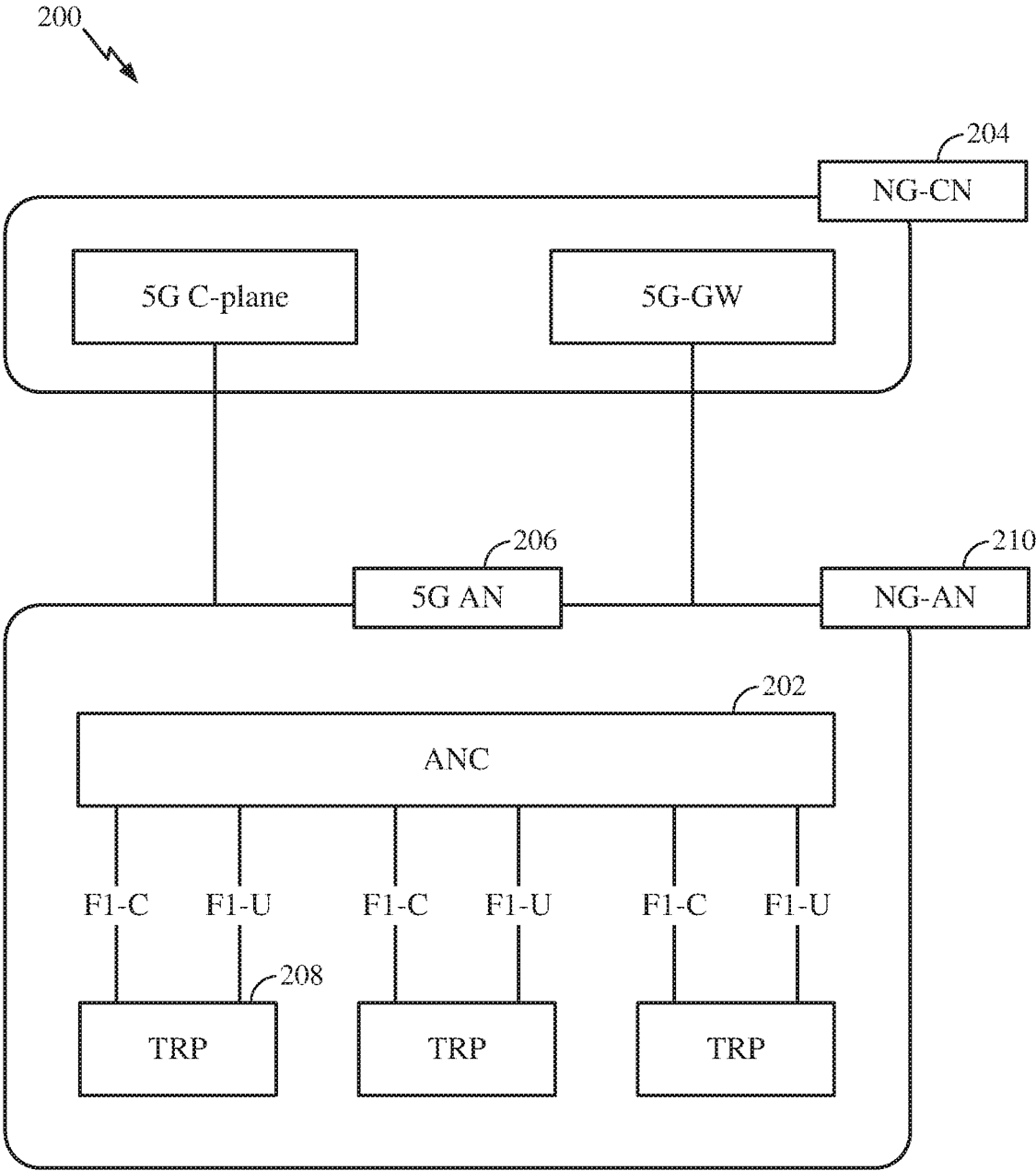


FIG. 2

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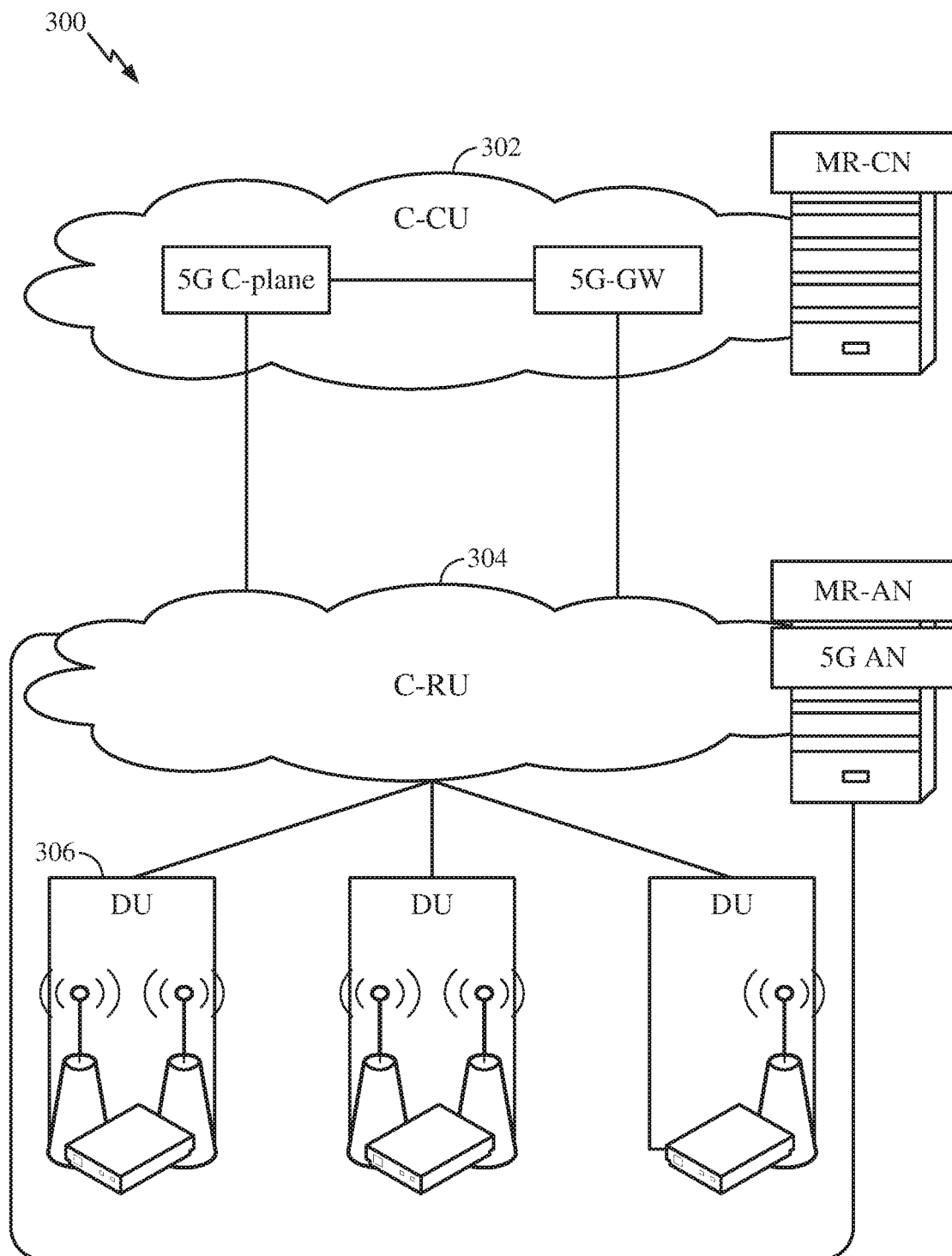


FIG. 3

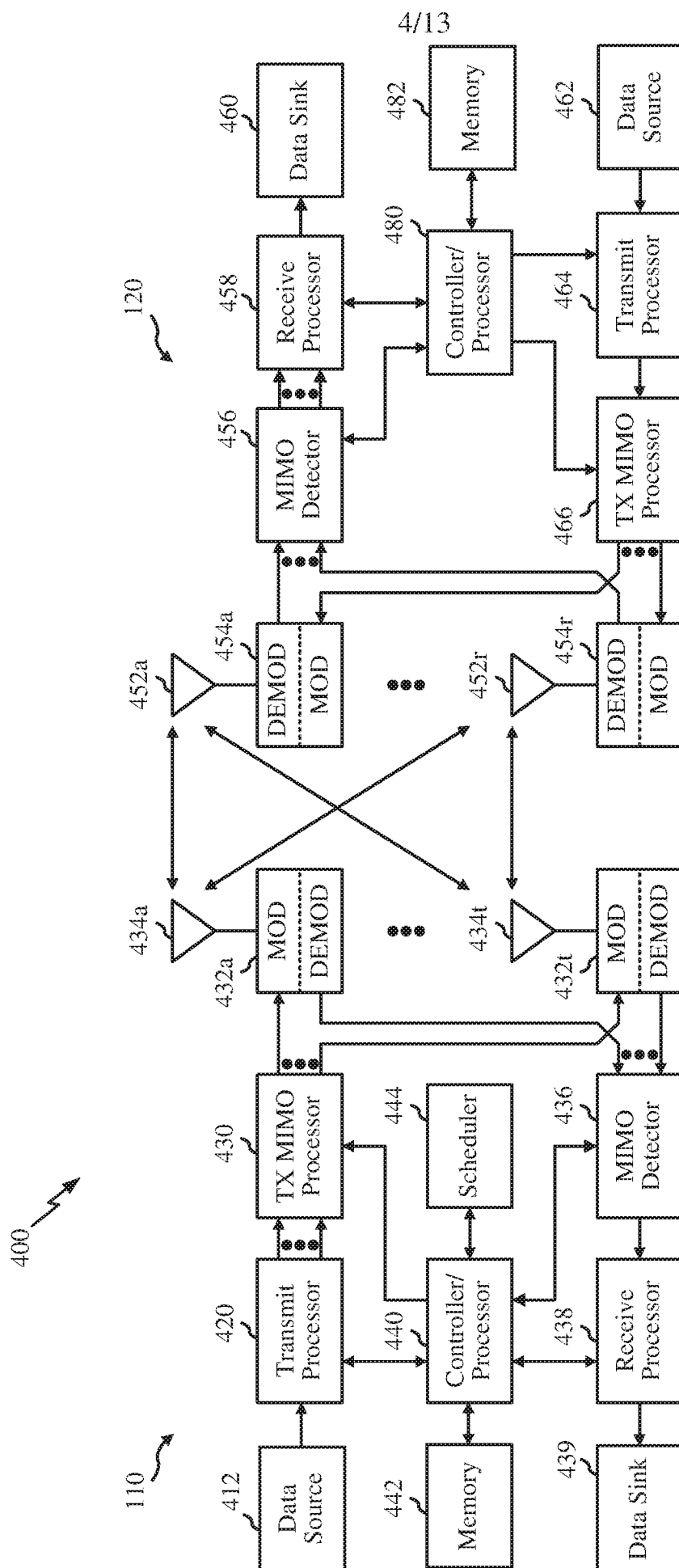


FIG. 4

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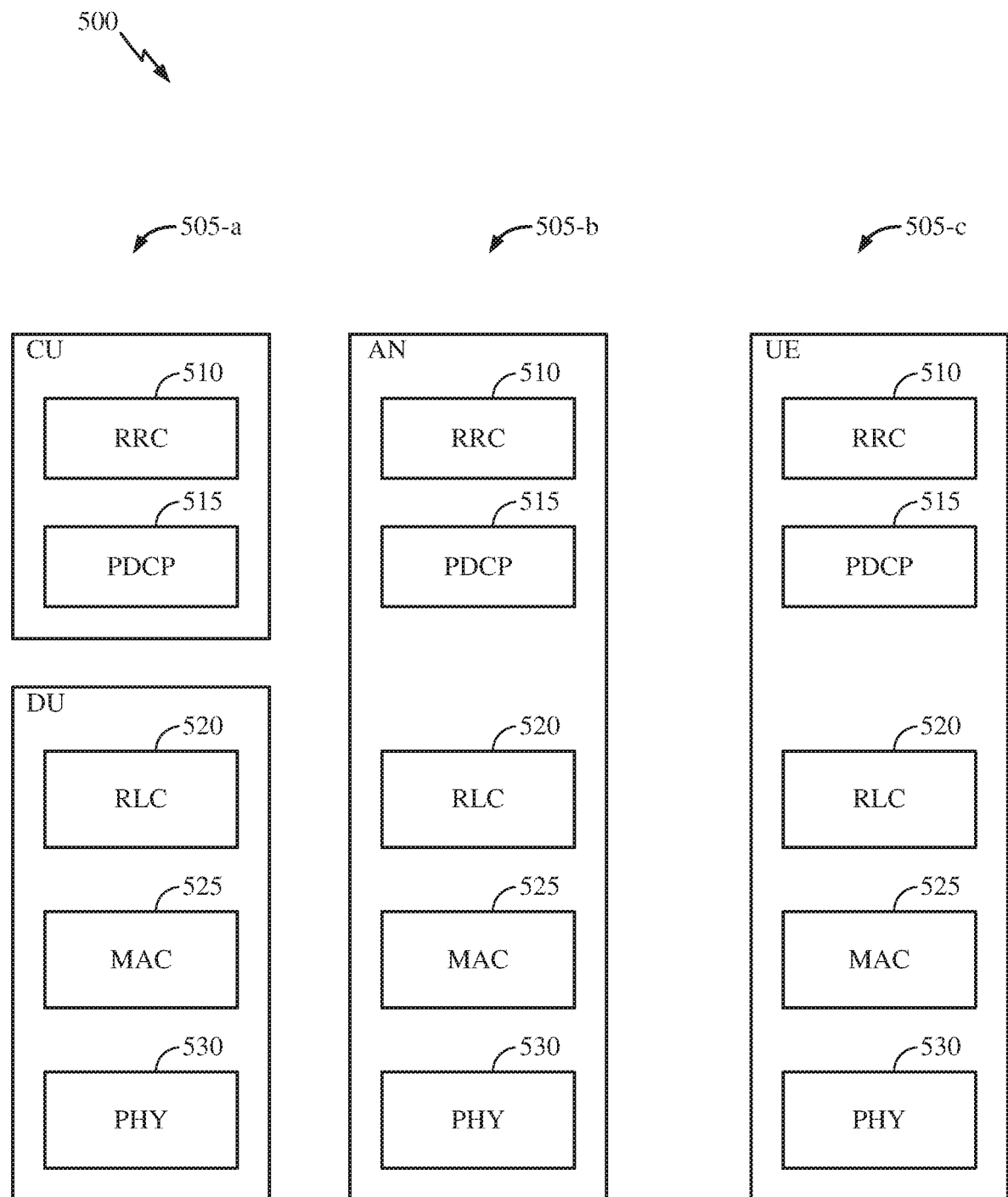
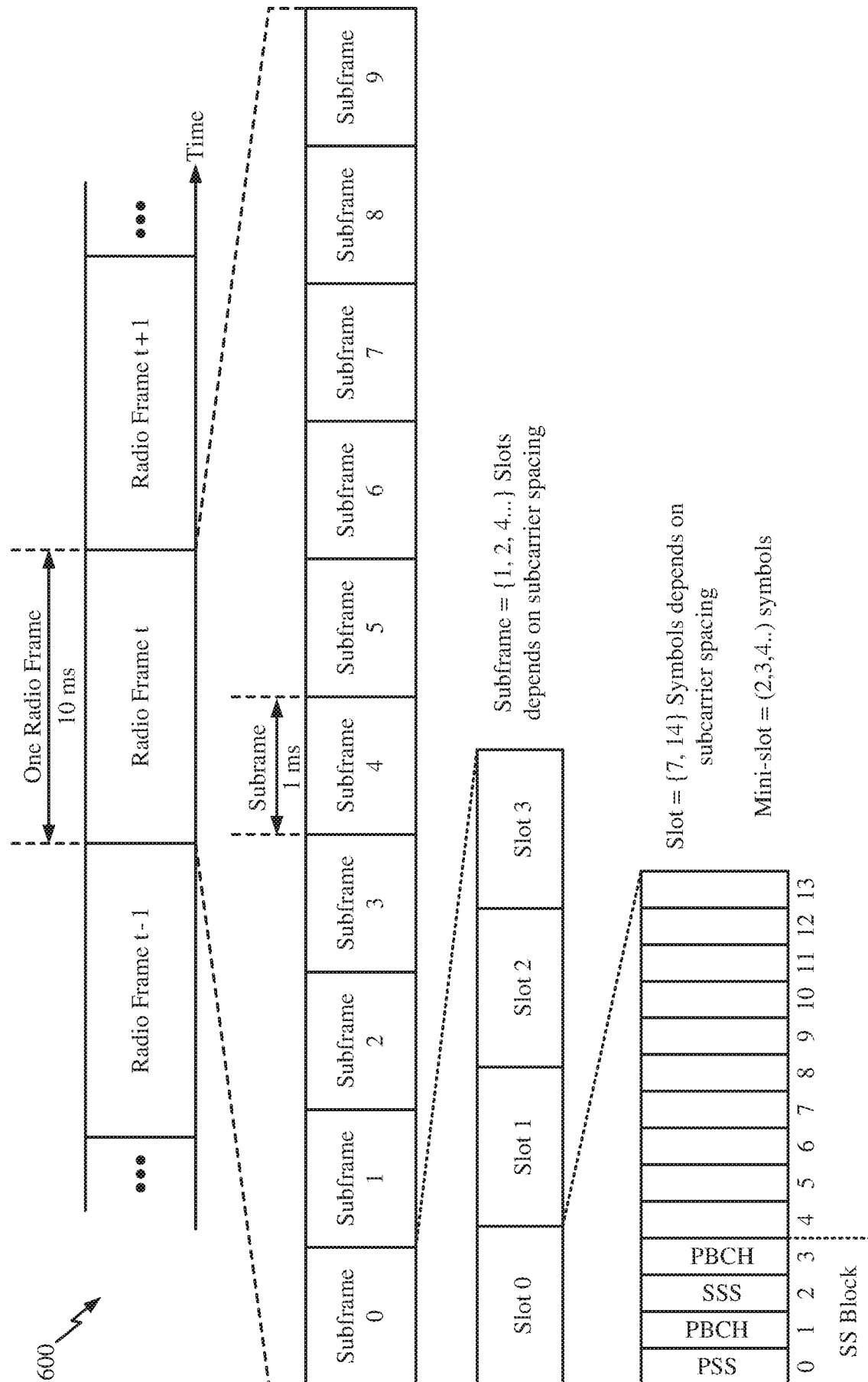


FIG. 5



6
30
G
H
L

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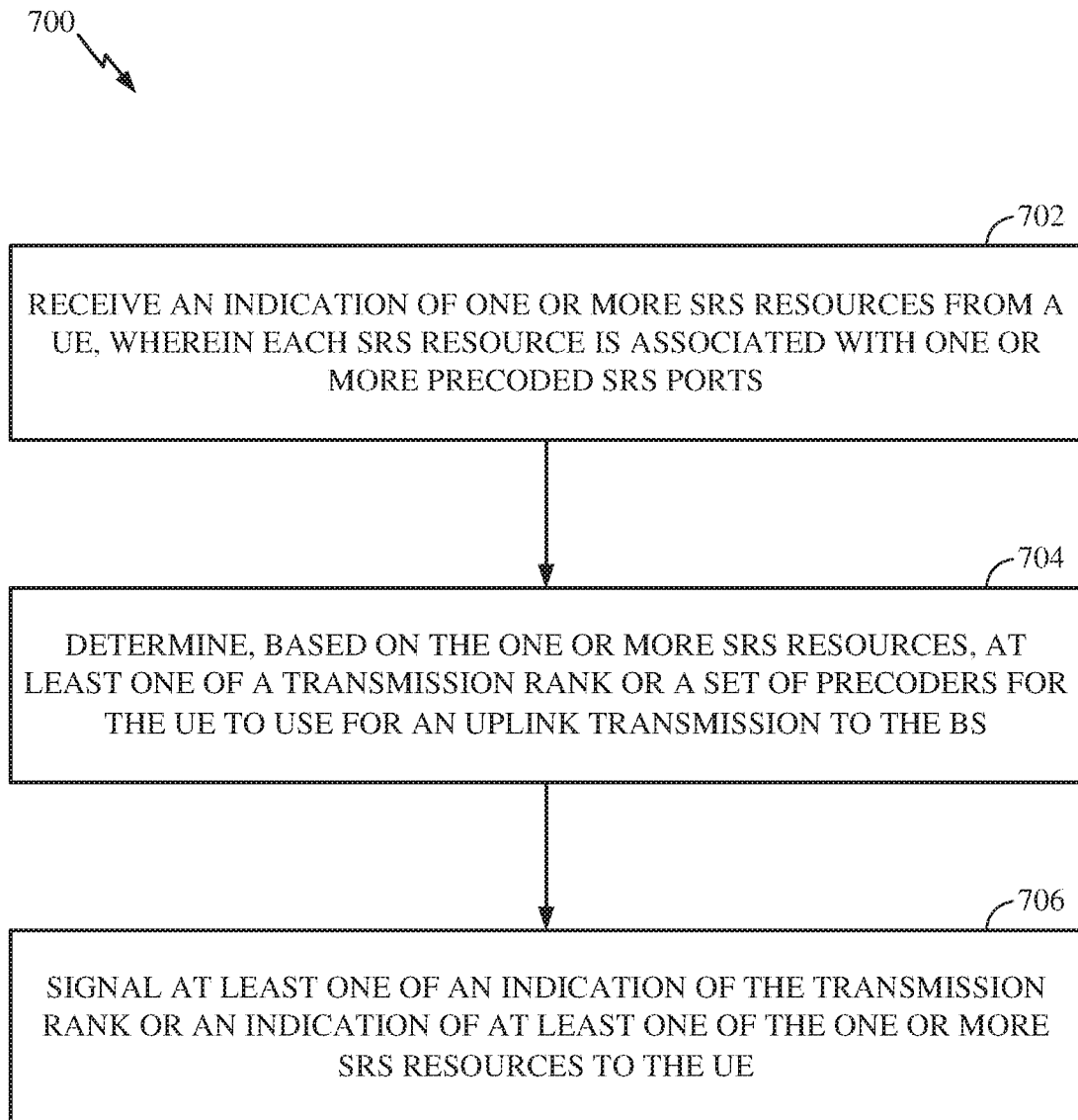


FIG. 7

	TRI=0	TRI=1	TRI=2	TRI=3
SRI=0	0	0,1	0,1,2	0,1,2,3
SRI=1	1	1,2	0,1,3	
SRI=2	2	0,3	0,1,4	
SRI=3	3	1,3	1,2,3	

FIG. 8

	TRI=0	TRI=1	TRI=2	TRI=3
SRI=0	0	0,1	0,1,2	0,1,2,3
SRI=1	0	1,2		

FIG. 9

Index	0	1	2	3	4	5	6	7
TRI	0	0	0	0	1	1	2	3
SRI	0	1	2	3	0	1	0	0

FIG. 10A

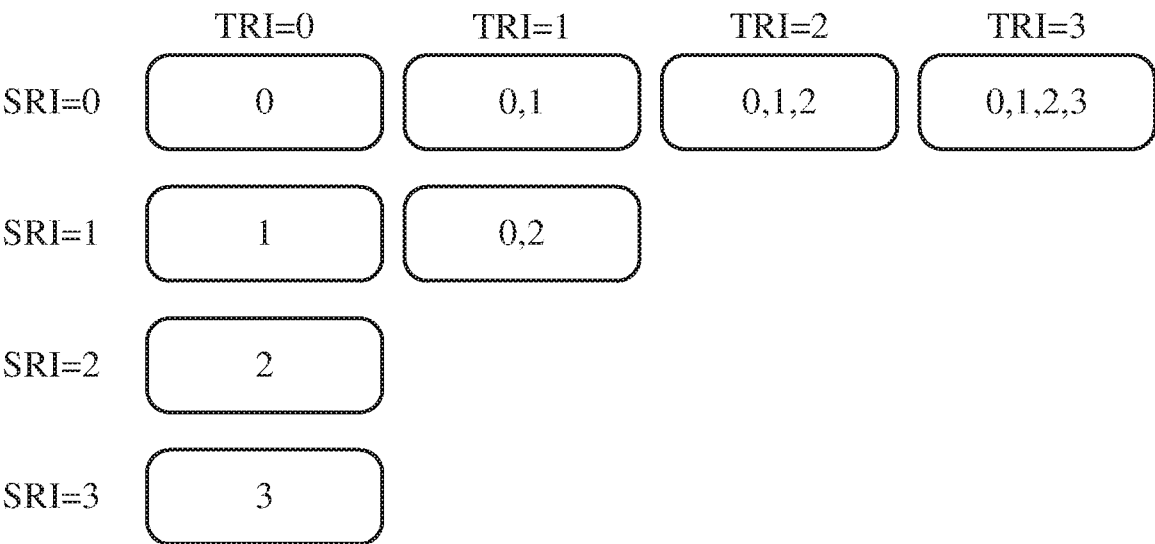


FIG. 10B

SRS group 1		SRS group 2				PUSCH signaling for SRS group 1					PUSCH signaling for SRS group 2					
Slot index	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15

FIG. 11

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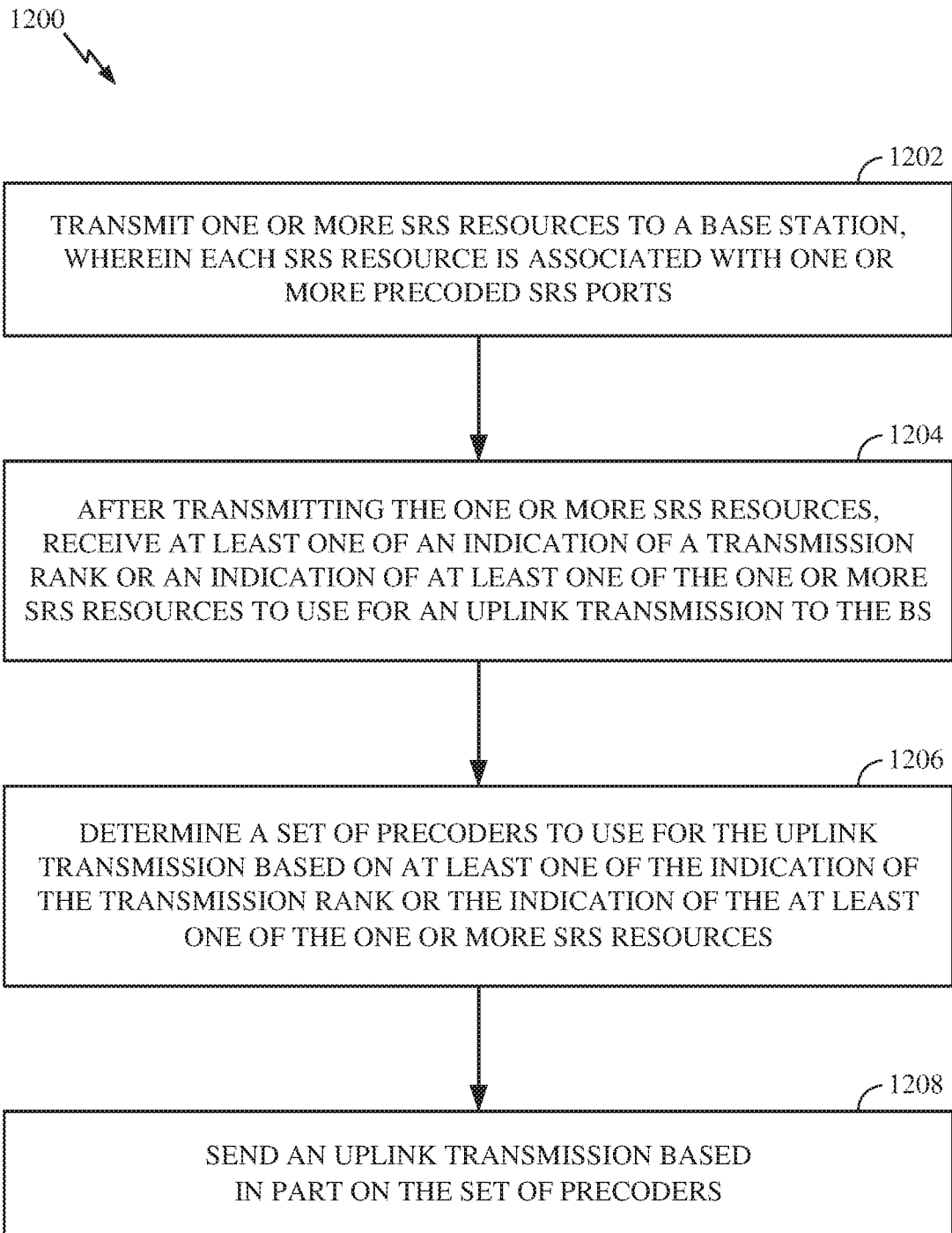


FIG. 12

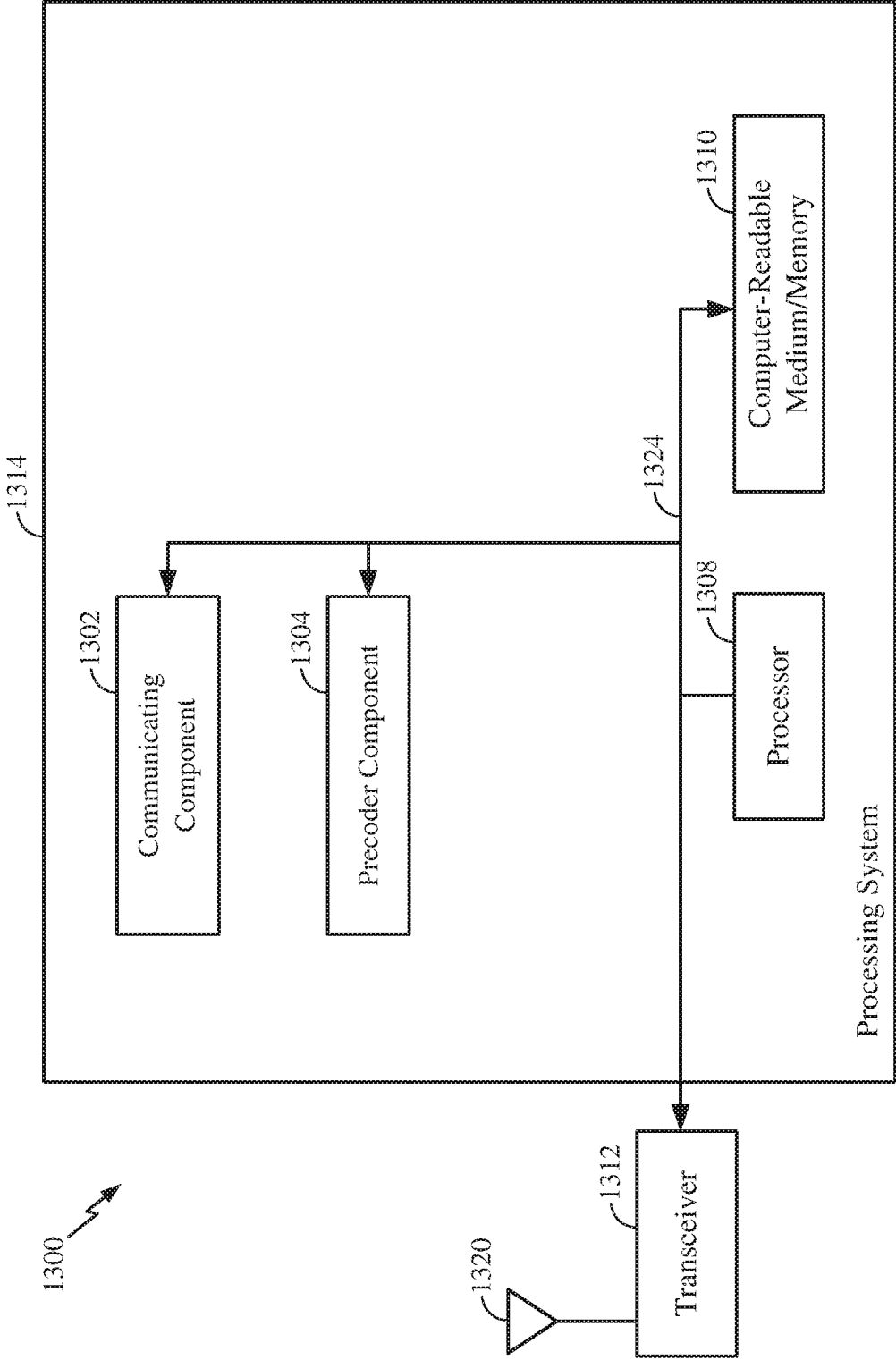


FIG. 13