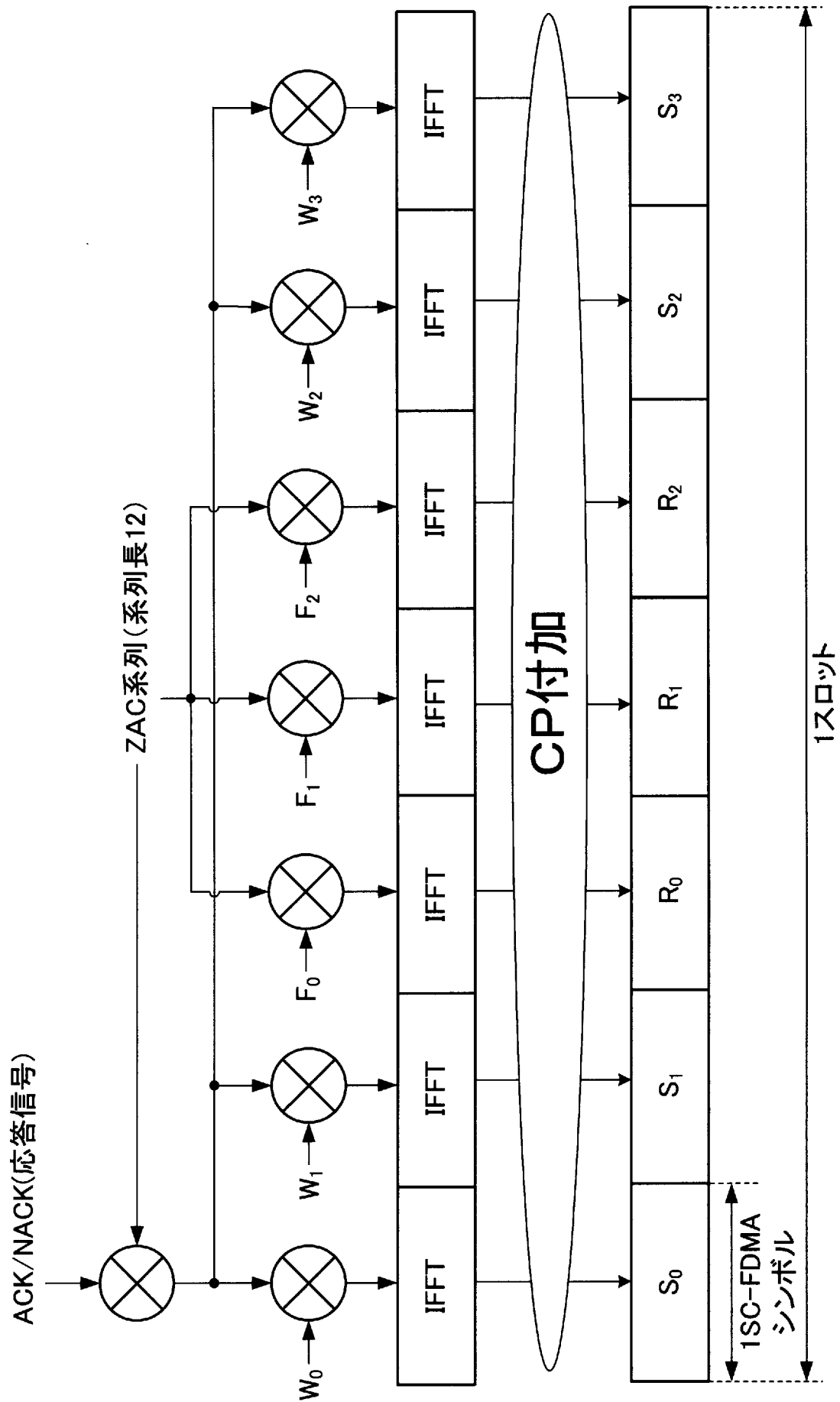
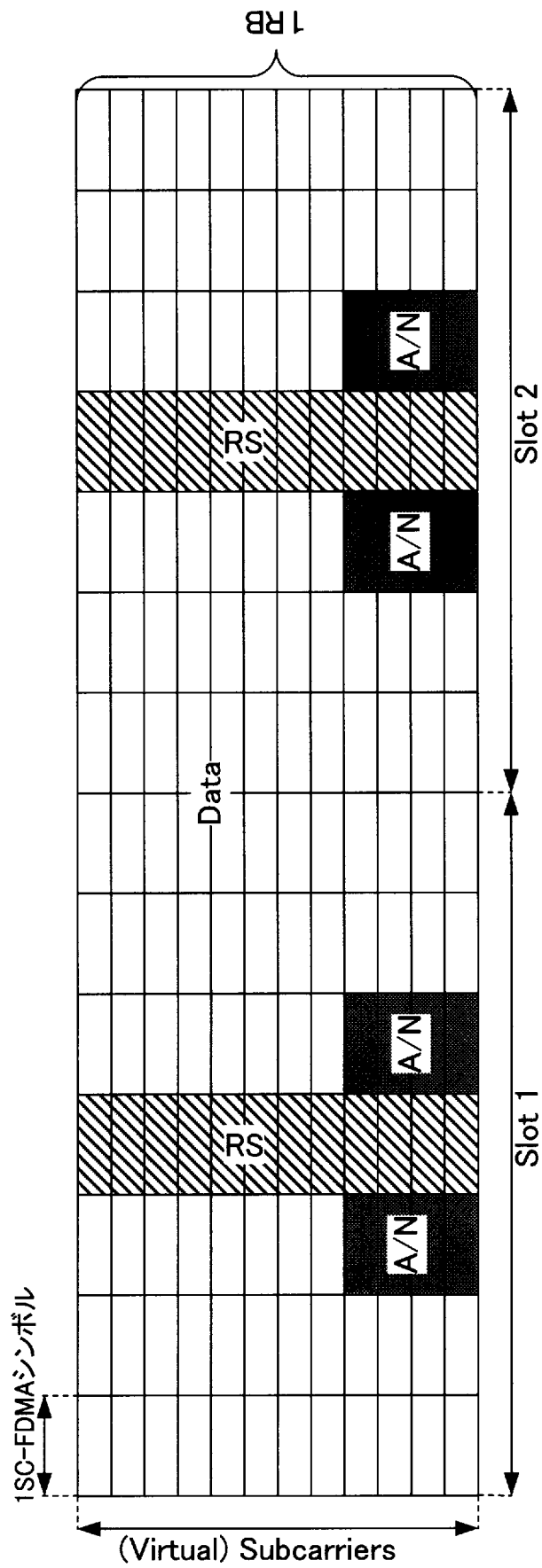


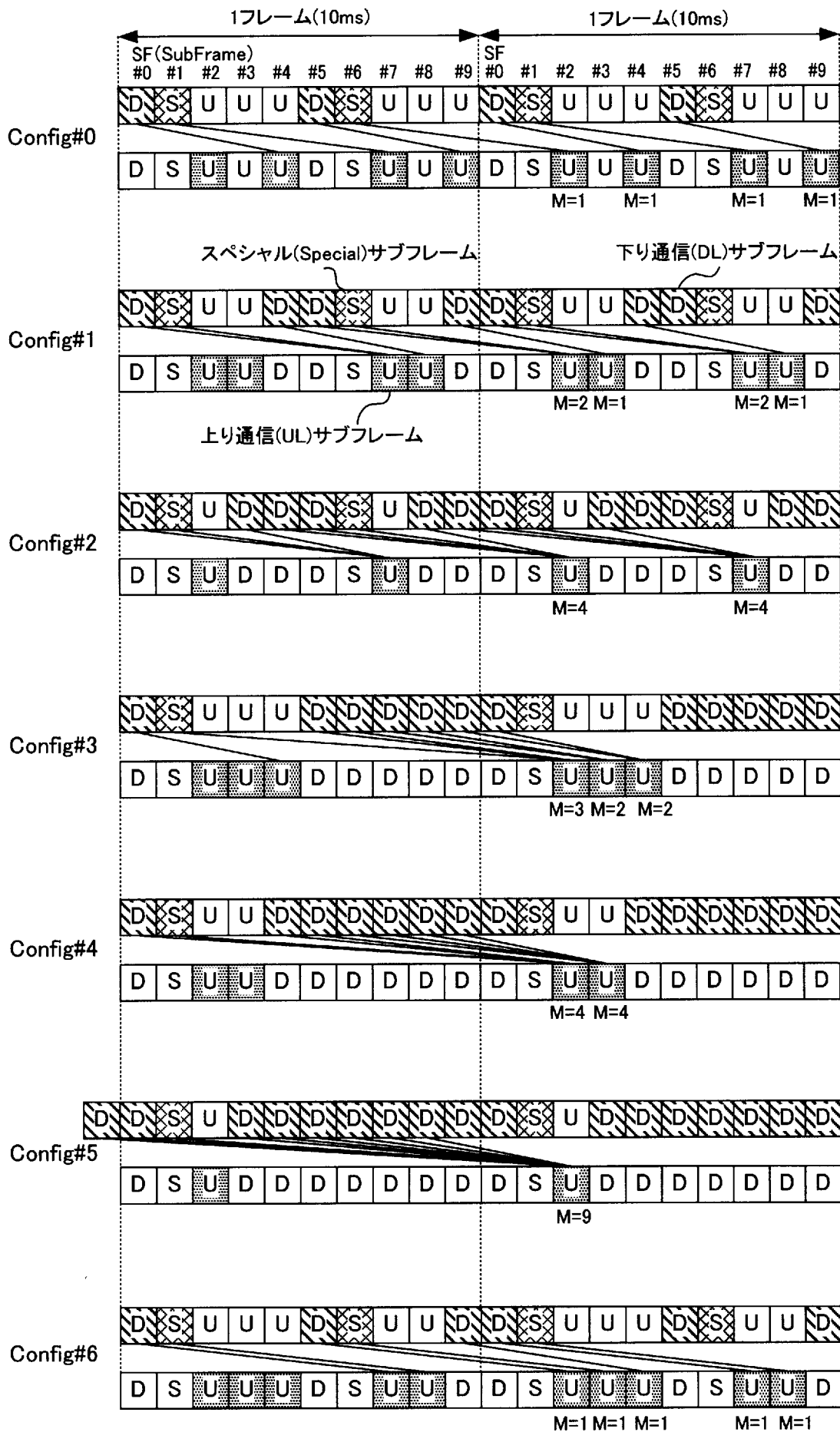
[図1]



[図2]



[図3]



[図4]

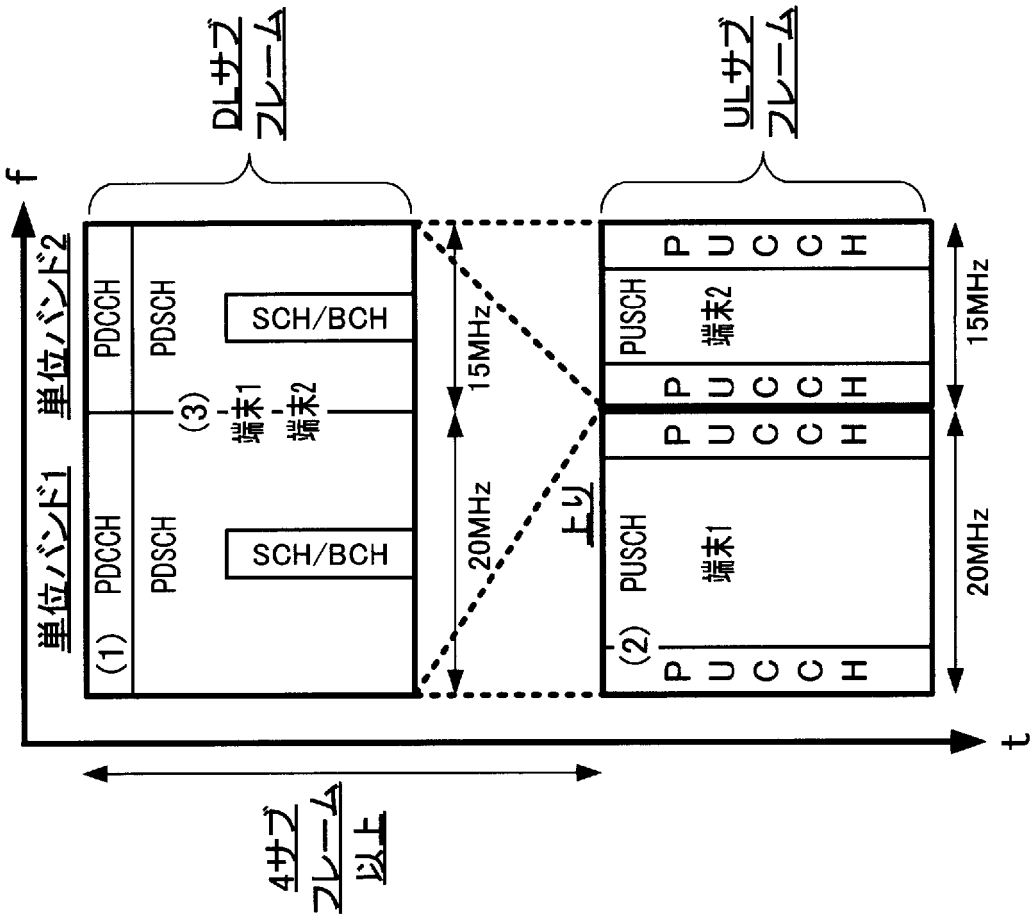


図4B

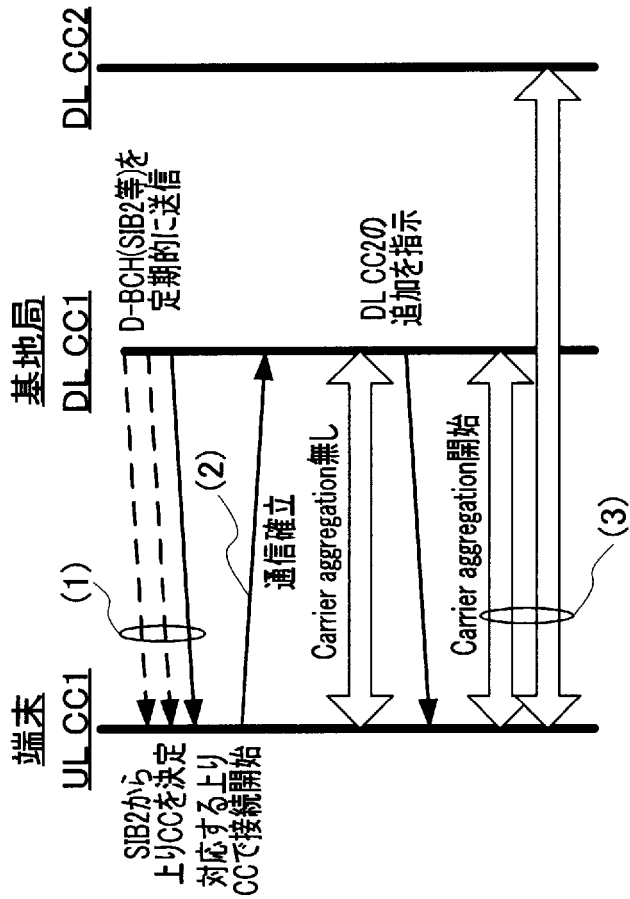
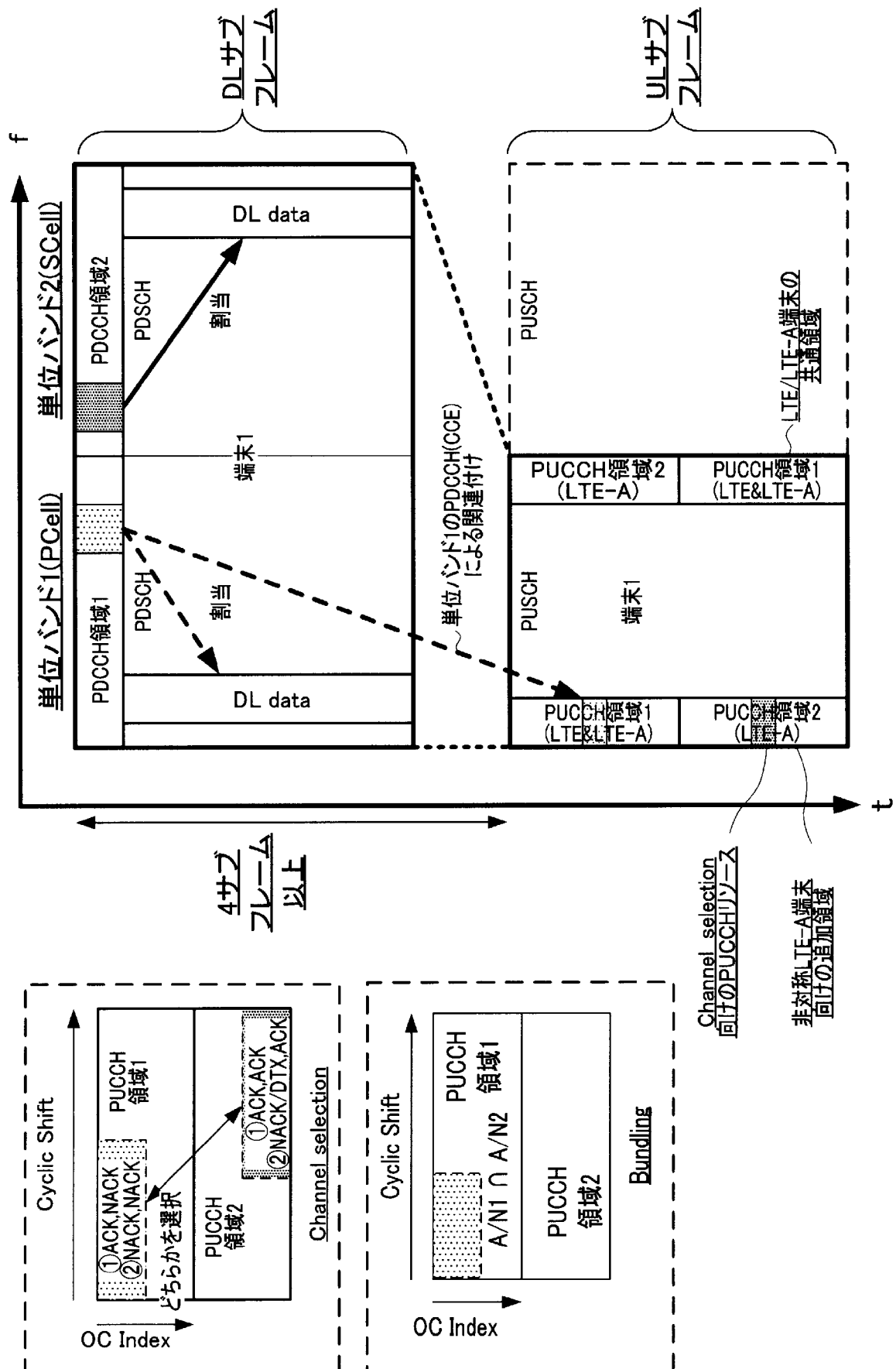
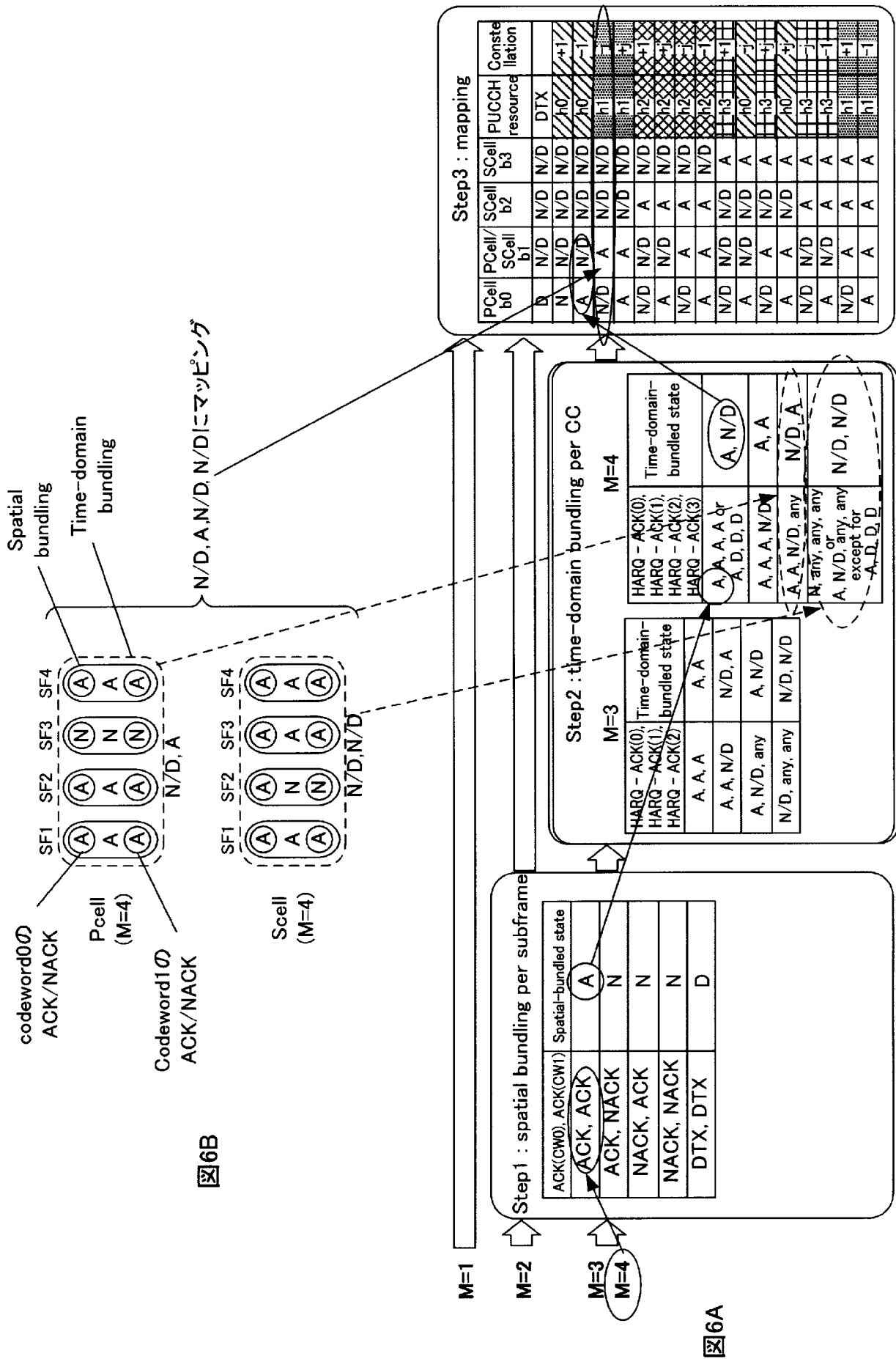


図4A

[図5]



[図6]



[図6A]

[図6B]

[図7]

UL-DL Config	最大DL HARQプロセス数 M_{DLHARQ}
0	4
1	7
2	10
3	9
4	12
5	15
6	6

図7B

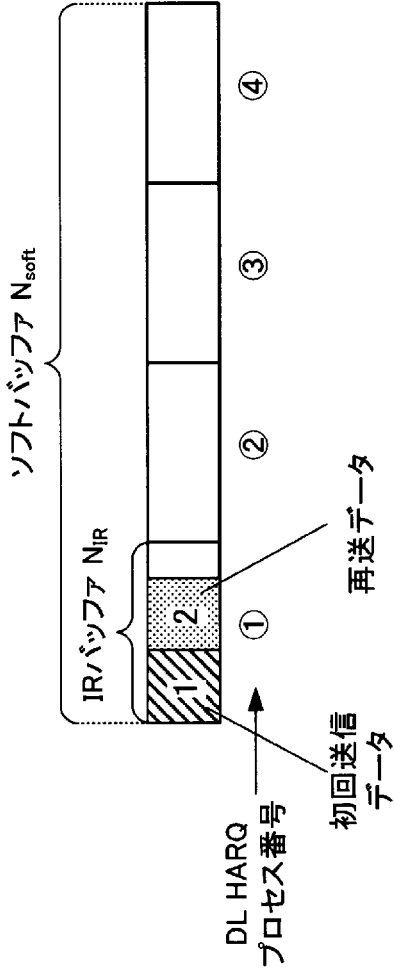
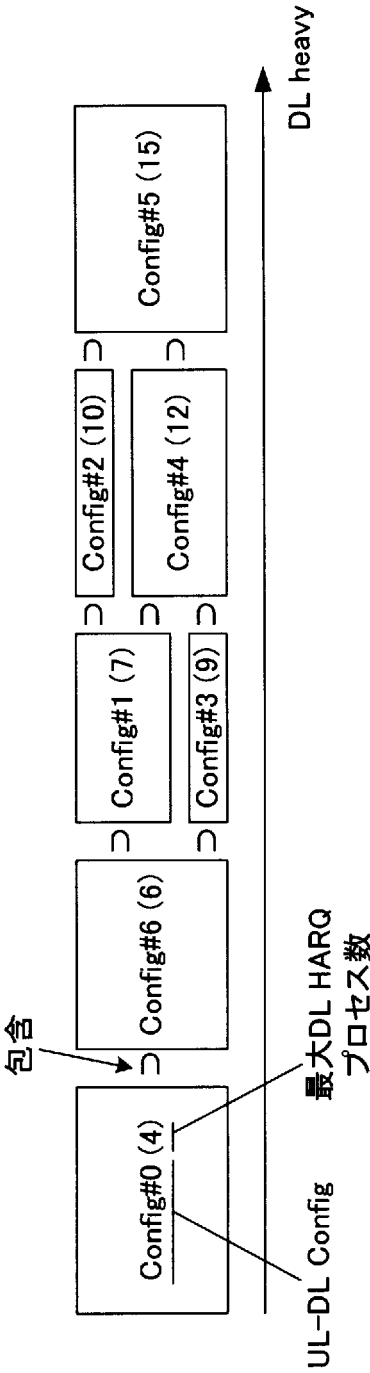
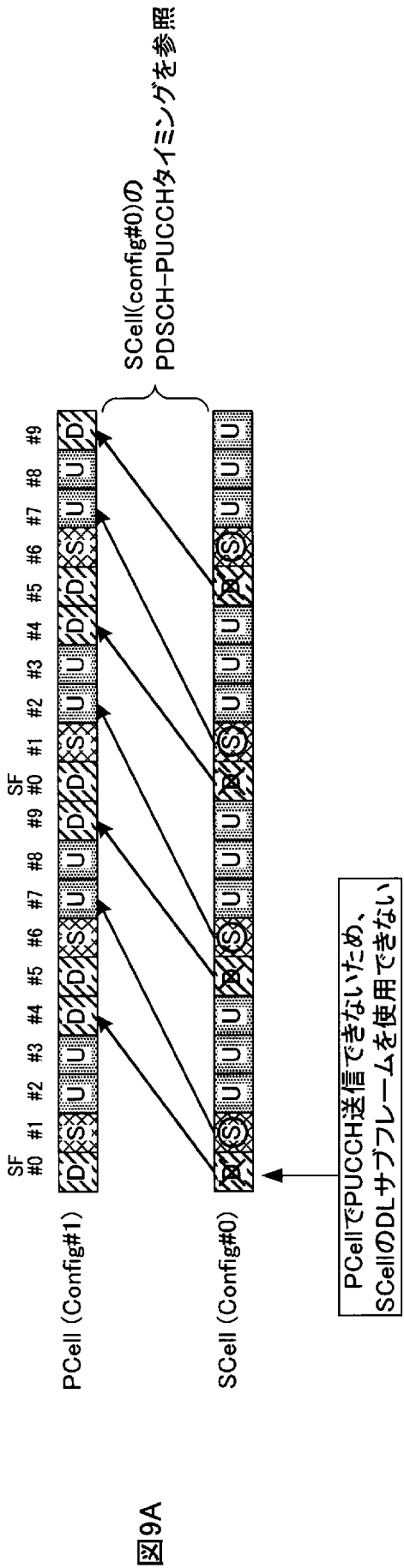
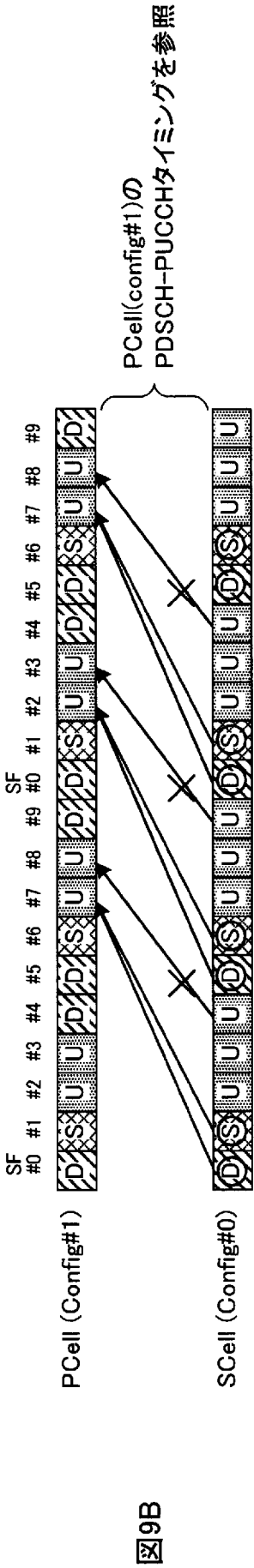


図7A

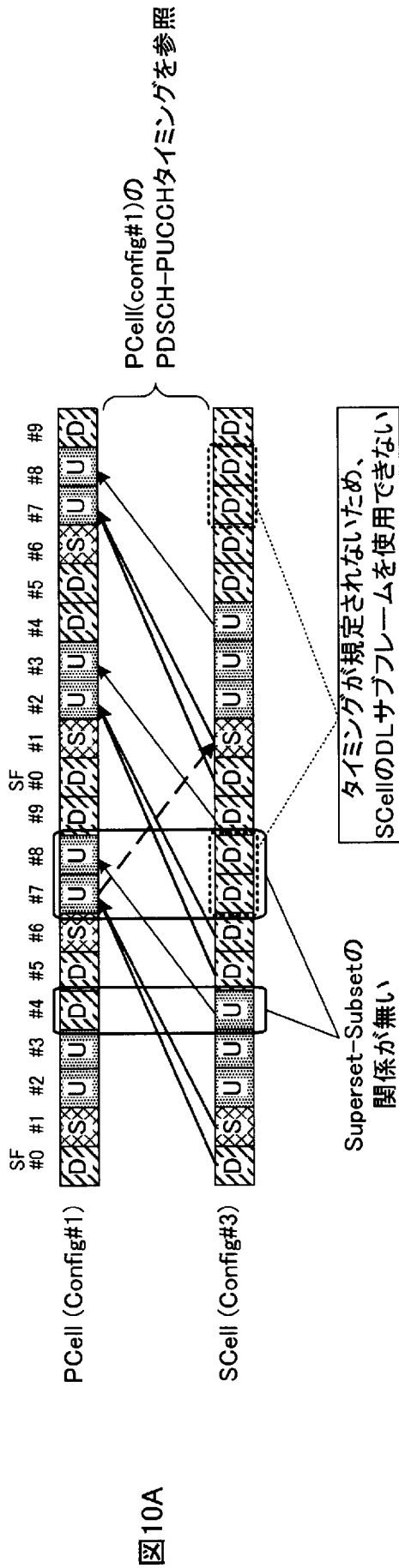
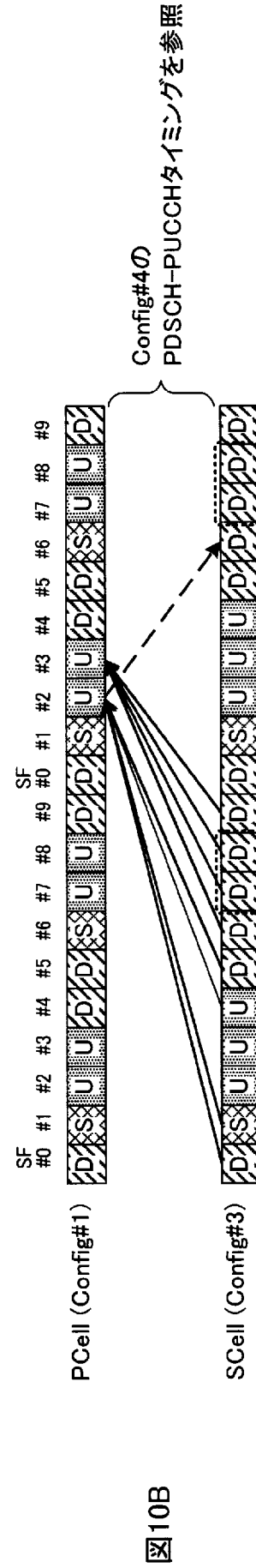
[図8]



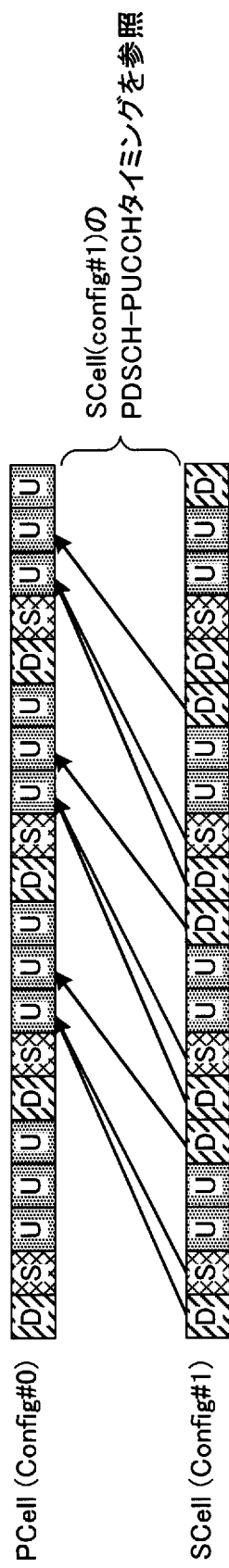
[図9]



[図10]



[図11]



[図12]

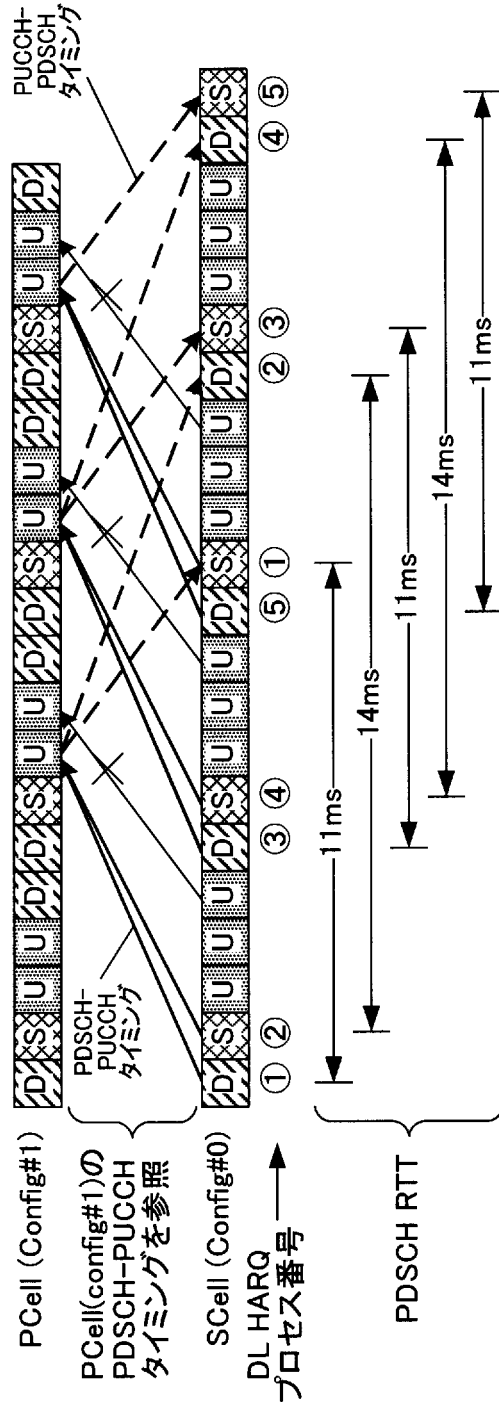


図12A

SCell用ソフトバッファを4分割
(Scell=Config#0)

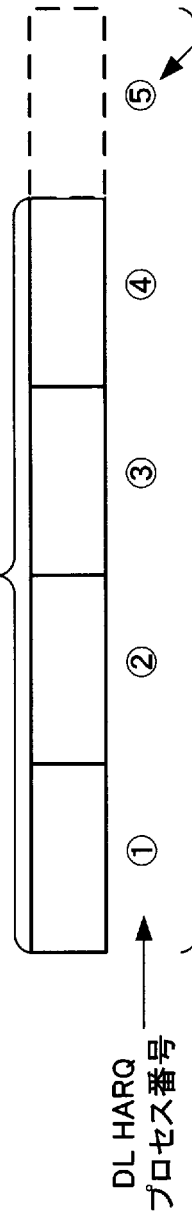


図12B

[図13]

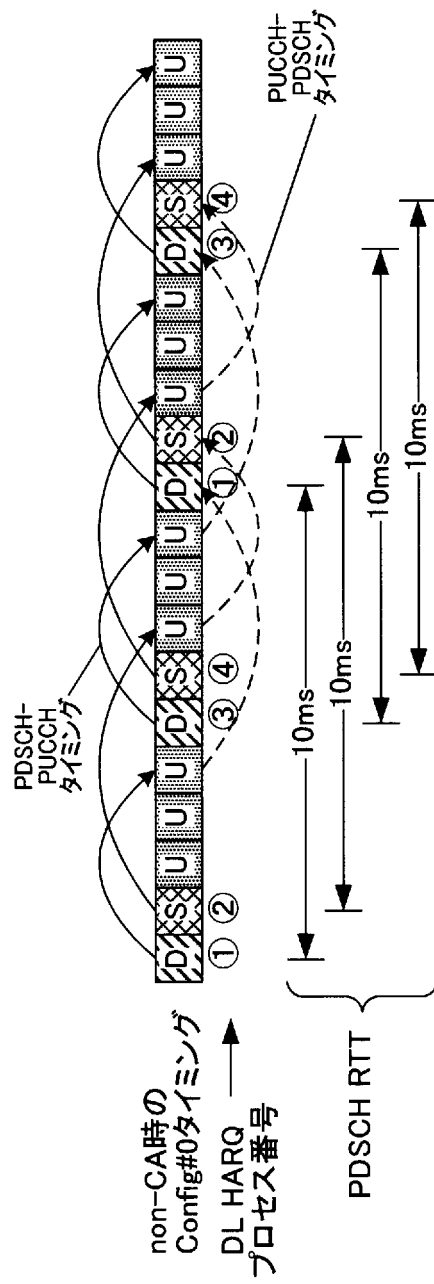


図13A

SCell用ソフトバッファを4分割
(Scell=Config#0)

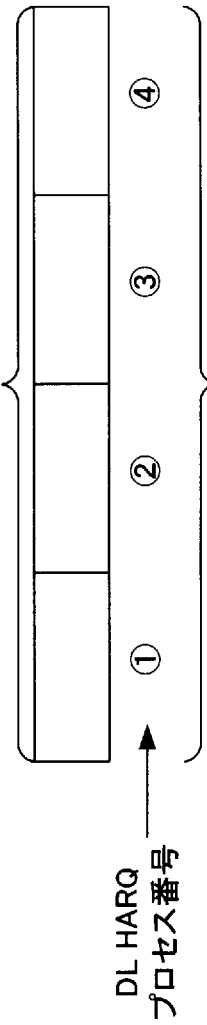
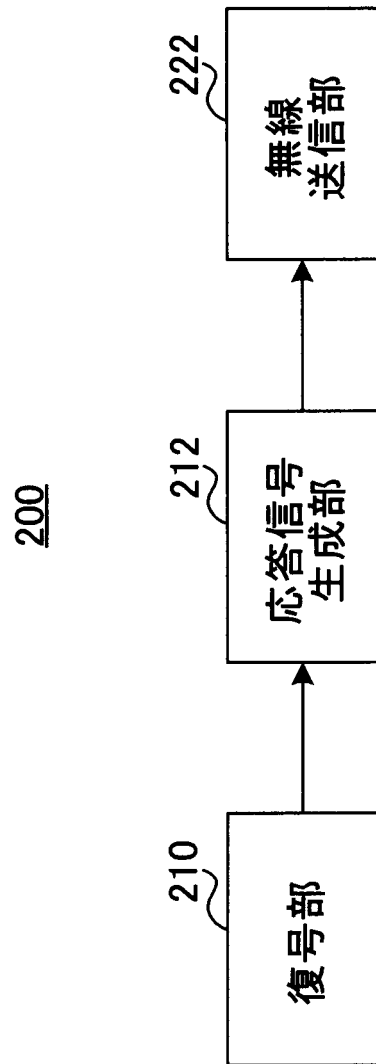


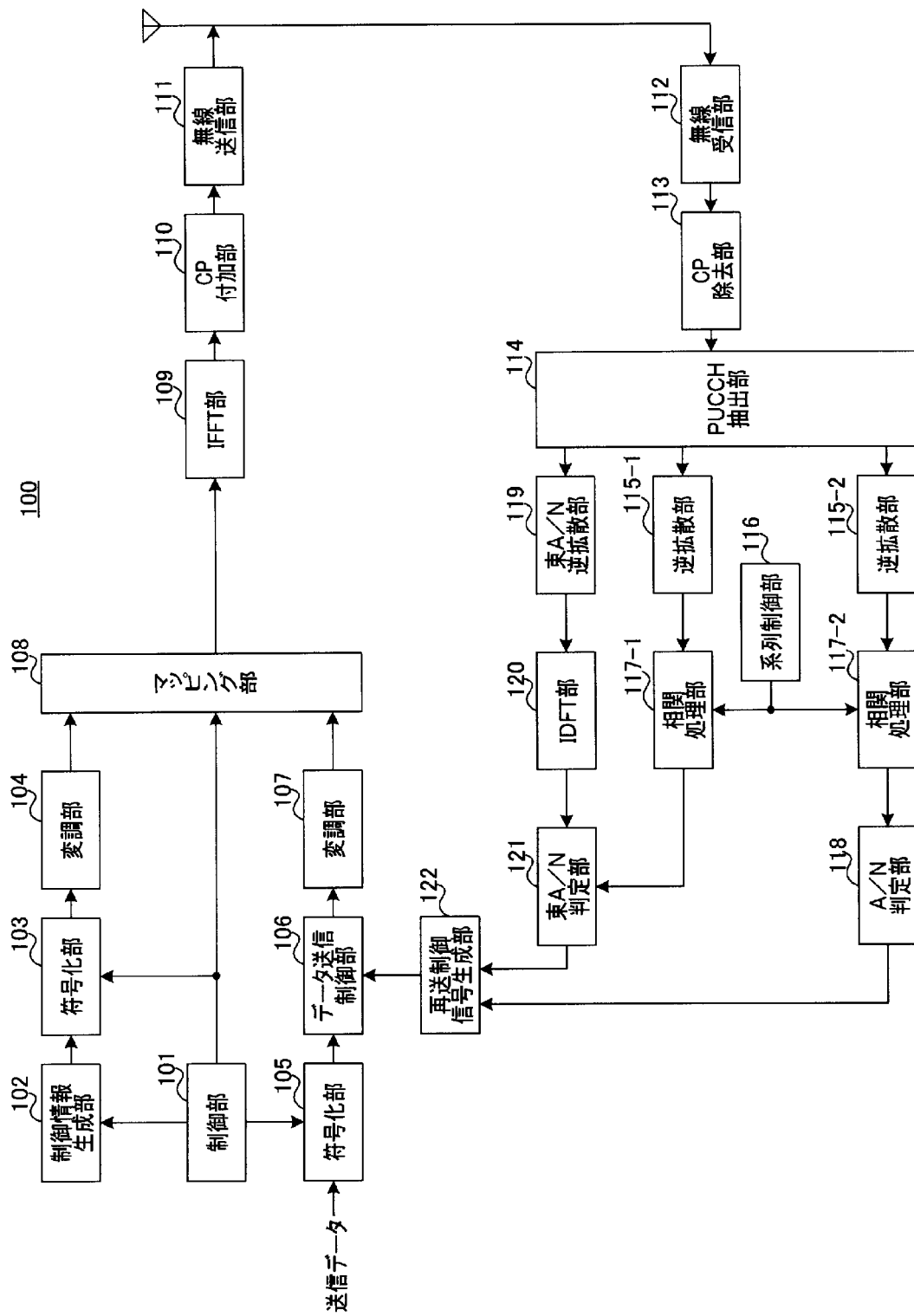
図13B

最大DL HARQプロセス数は4

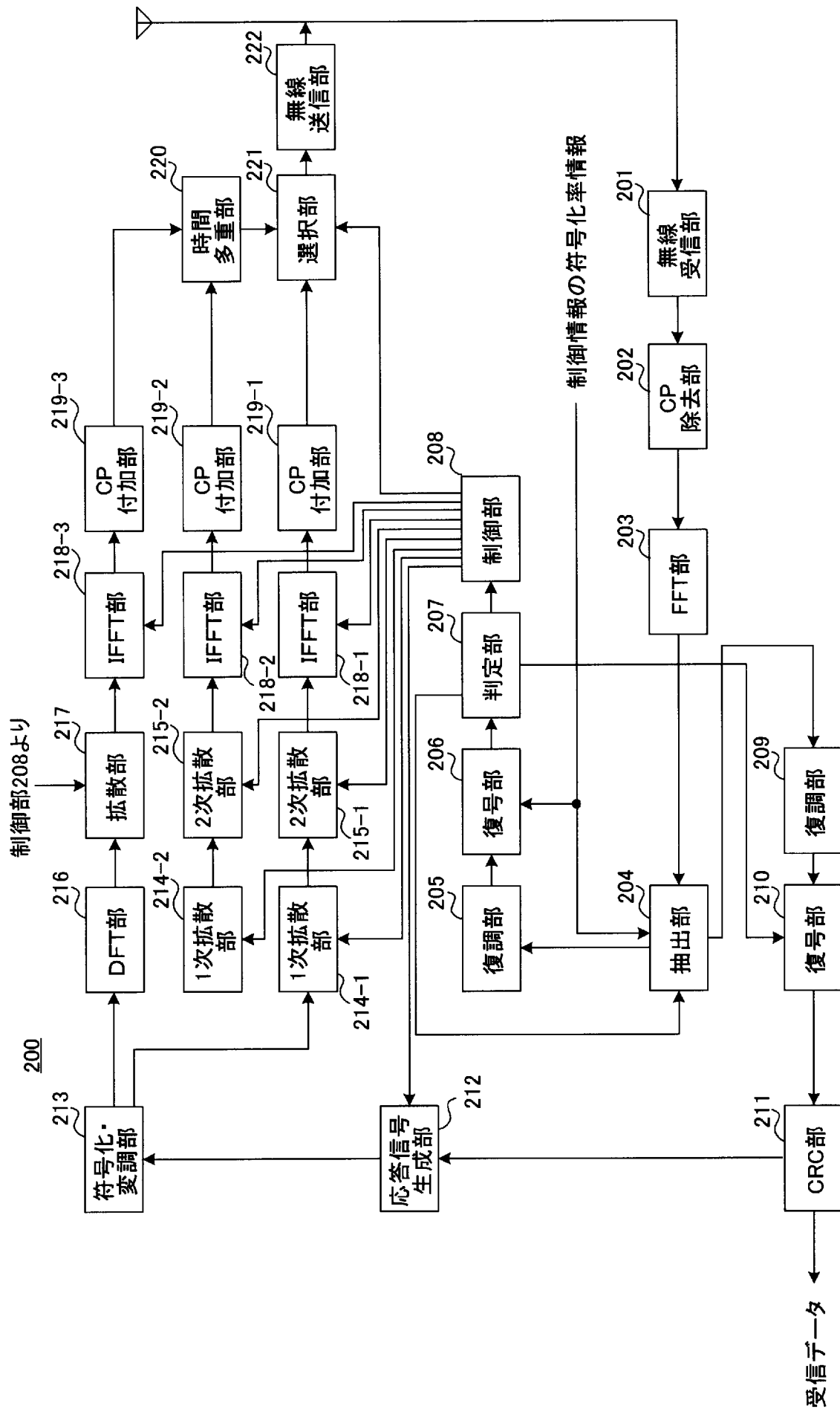
[図14]



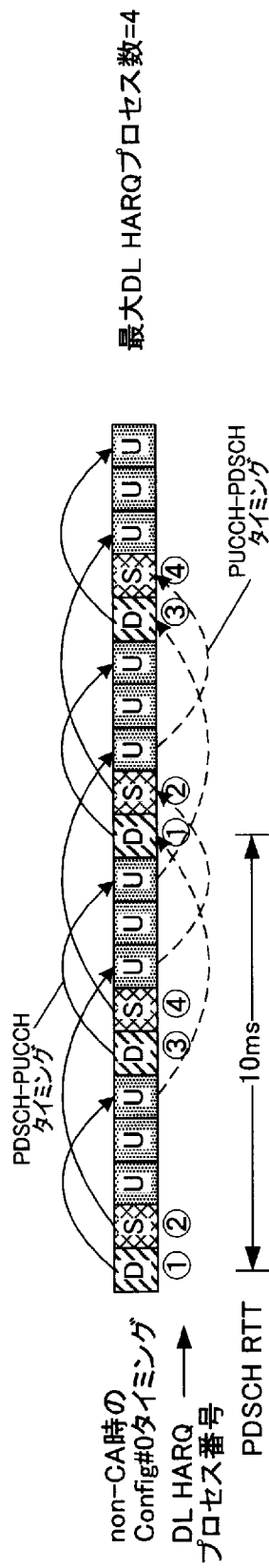
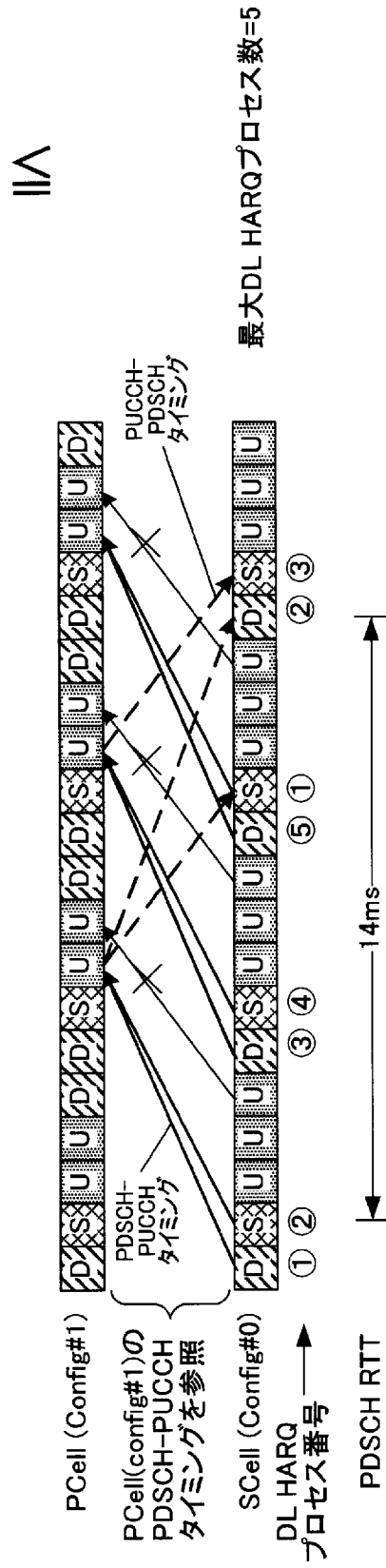
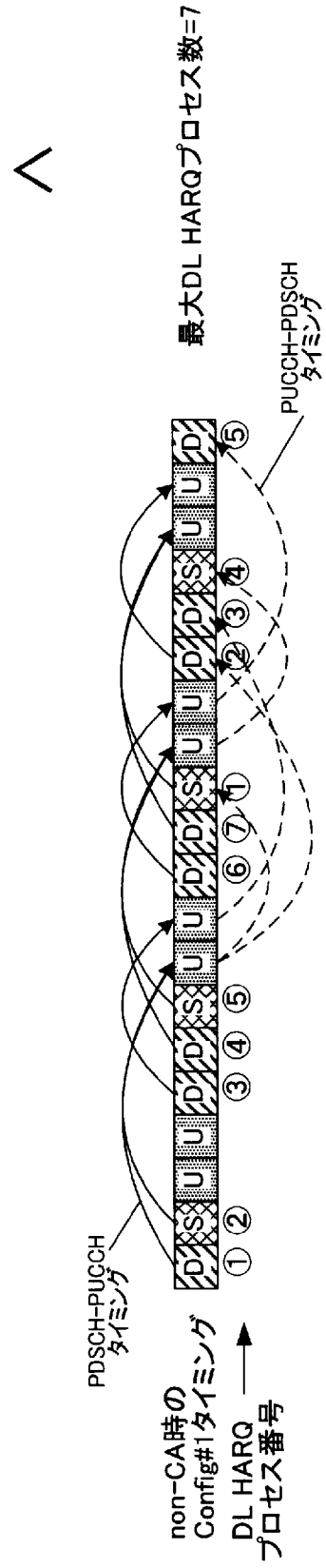
[図15]



[図16]



[図17]



[図18]

UL-DL Config	PDSCH RTT [msec]
0	10
1	11
2	12
3	15
4	16
5	17
6	14

[図19]

SCellの参照最大DL HARQプロセス数

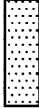
	PCell の設定UL- DL Config						
	0	1	2	3	4	5	6
0		4+1	4+2	4+2	4+3	4+3	4+1
1	7		7+1	7+2	7+3	7+4	7
2	10	10		10+4	10+4	10+4	10
3	9	9+1	9+1		9+1	9+2	9
4	12	12	12+1	12		12+1	12
5	15	15	15	15	15		15
6	6	6+0	6+1	6+1	6+2	6+3	

図19A

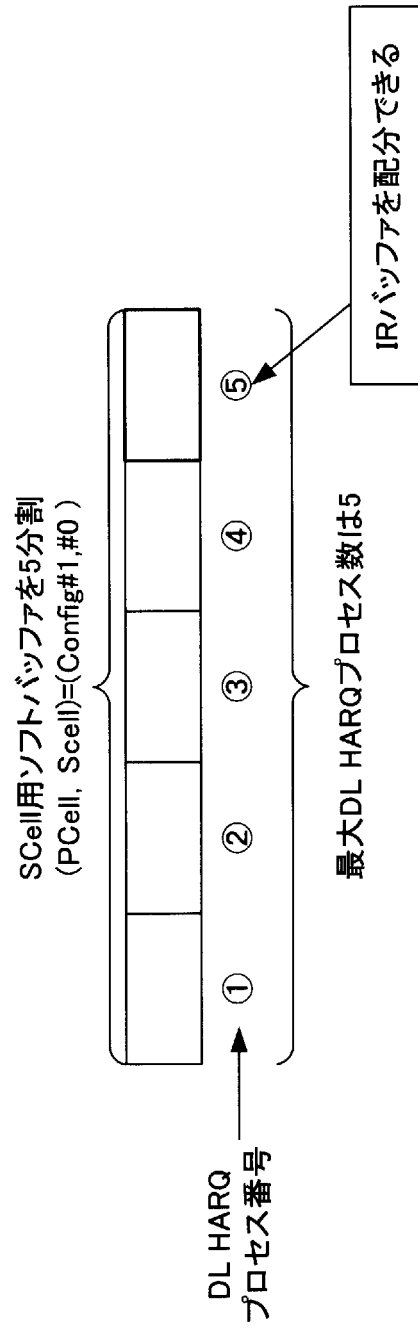
SCellのタイミングが参照するUL-DL Configuration

	PCell の設定UL- DL Config						
	0	1	2	3	4	5	6
0		1	2	3	4	5	6
1	1		2	4	4	5	1
2	2	2		5	5	5	2
3	3	4	5		4	5	3
4	4	4	5	4		5	4
5	5	5	5	5	5		5
6	6	1	2	3	4	5	

図19B

-  PCellがDL heavyの場合
-  PCellがDL heavyでもUL heavyでもない場合
-  PCellがUL heavyの場合

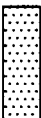
[図20]



SCellの参照最大DL HARQプロセス数と M_{limit} との最小値比較結果

		PCell の設定UL- DL Config						
		0	1	2	3	4	5	6
SCell の設定 UL- DL Config	0	/	5	6	6	7	7	5
	1	7	/	8	8	8	8	7
	2	8	8	/	8	8	8	8
	3	8	8	8	/	8	8	8
	4	8	8	8	8	/	8	8
	5	8	8	8	8	8	/	8
	6	6	6	7	7	8	8	/


 PCellがDL heavyの場合


 PCellがDL heavyでもUL heavyでもない場合


 PCellがUL heavyの場合

[図22]

SCellのタイミングが参照するUL-DL Configuration

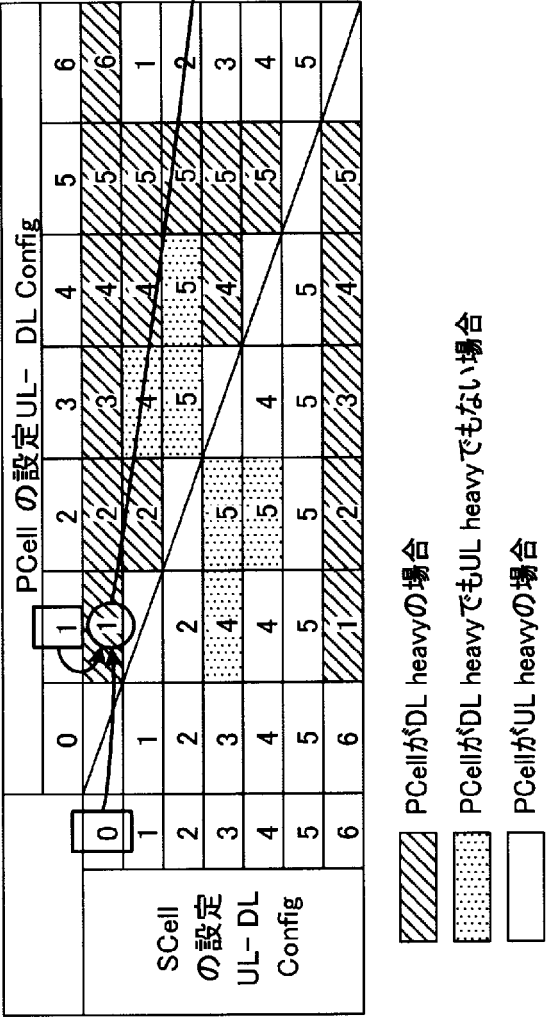
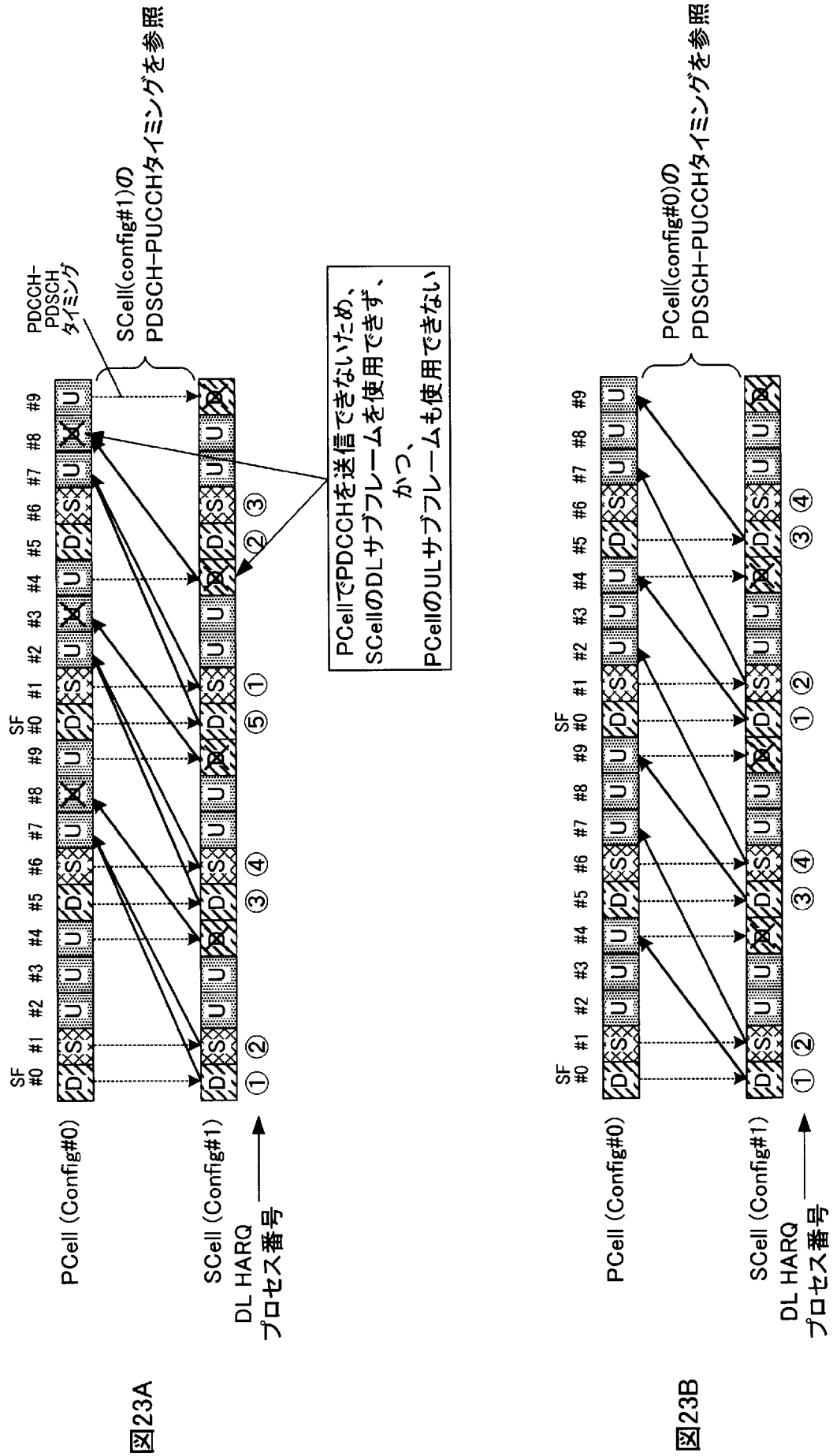


図22A

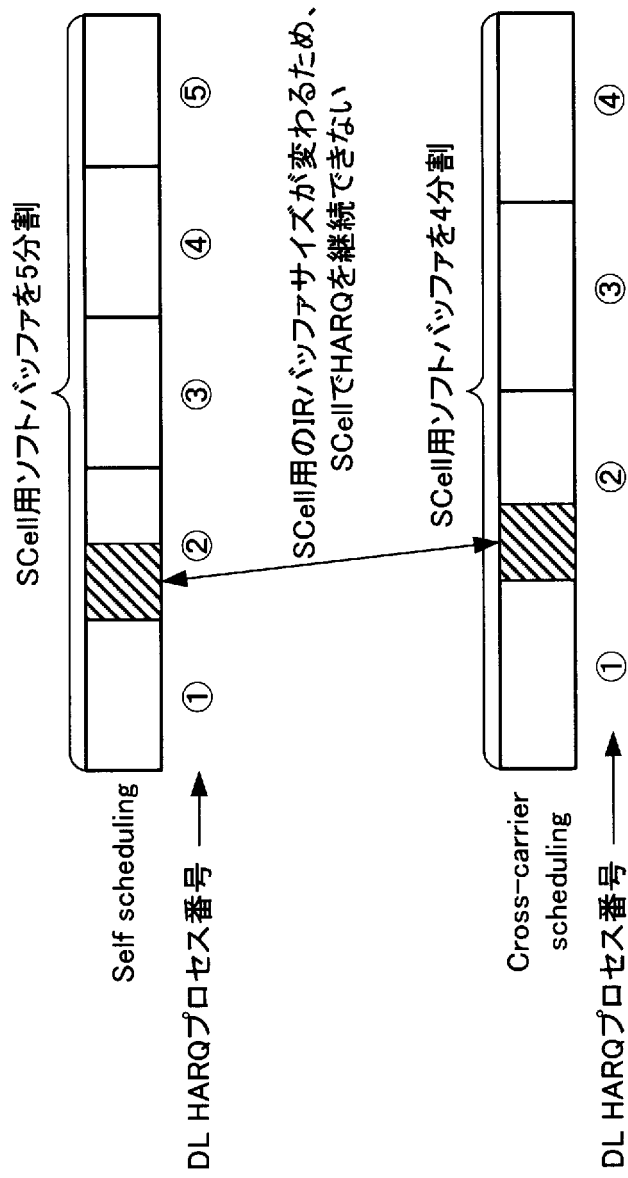
UL-DL Config	SCellの 参照最大DL HARQプロセス数 $M_{REF_DL_HARQ_SCell}$
0	4
1	7
2	10
3	9
4	12
5	15
6	6

図22B

[図23]



[図24]

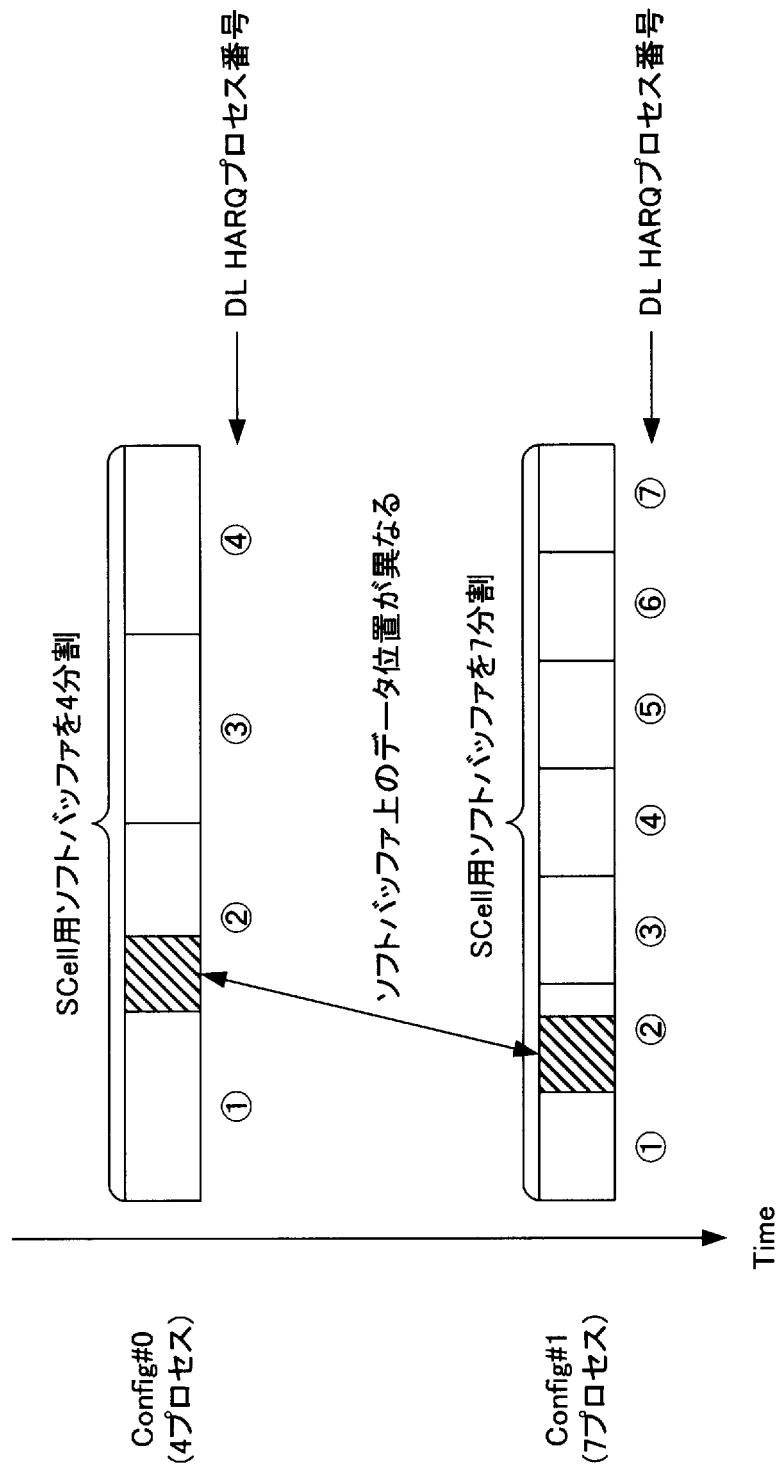


[図25]

SCellの参照最大DL HARQプロセス数(Cross-carrier scheduling)

		PCell の設定UL- DL Config						
		0	1	2	3	4	5	6
SCell の設定 UL- DL Config	0		7-2	10-5	9-3	12-5	15-9	6-1
	1	4		10-2	9-2	12-2	15-4	6
	2	4	7		9-1	12-1	15-1	6
	3	4	7-1	10-3		12-2	15-4	6
	4	4	7	10-1	9		15-2	6
	5	4	7	10	9	12		6
	6	4	7-1	10-4	9-2	12-4	15-6	

[図26]



[図27]

SCellの参照最大DL HARQプロセス数

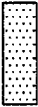
SCell の設定 UL-DL Config	PCell の設定UL- DL Config						
	0	1	2	3	4	5	6
0	4	4+1	4+2	4+2	4+3	4+3	4+1
1	7	7	7+1	7+2	7+3	7+4	7
2	10	10	10	10+4	10+4	10+4	10
3	9	9+1	9+1	9	9+1	9+2	9
4	12	12	12+1	12	12	12+1	12
5	15	15	15	15	15	15	15
6	6	6+0	6+1	6+1	6+2	6+3	6

図27A

SCellのタイミングが参照するUL-DL Configuration

SCell の設定 UL-DL Config	PCell の設定UL- DL Config						
	0	1	2	3	4	5	6
0	0	1	2	3	4	5	6
1	1	1	2	4	4	5	1
2	2	2	2	5	5	5	2
3	3	4	5	3	4	5	3
4	4	4	5	4	4	5	4
5	5	5	5	5	5	5	5
6	6	1	2	3	4	5	6

図27B

-  PCellがDL heavyの場合
-  PCellがDL heavyでもUL heavyでもない場合
-  PCellがUL heavyの場合
-  PcellとScellとが同一UL-DL Configurationの場合