

ABSTRACT

An integrated circuit

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An integrated circuit comprising: a substrate; a first transmission line arranged
5 on the substrate, the first transmission line having a first termination; a die
having a first surface on the substrate and an opposed second surface, the die
being spaced from the first termination; a second transmission line arranged on
the second surface of the die, the second transmission line having a second
termination; and a bond wire connected between the first termination and the
10 second termination configured to have a length half the wavelength of the signal
central frequency.

Fig. 2



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