CLAIMS

1. A memory, comprising a memory body, and a replacement key and a power on key arranged on the memory body, wherein

the replacement key and the power on key are separately connected to a hub through an input/output interface; and the hub is configured to send a system management bus signal to a central processing unit (CPU) when level conversion occurs at the input/output interface; and

the CPU is configured to power on or power off the memory after receiving the system management bus signal.

2. The memory of claim 1, further comprising a first power supply and a first resistor, wherein

the input/output interface is grounded through the replacement key; the first power supply is arranged on a connecting line between the input/output interface and the replacement key; and the first resistor is arranged on a connecting line of the first power supply.

3. The memory of claim 1, further comprising a second power supply and a second resistor, wherein

the input/output interface is grounded through the power on key; the second power supply is arranged on the connecting line between the input/output interface and the power on key; and the second resistor is arranged on a connecting line of the second power supply.

4. The memory of claim 1, further comprising a third power supply, wherein

the input/output interface is grounded; and the replacement key is arranged between the third power supply and the input/output interface.

5. The memory of claim 1, further comprising a fourth power supply, wherein

the input/output interface is grounded; and the power on key is arranged between the fourth power supply and the input/output interface.

6. The memory of any one of claims 1 to 5, wherein both the power on key and the replacement key are switches.

7. The memory of claim 1, wherein the CPU is connected to the power management IC of the memory; the CPU controls the memory to be powered off by pulling down the power management IC enable signal; and

the CPU controls the memory to be powered on by pulling up the power management IC enable signal.