

A B S T R A C T

According to one embodiment, a voltage generation circuit includes: a charge pump circuit 20 configured to boost a voltage input to a first node and output a first
5 signal VX2 to a second node; an operational amplifier 37 configured to receive a first reference voltage VREF1 and a first voltage obtained by dividing a voltage of the second node and output a second signal to a third node; a first
10 transistor 90 having a gate coupled to the third node, one terminal coupled to a power supply, and the other terminal coupled to the first node; a logic circuit 30, 60 configured to detect the voltage of the second node and output a third signal; and a charge-up circuit 40
15 configured to receive the third signal and charge a voltage of the third node.