

FIG. 1

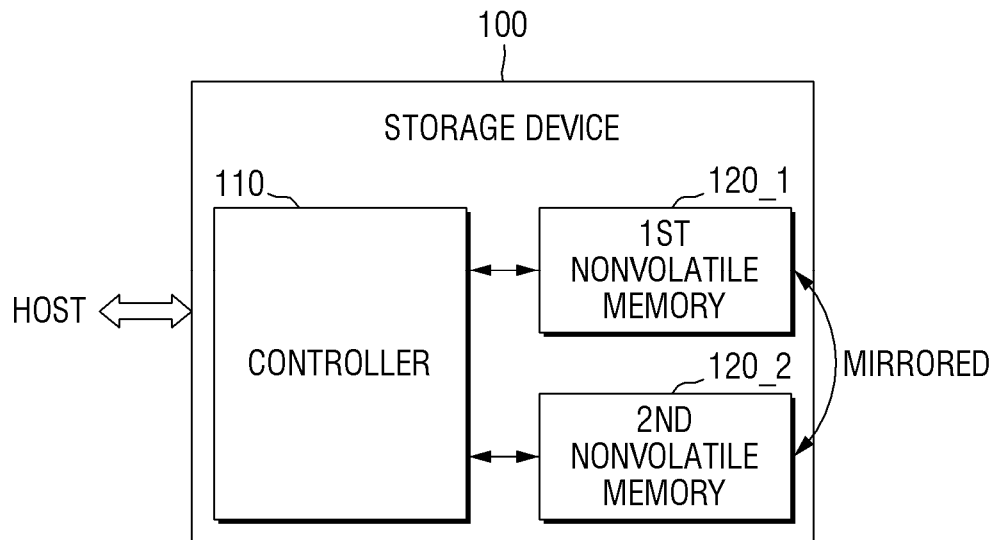


FIG. 2

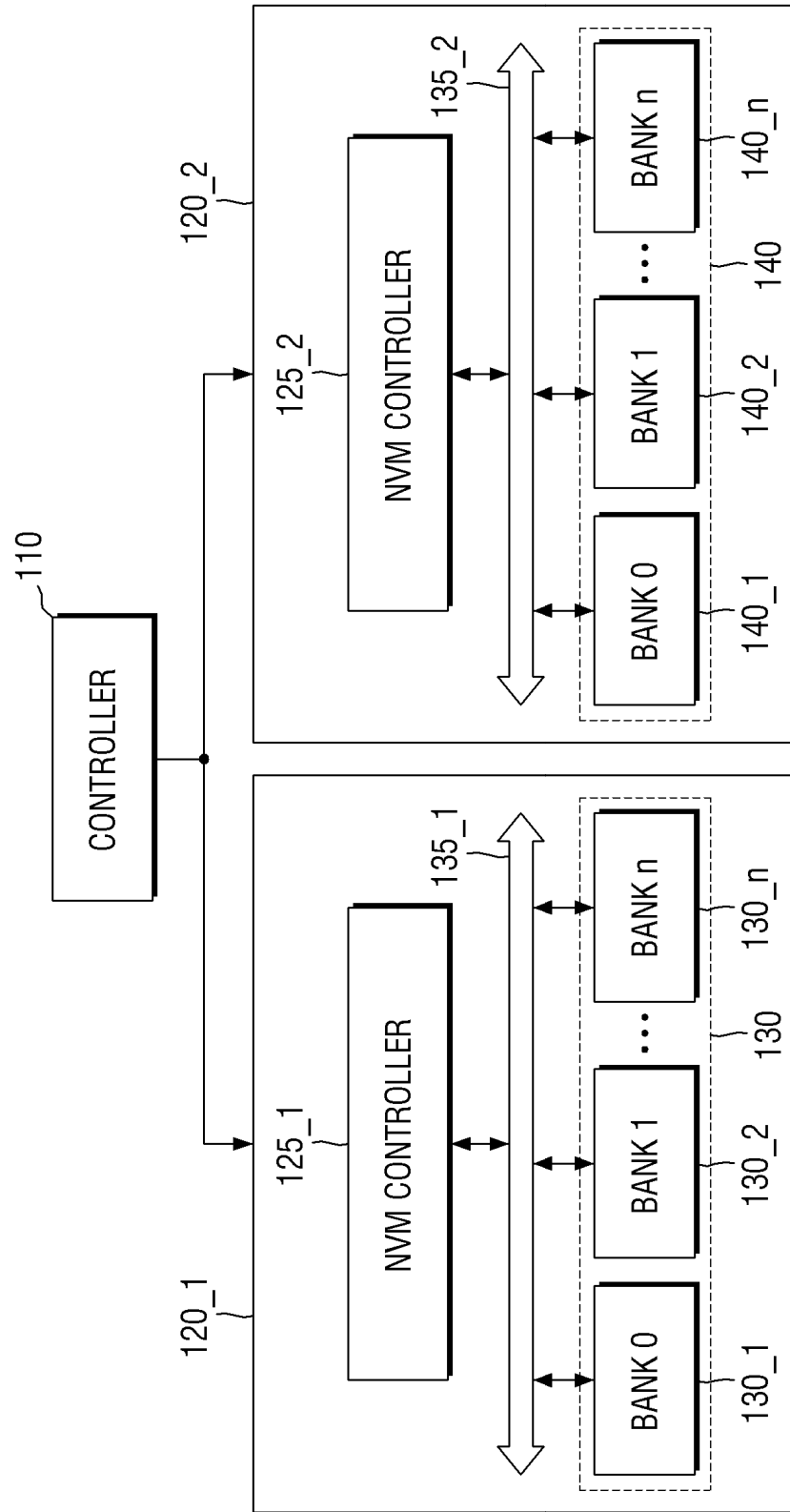


FIG. 3

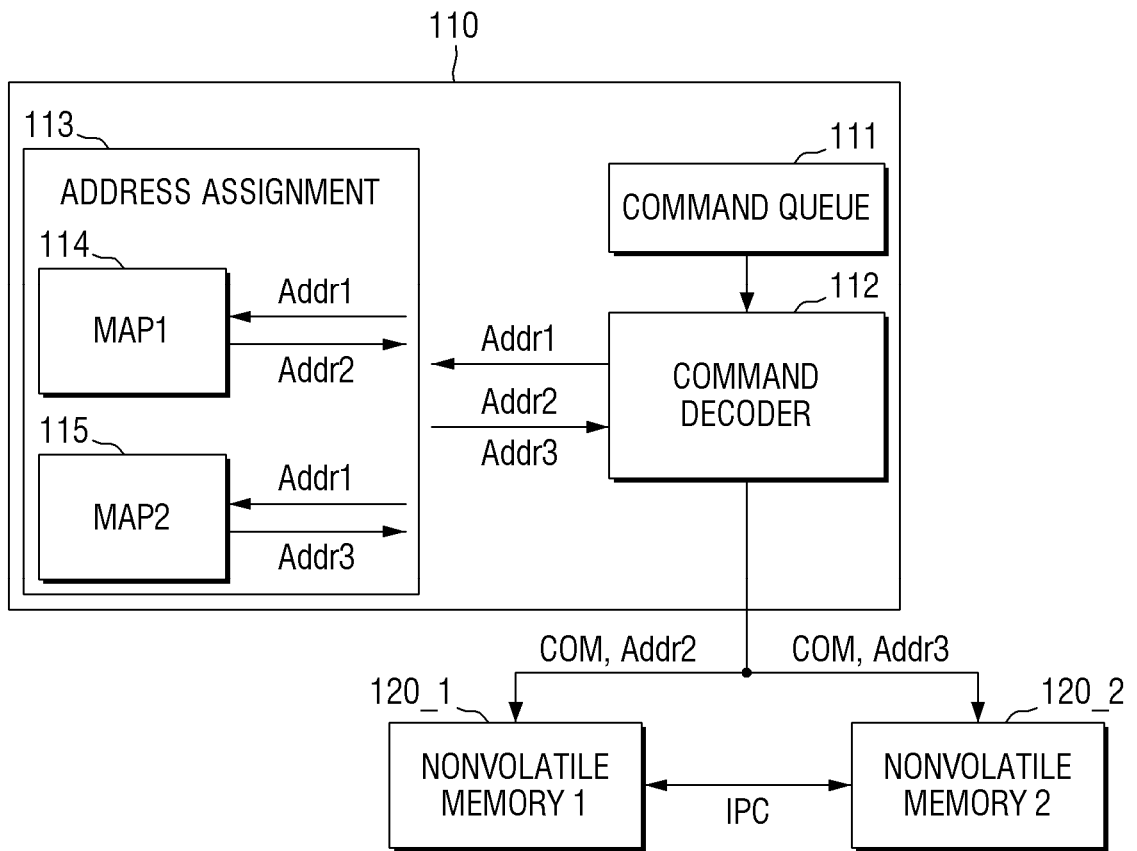


FIG. 4A

	130_0 BANK 0	130_1 BANK 1	130_2 BANK 2	130_3 BANK 3	
#0	0	1	2	3	R1_0
#1	4	5	6	7	R1_1
#2	8	9	10	11	R1_2
#3	12	13	14	15	R1_3
#4	16	17	18	19	R1_4
...	...	...	...	...	

FIG. 4B

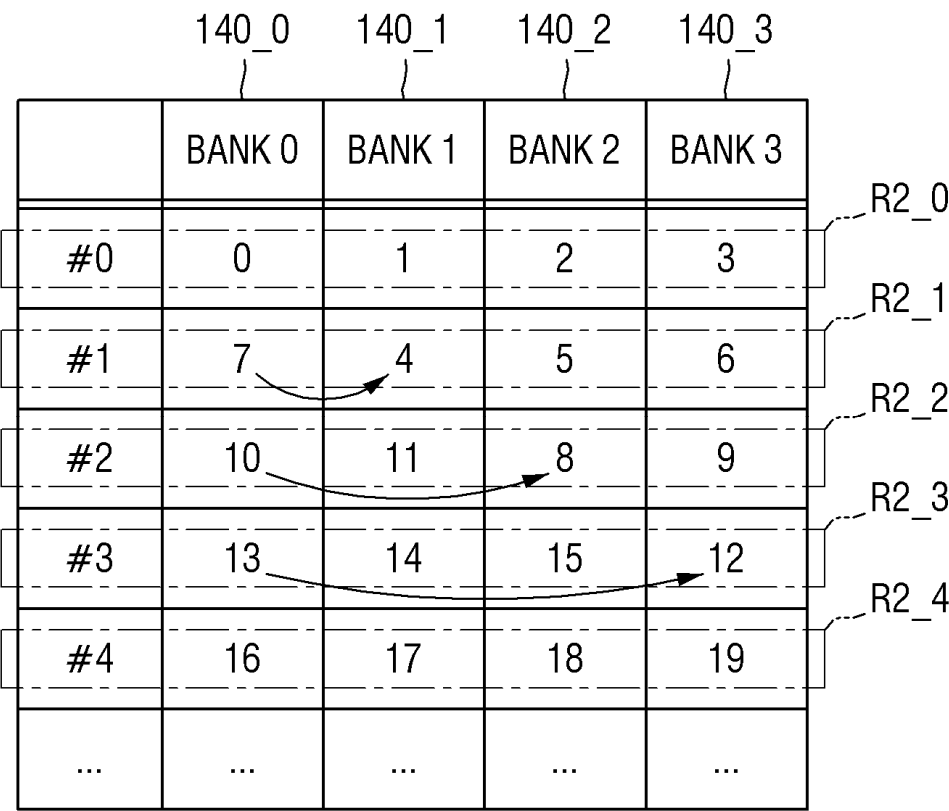


FIG. 5

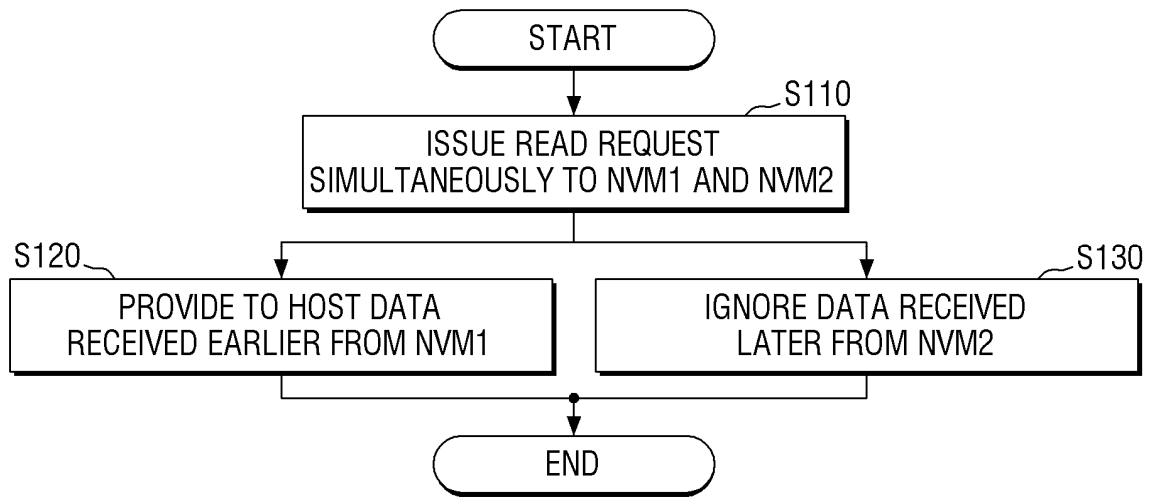


FIG. 6

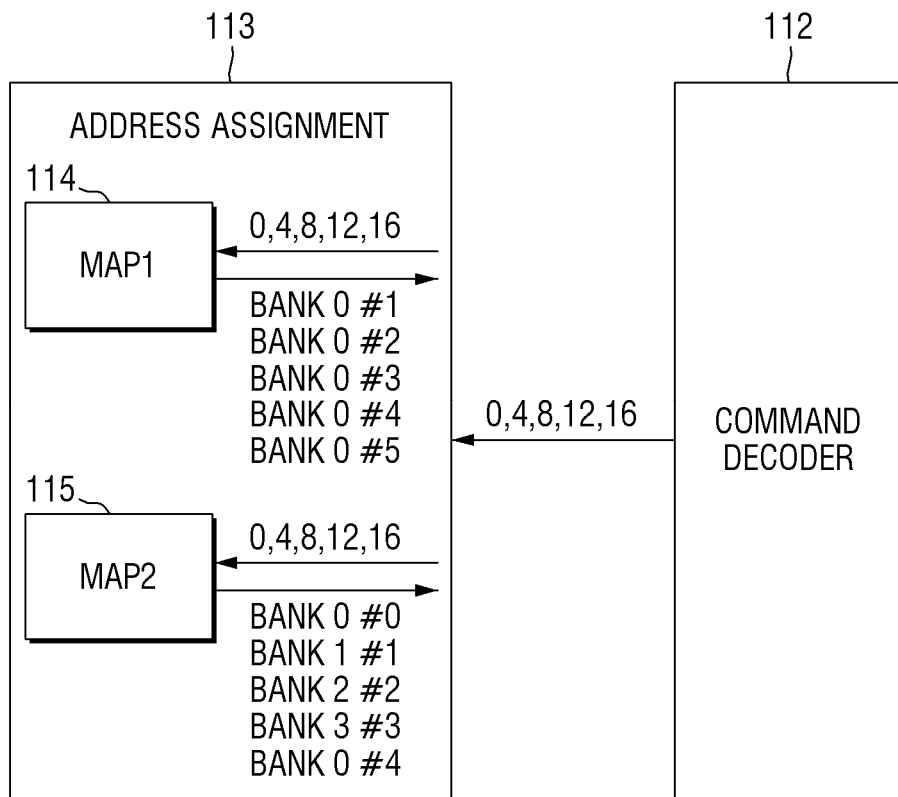


FIG. 7A

	130_0 BANK 0	130_1 BANK 1	130_2 BANK 2	130_3 BANK 3	
#0	0	1	2	3	R1_0
#1	4	5	6	7	R1_1
#2	8	9	10	11	R1_2
#3	12	13	14	15	R1_3
#4	16	17	18	19	R1_4
...	...	...	...	...	

FIG. 7B

	140_0 BANK 0	140_1 BANK 1	140_2 BANK 2	140_3 BANK 3	
#0	0	1	2	3	R2_0
#1	7	4	5	6	R2_1
#2	10	11	8	9	R2_2
#3	13	14	15	12	R2_3
#4	16	17	18	19	R2_4
...	...	...	...	...	

FIG. 8

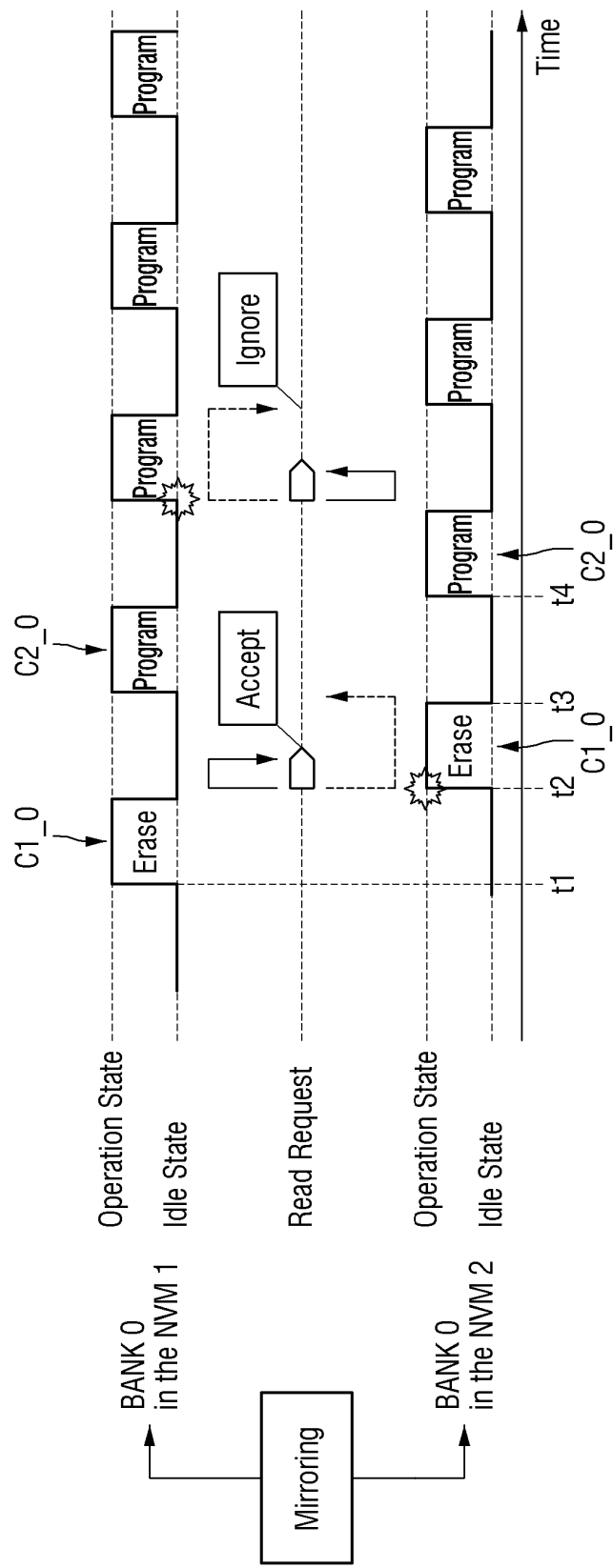


FIG. 9

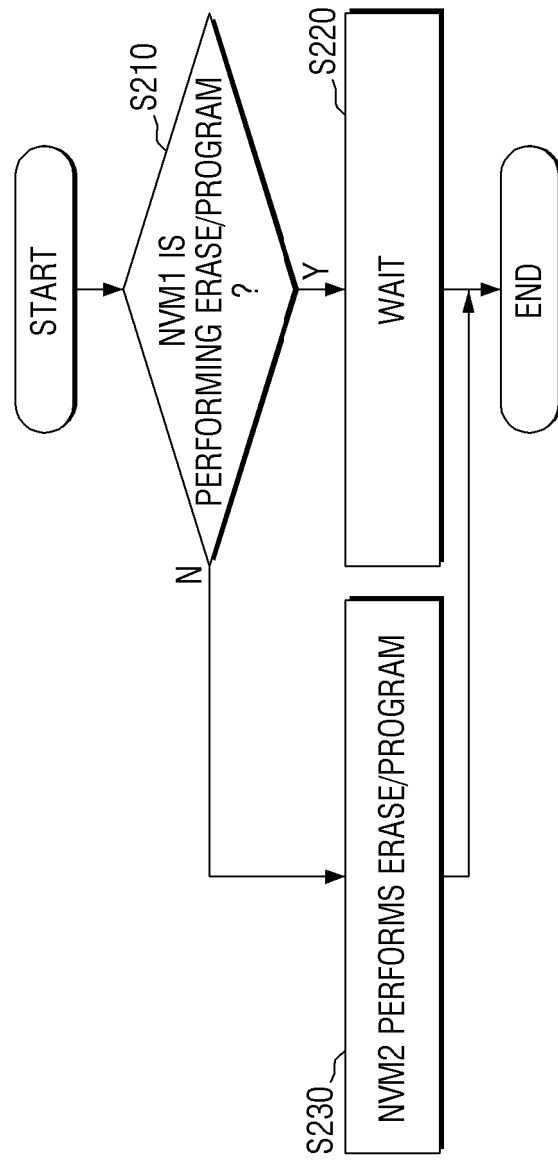


FIG. 10A

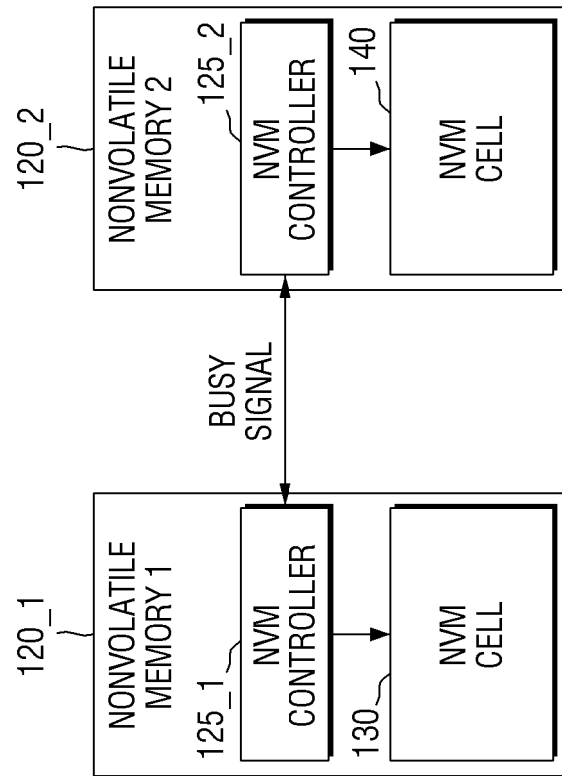


FIG. 10B

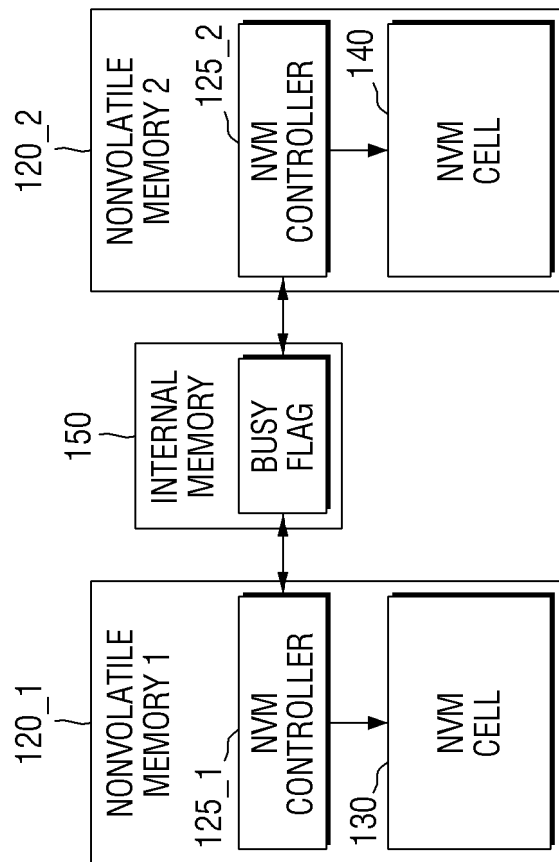


FIG. 11

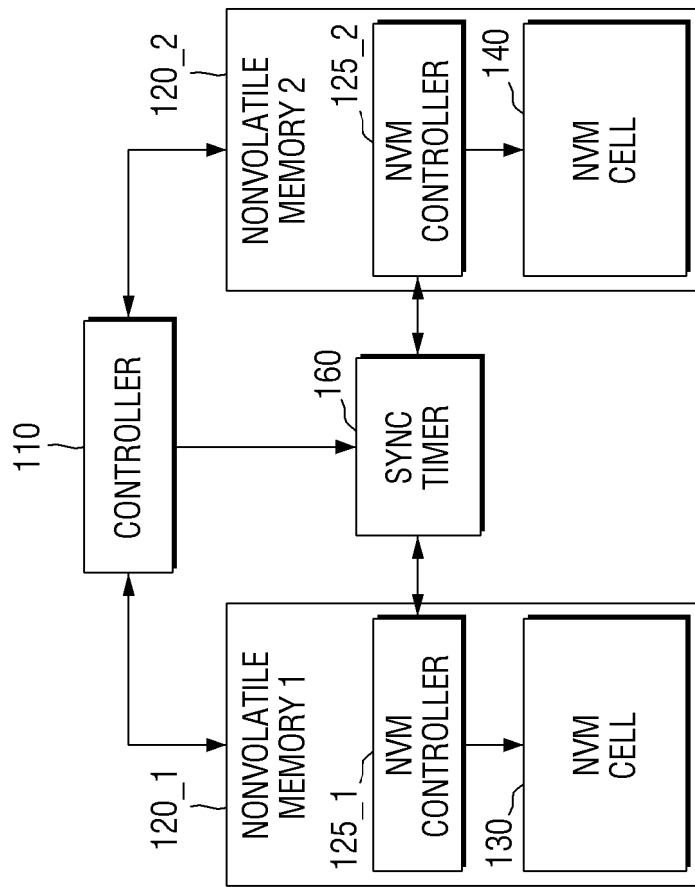


FIG. 12A

ERASE OPERATION
TIMING TABLE 1

NVM 1	NVM 2
0	2
4	6
8	10
12	14
16	18
⋮	⋮
4N	4N+2

FIG. 12B

ERASE OPERATION
TIMING TABLE 2

NVM 1	NVM 2
0	2-k
4	6-k
8	10-k
12	14-k
16	18-k
⋮	⋮
4N	4N+2-k

FIG. 13

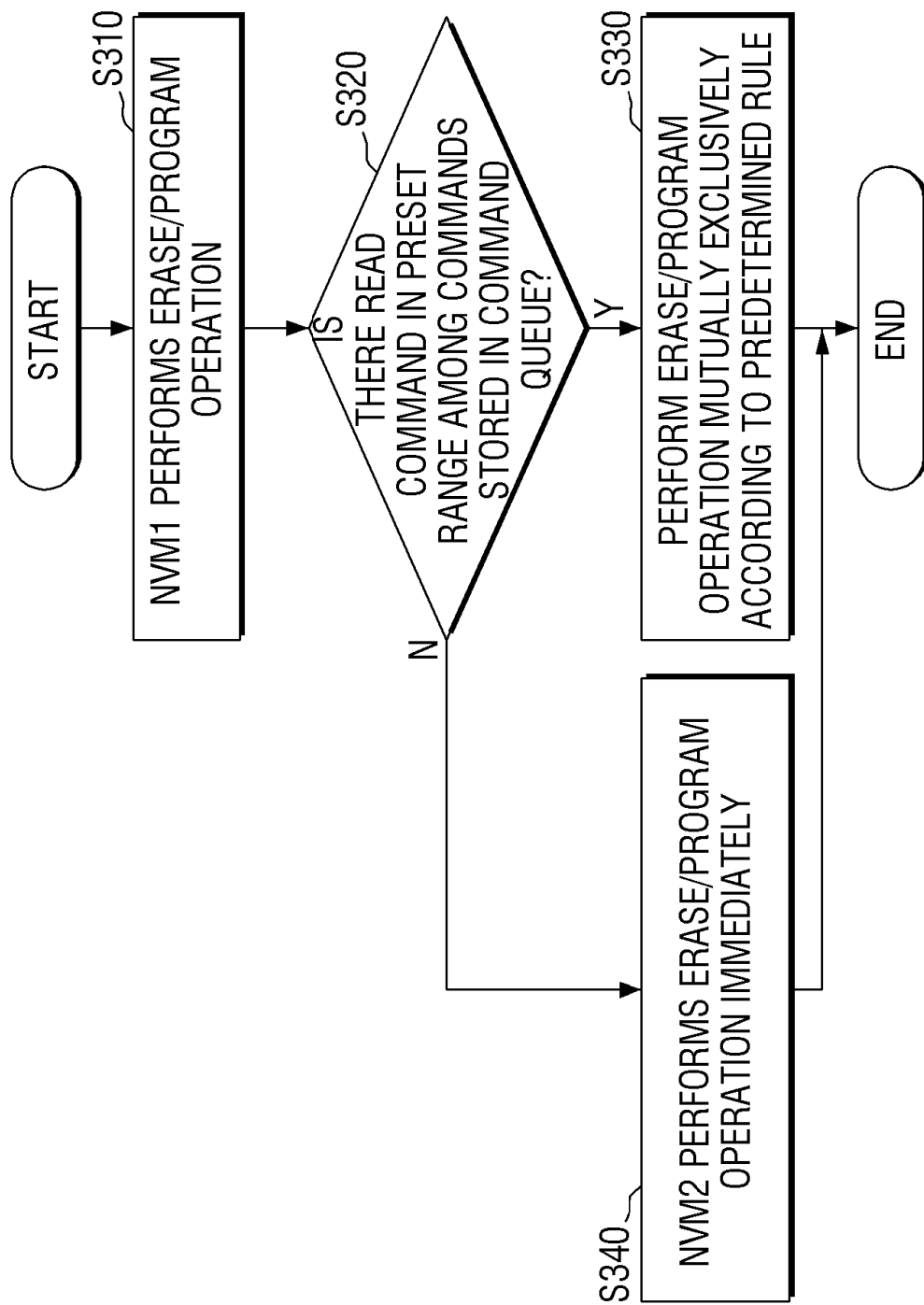


FIG. 14

