

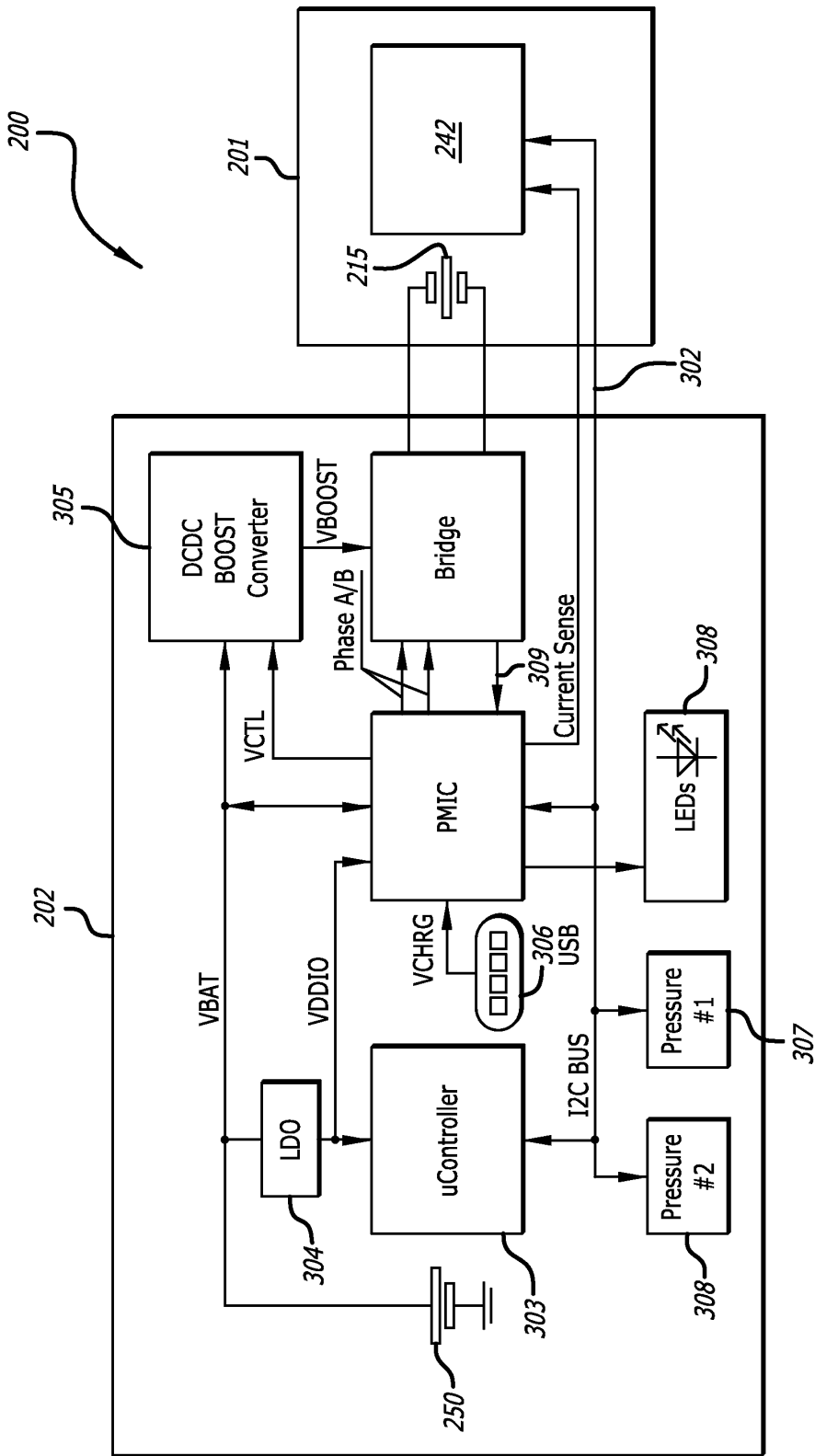


FIG. 1

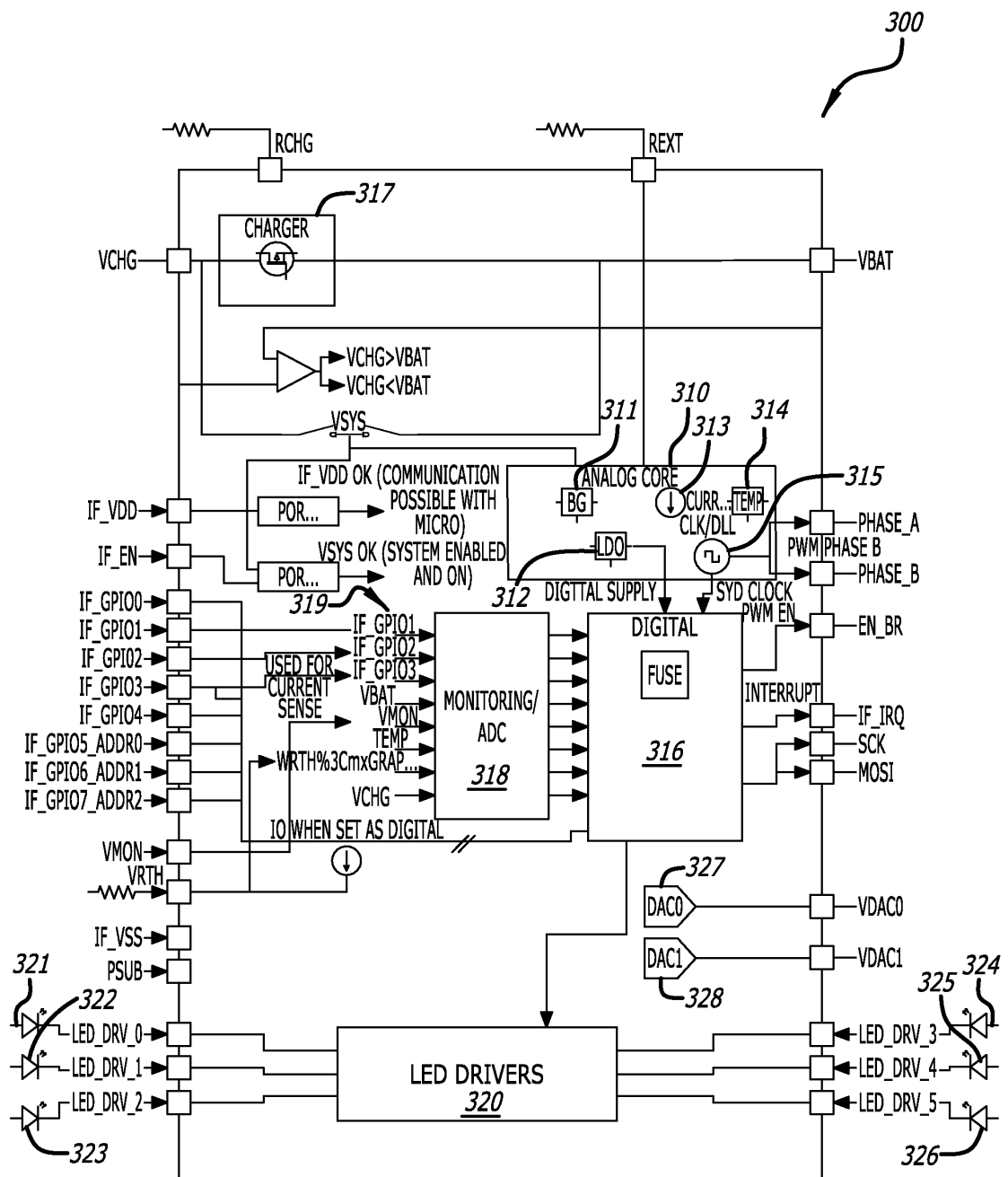


FIG. 2

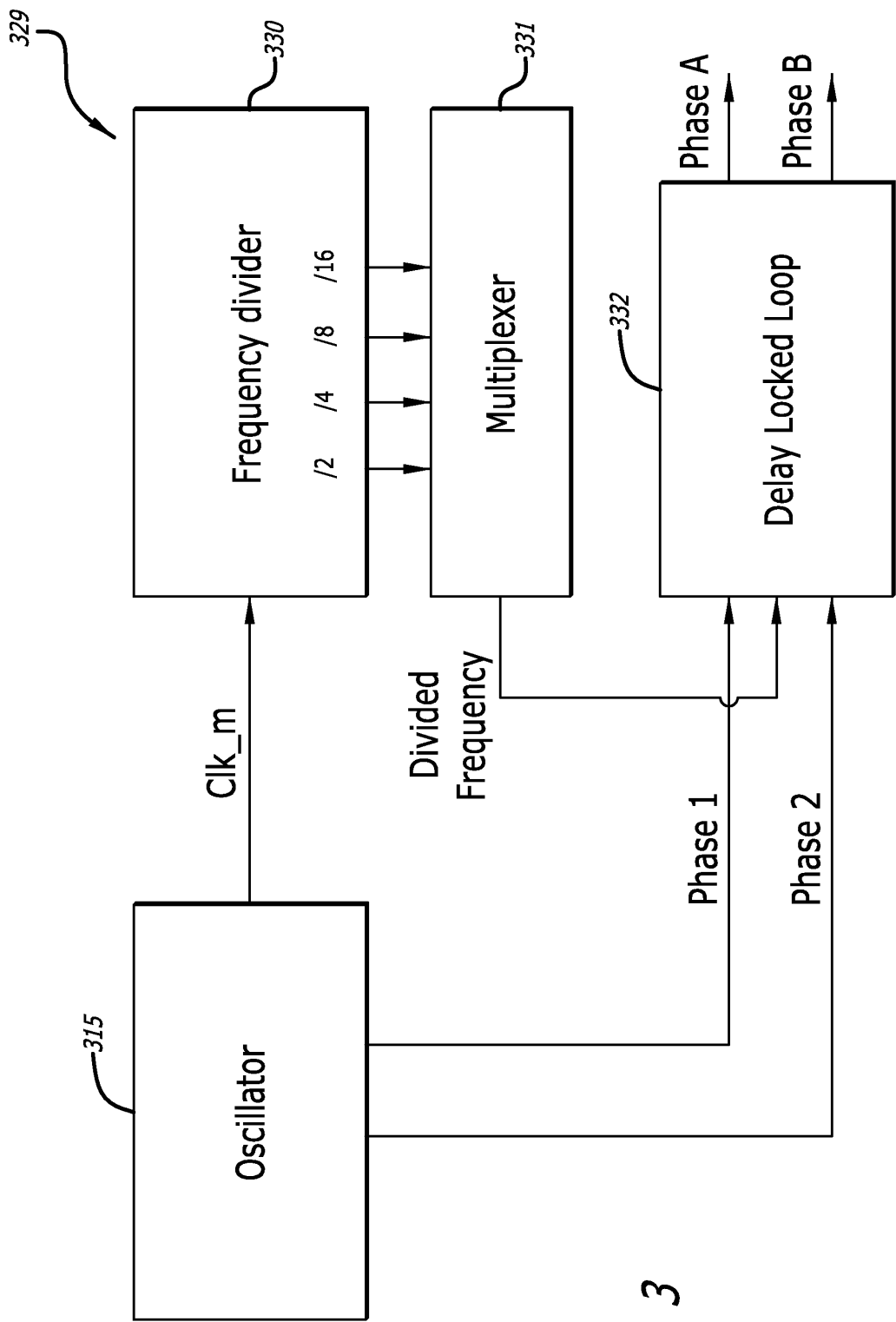


FIG. 3

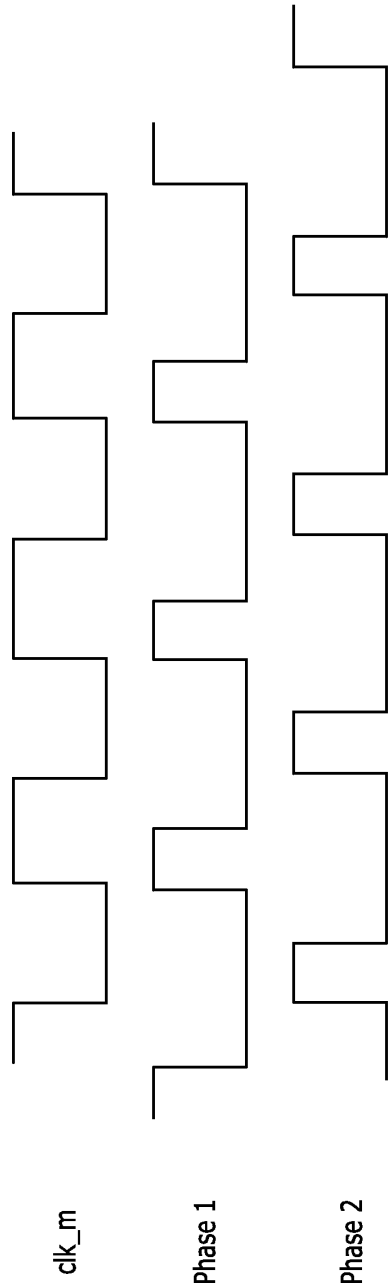


FIG. 4

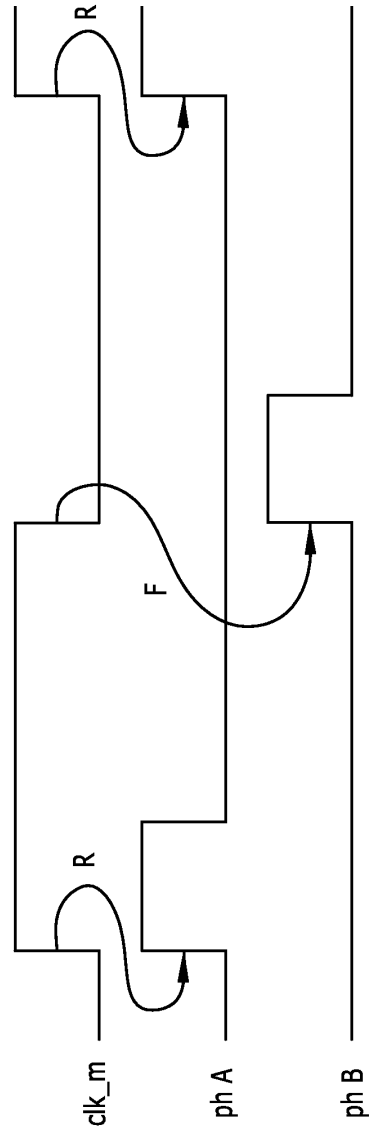
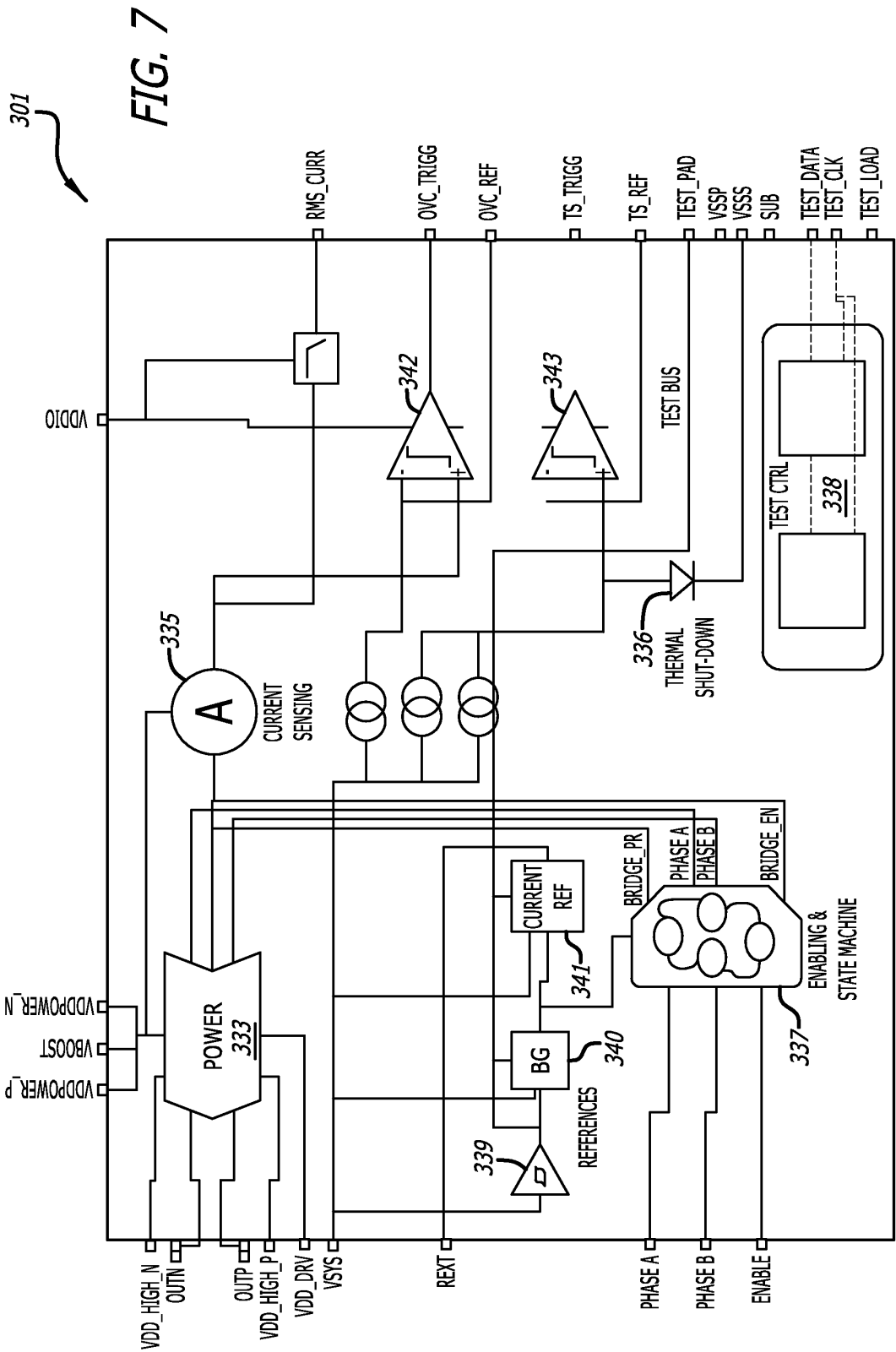


FIG. 5

FIG. 6

Port #	Primary Function	Secondary Function
GPIO.0	digital GPIO	used for chip test
GPIO.1	digital GPIO	ADC channel 0
GPIO.2	digital GPIO	ADC channel 1
GPIO.3	rms current	digital GPIO
GPIO.4	digital GPIO	used for chip test
GPIO.5	digital GPIO	I2C address select ADR0
GPIO.6	digital GPIO	I2C address select ADR1
GPIO.7	digital GPIO	I2C address select ADR2



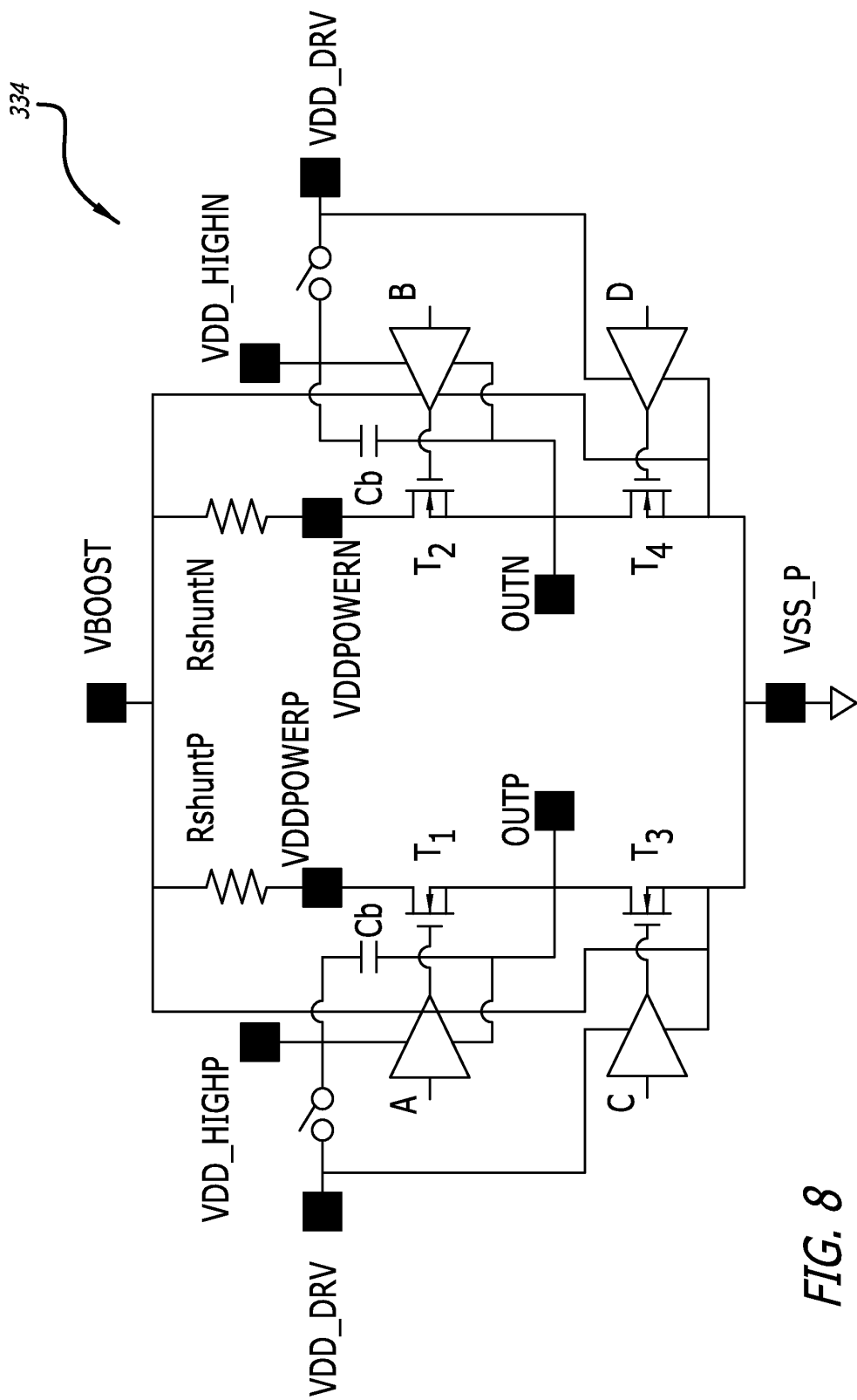


FIG. 8

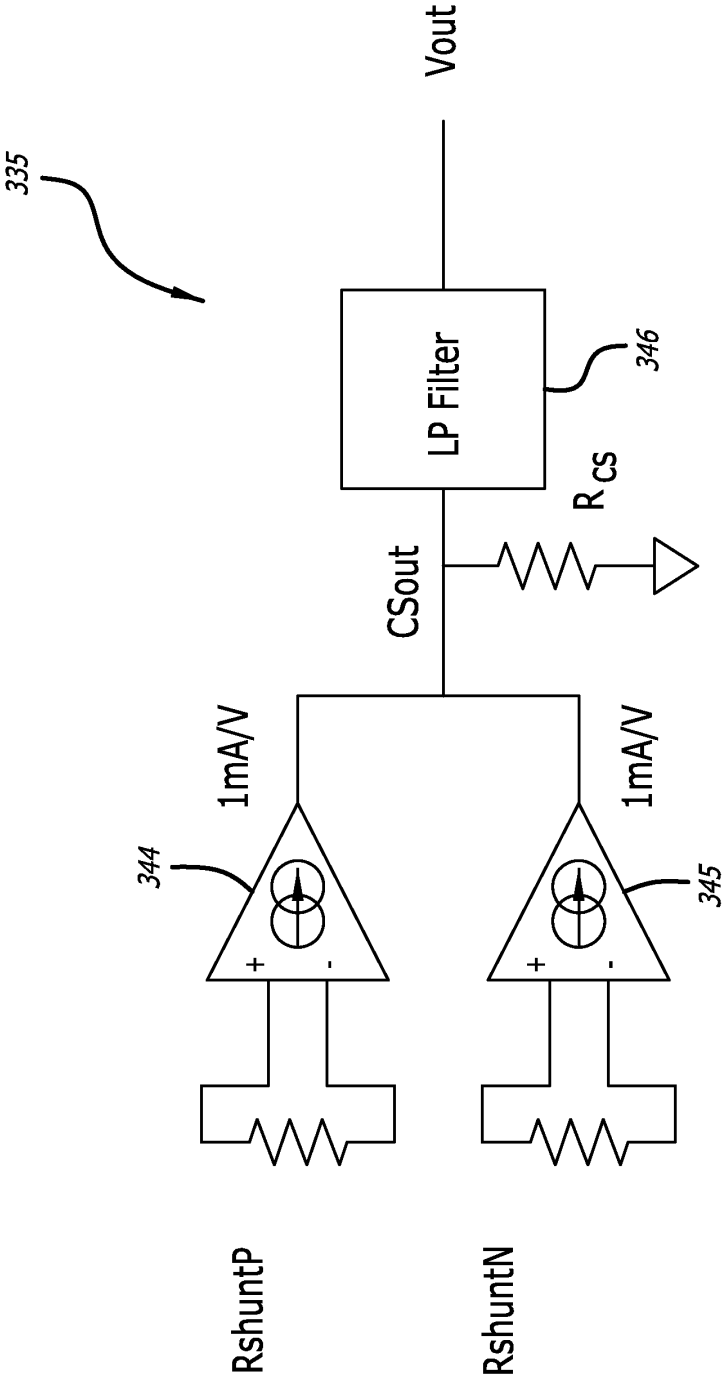
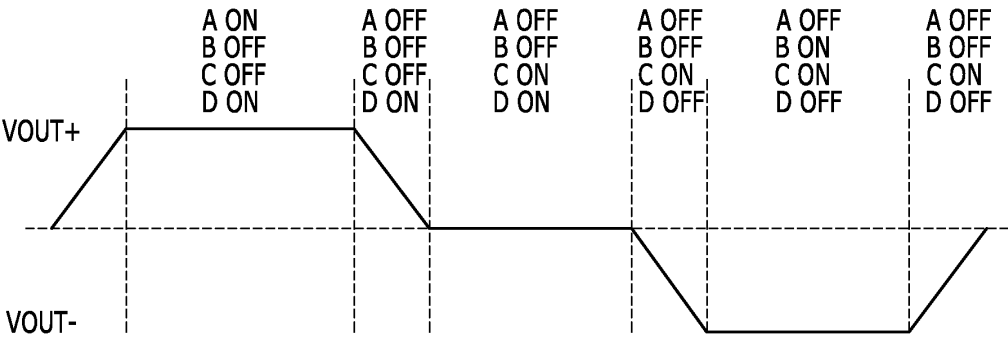
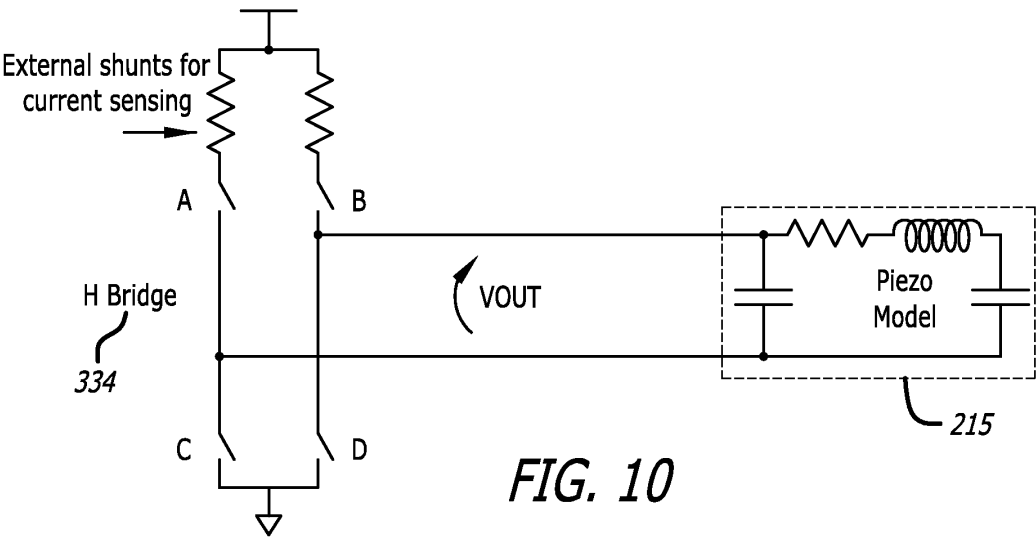


FIG. 9



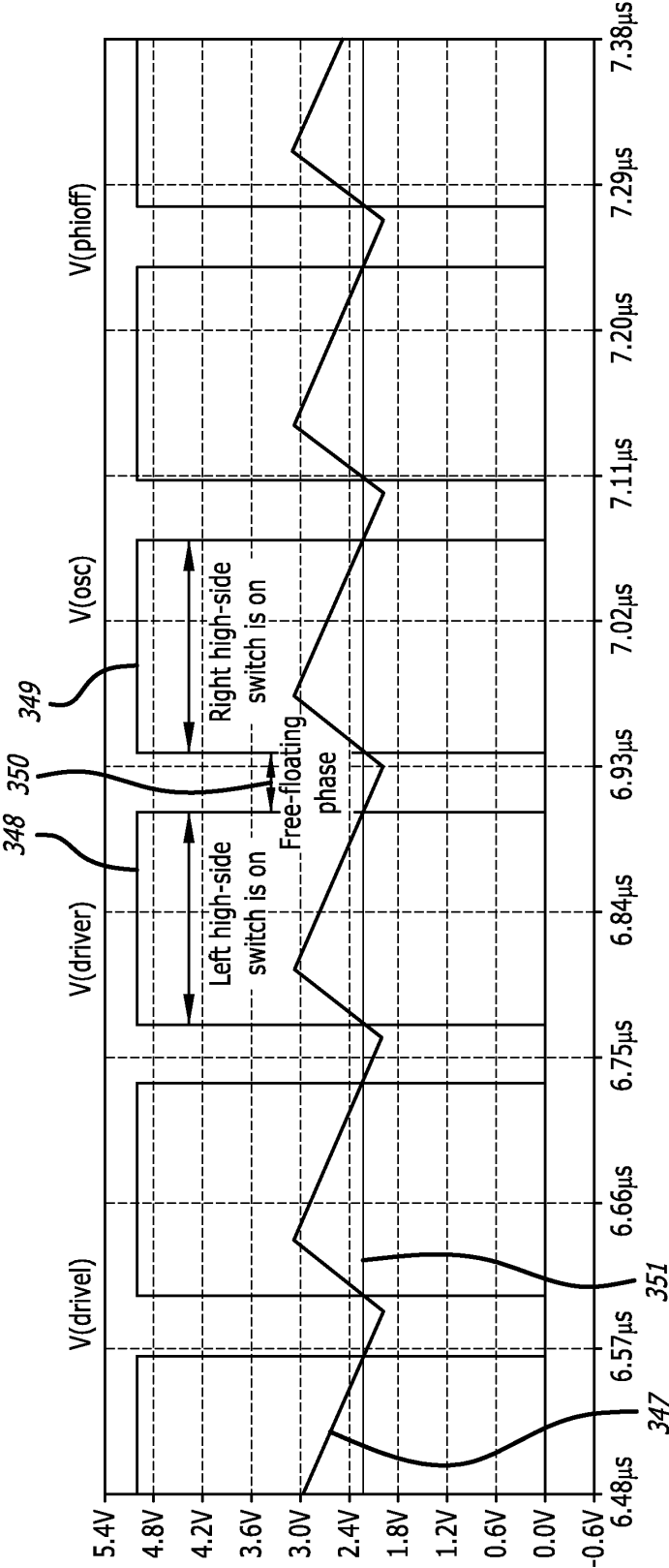


FIG. 12

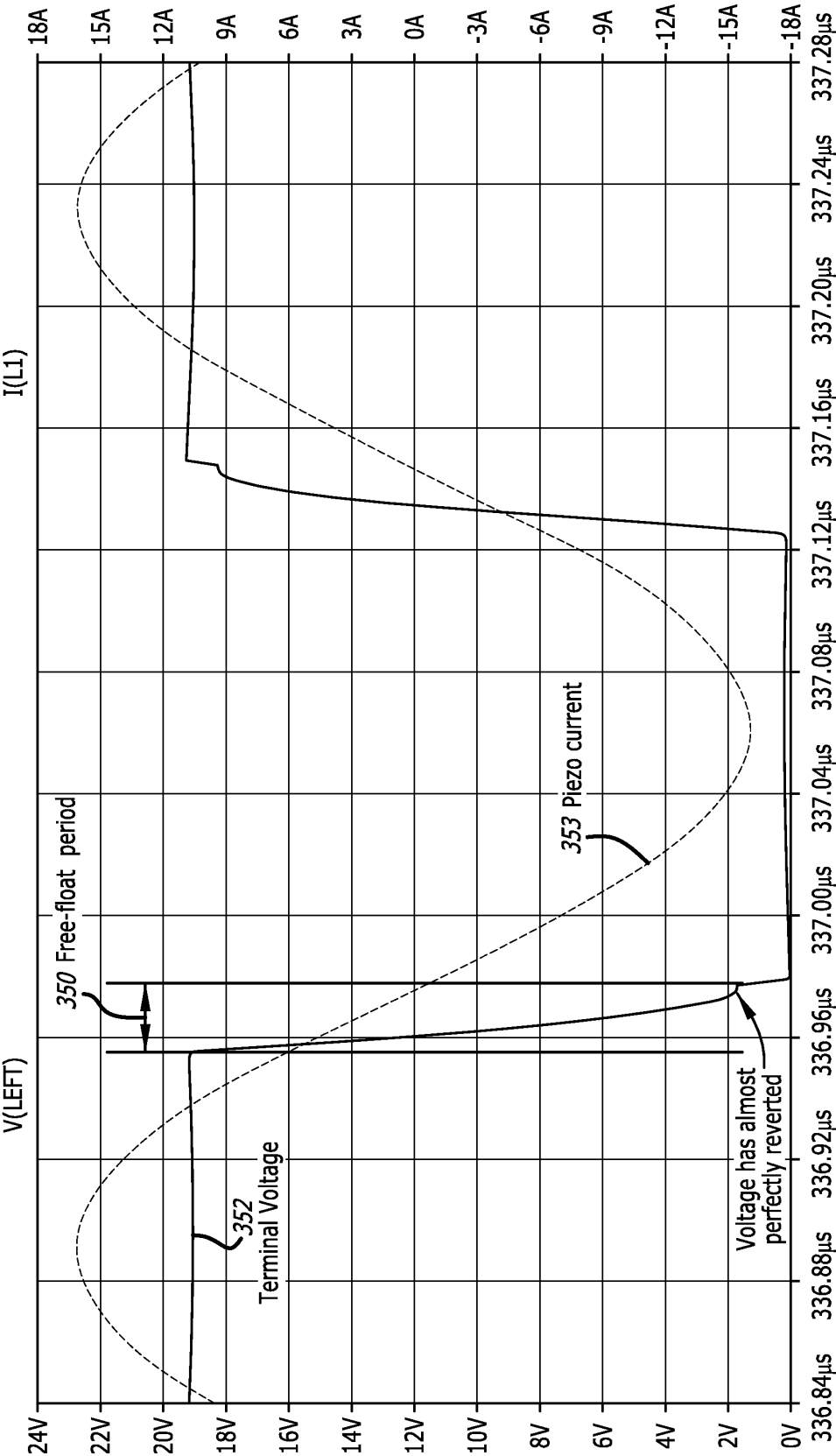


FIG. 13

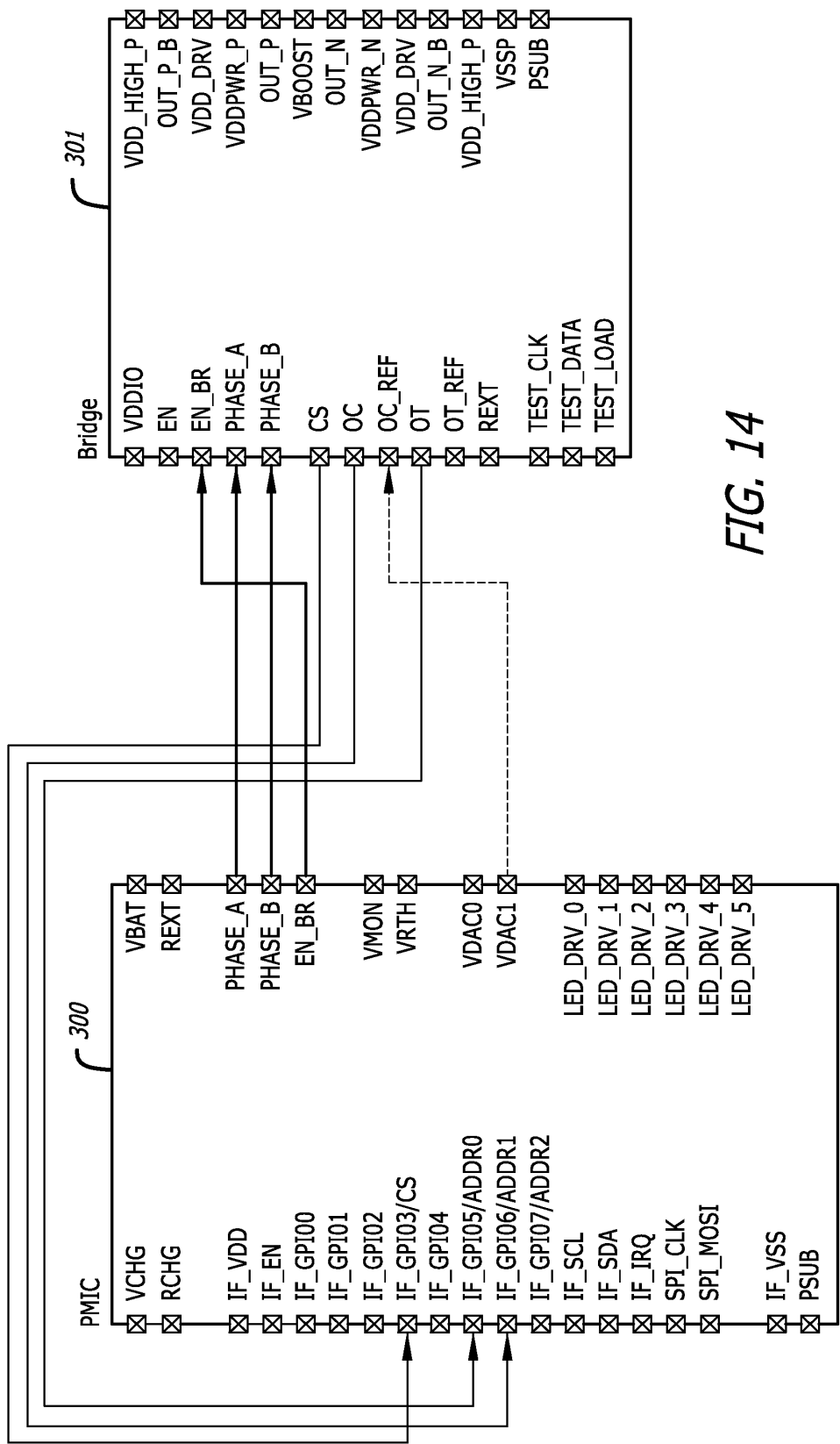


FIG. 14

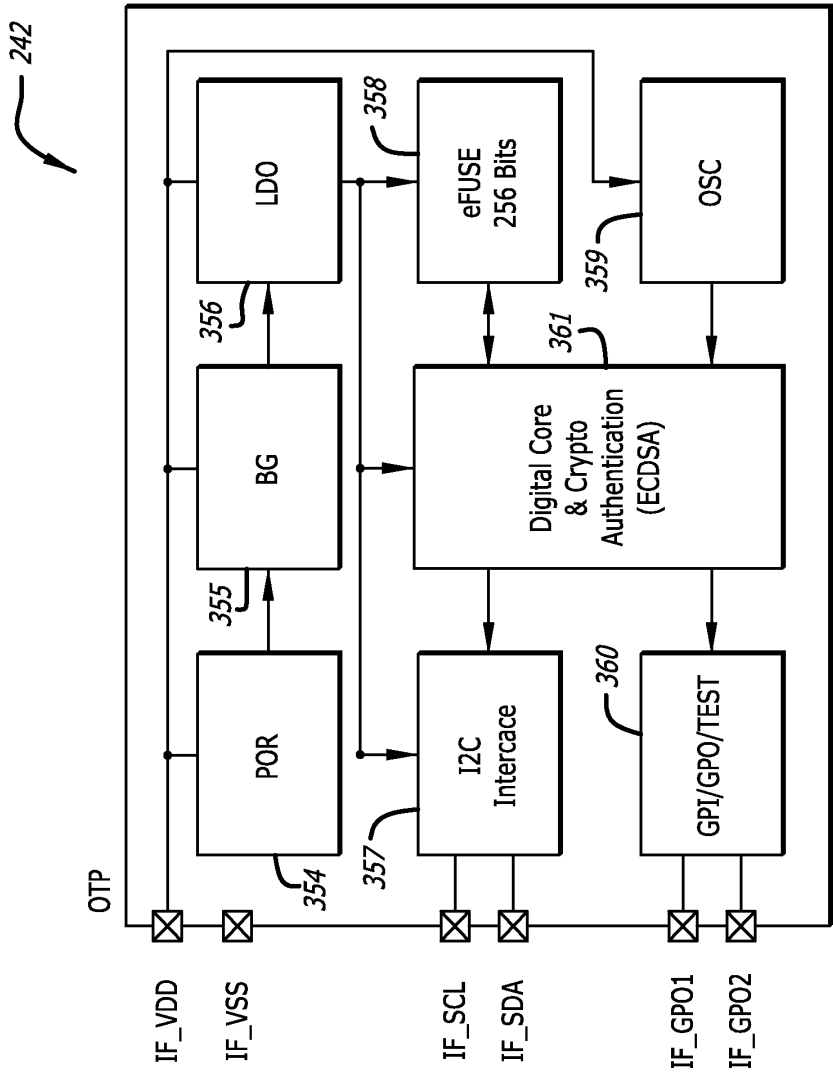


FIG. 15

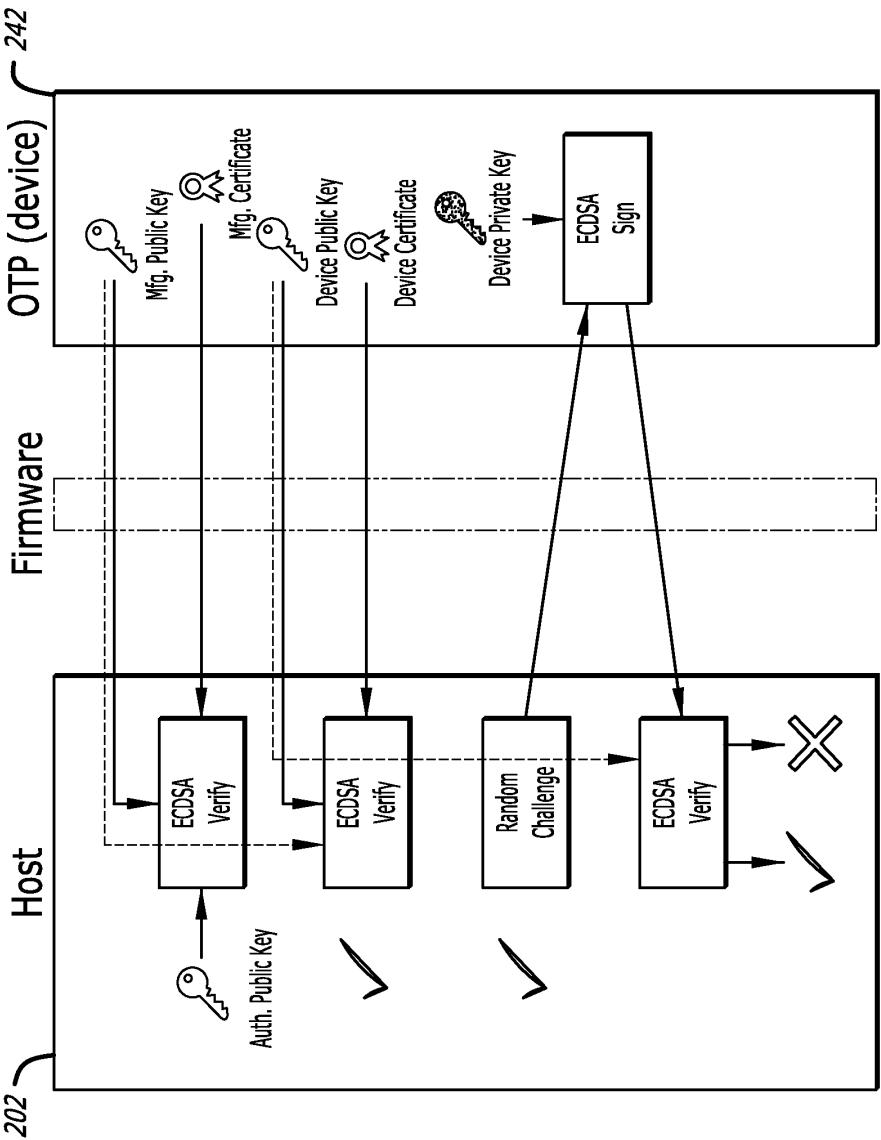


FIG. 16