

1/37

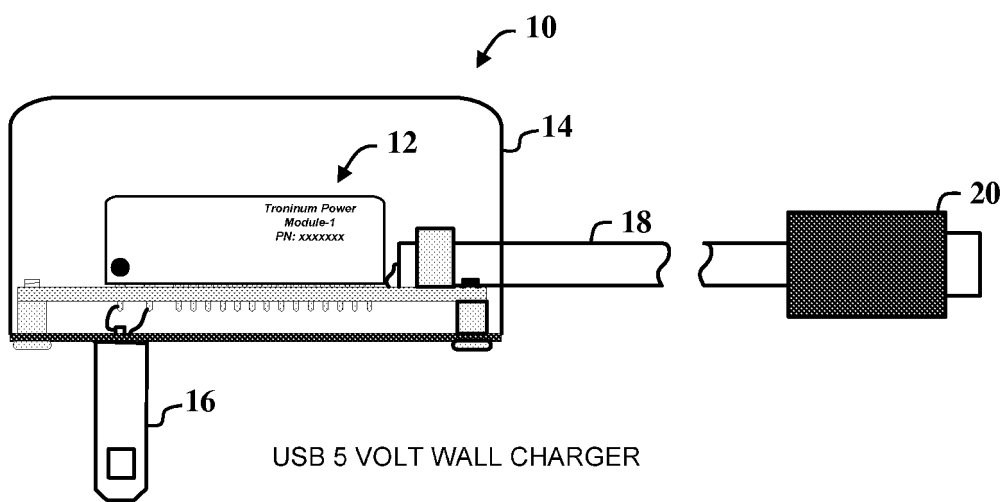


Figure 1

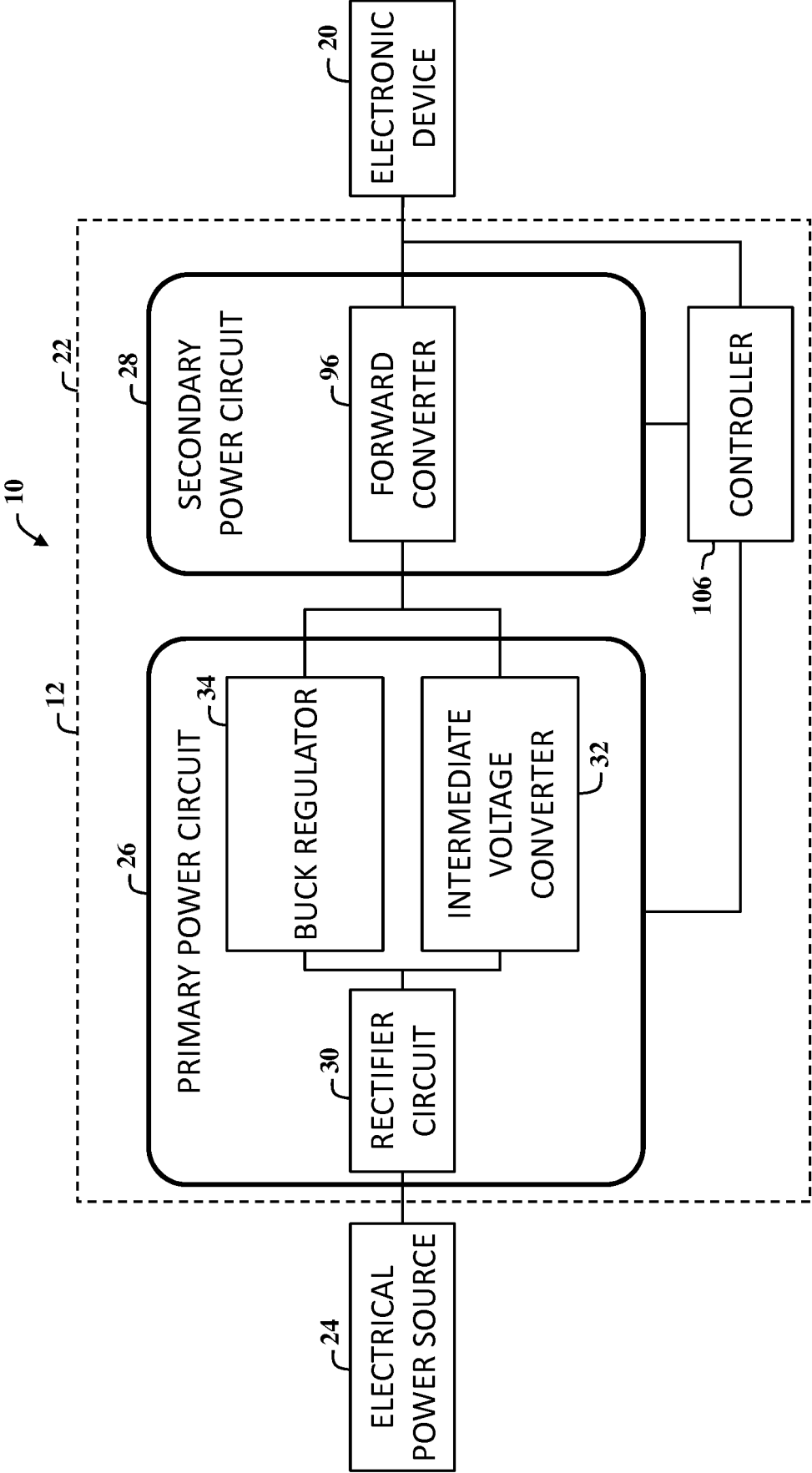


Figure 2

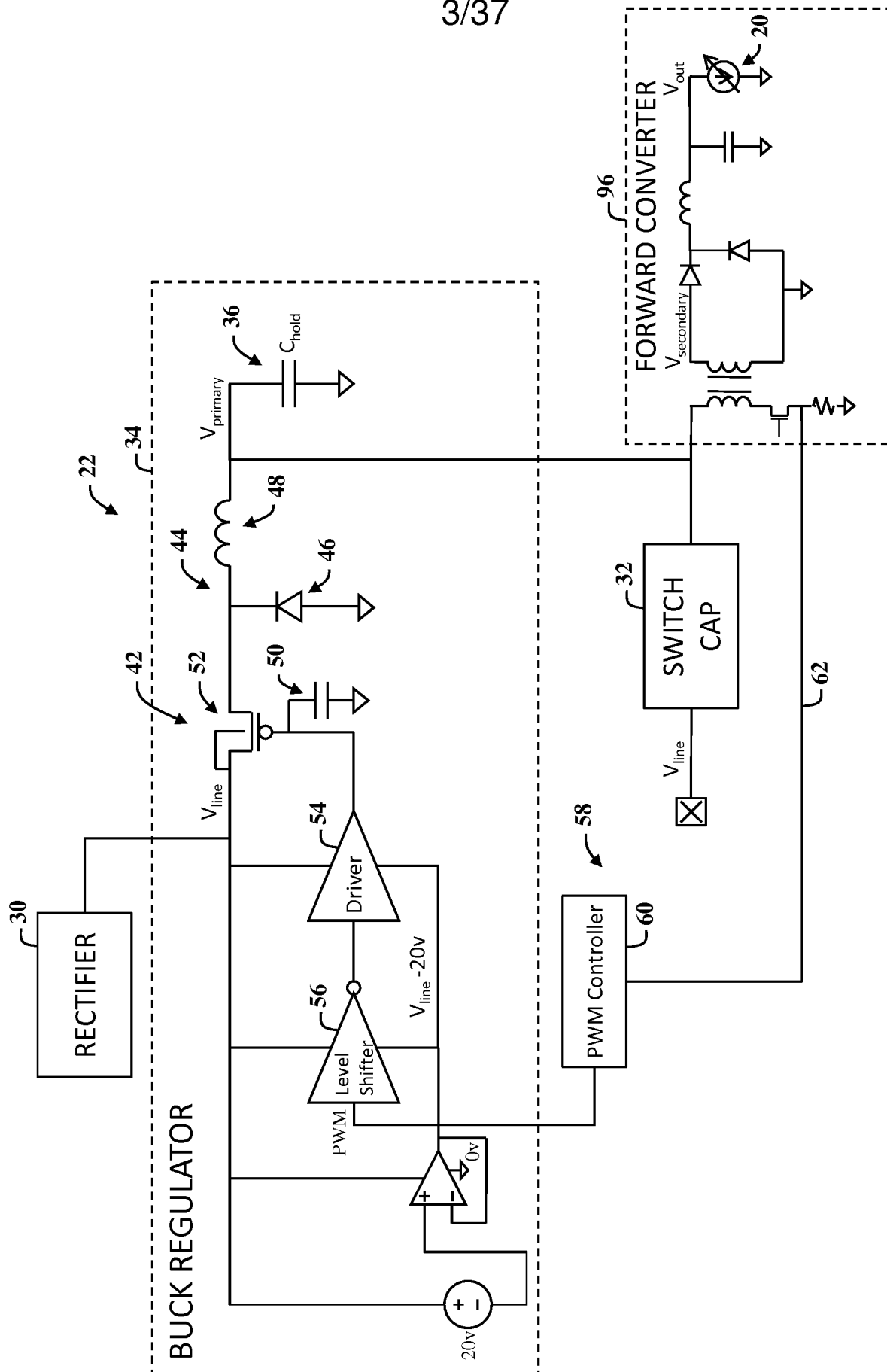


Figure 3

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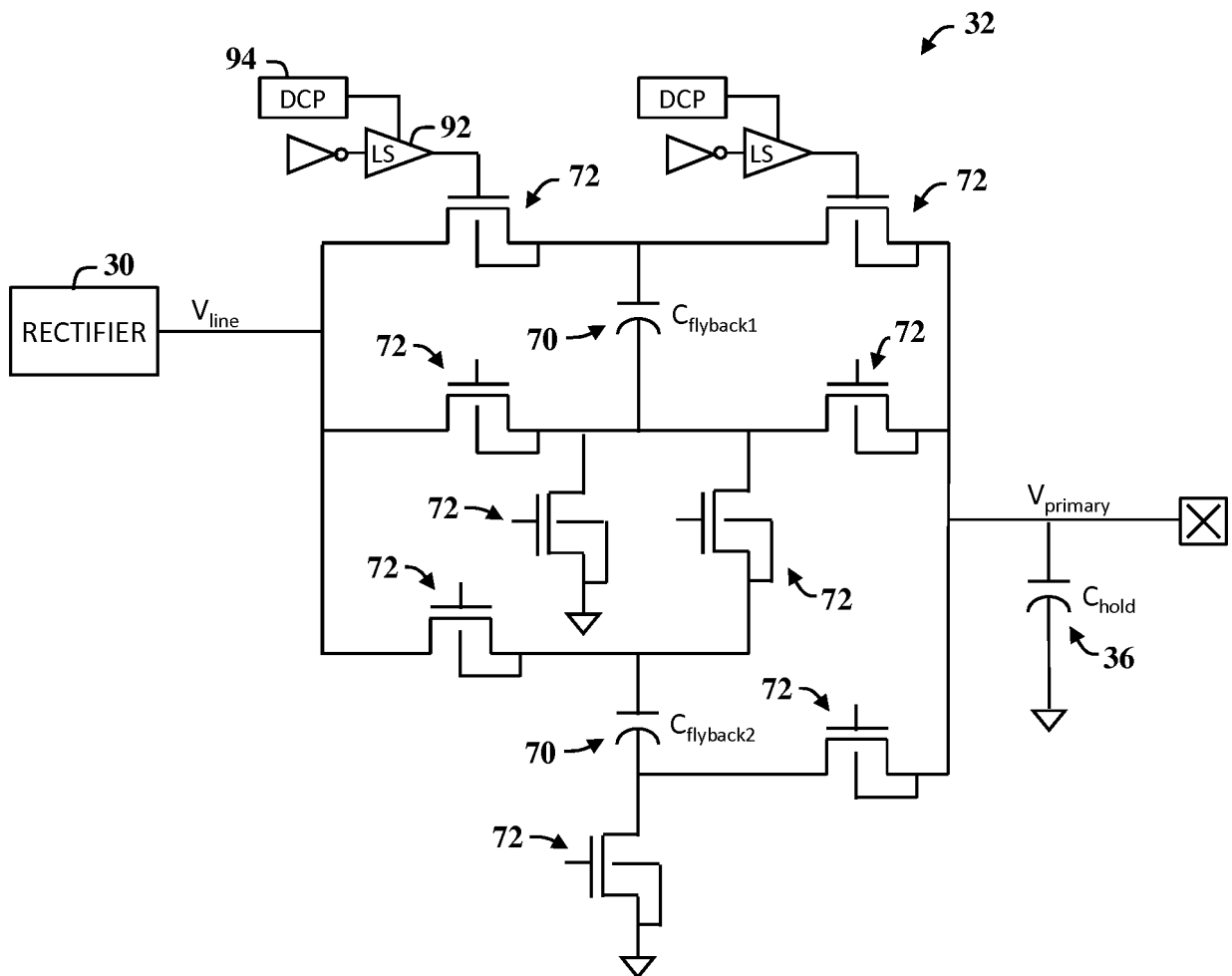


Figure 4

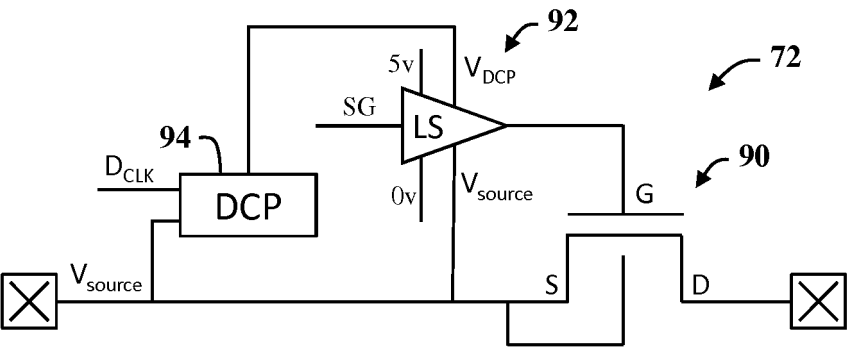


Figure 5

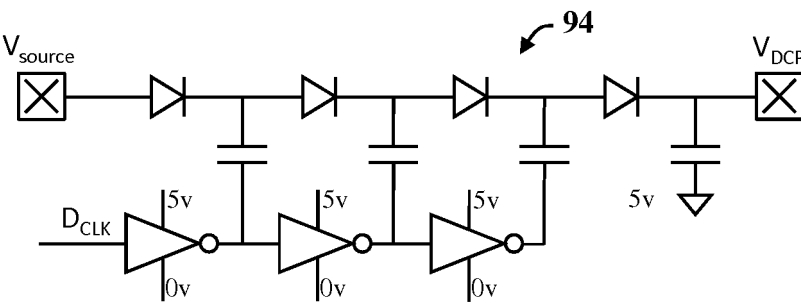


Figure 6

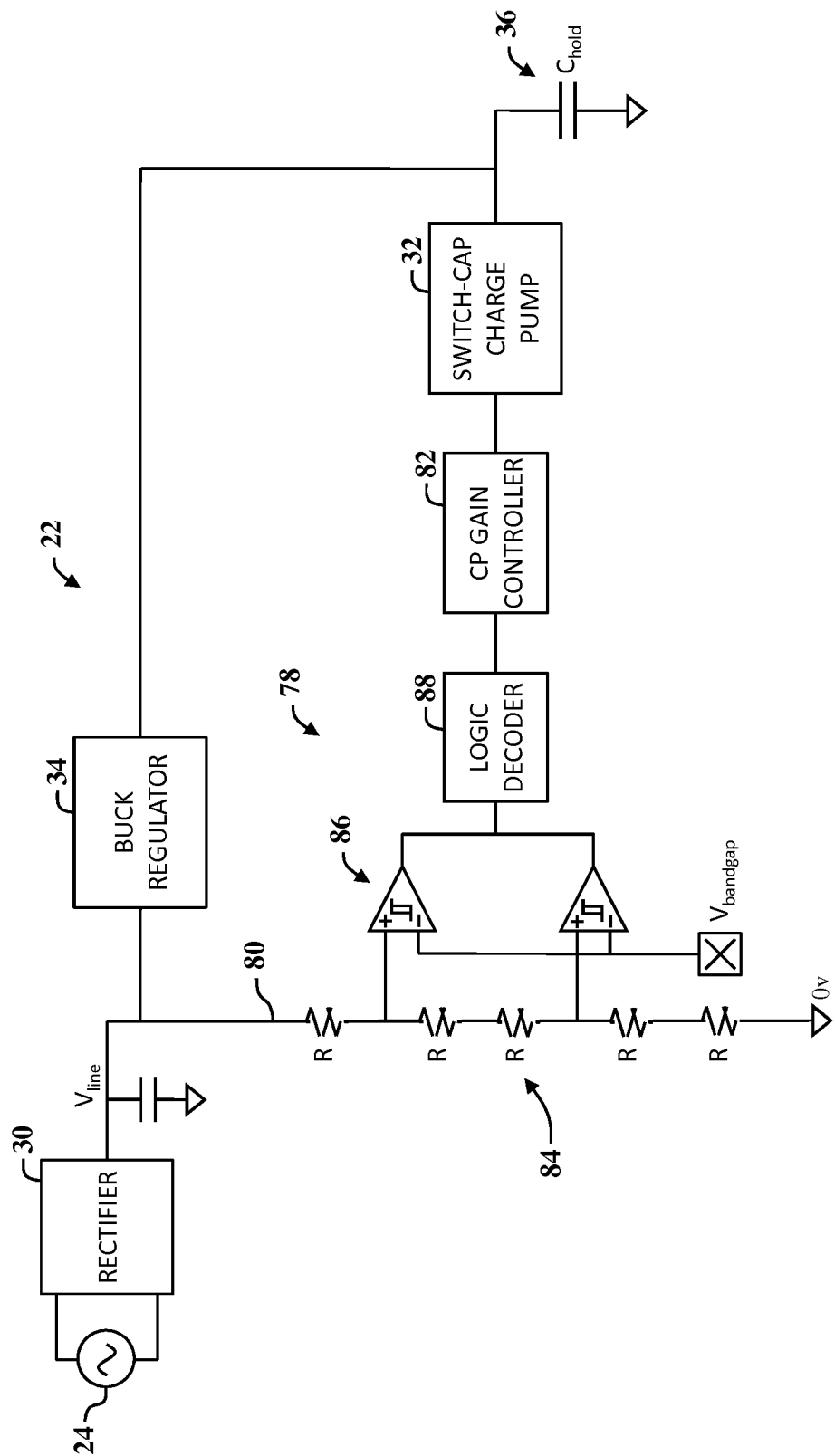


Figure 7

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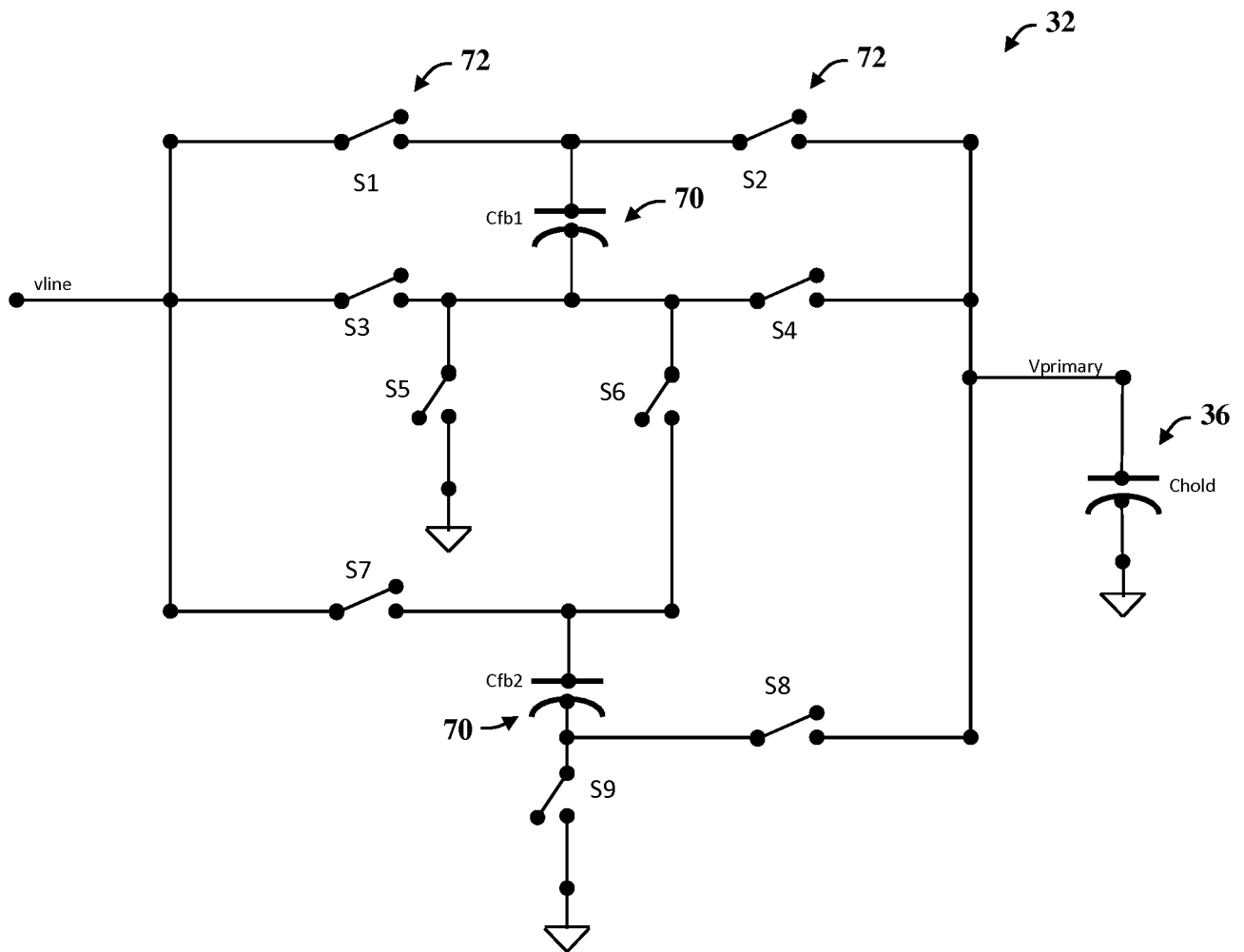
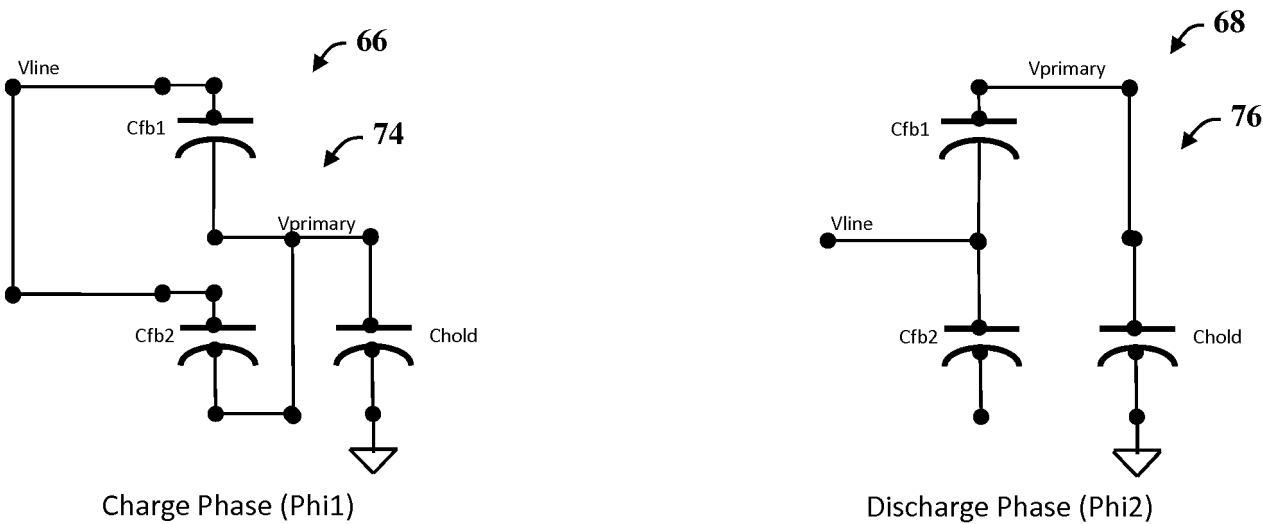


Figure 8

Switch Name	Phase 1	Phase 2 G=1/2	Phase 2 G=2/3	Phase 2 G=1
S1	On	Off	Off	Off
S2	Off	On	On	On
S3	Off	Off	Off	On
S4	On	Off	Off	Off
S5	Off	On	Off	Off
S6	Off	Off	On	Off
S7	On	Off	Off	On
S8	On	Off	Off	Off
S9	Off	On	On	Off

Figure 9



Switched Capacitor Gain: 1x

Figure 10

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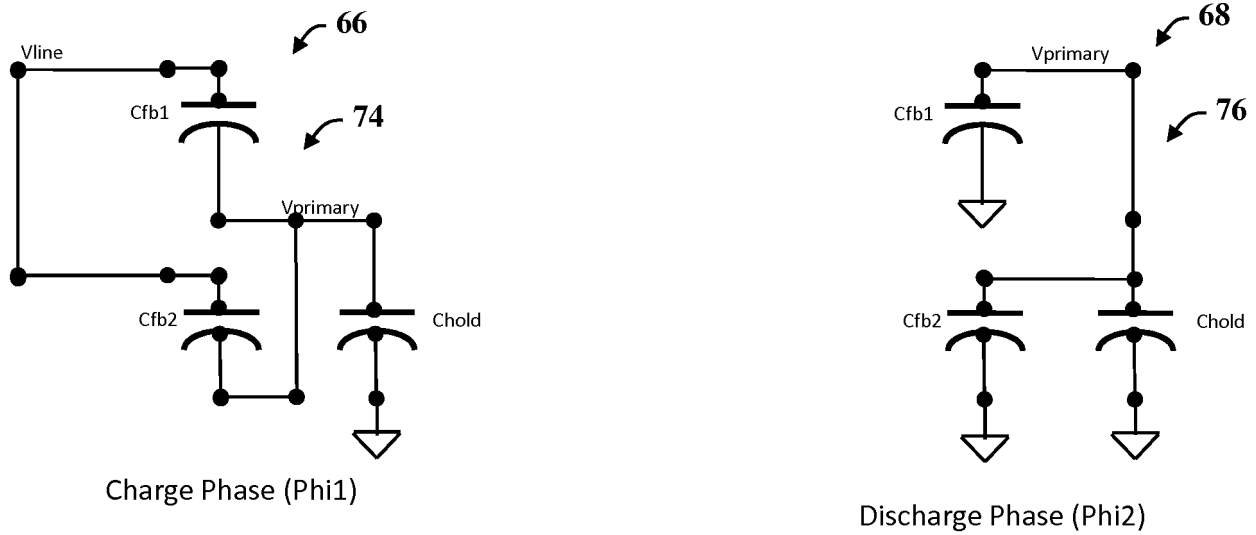
Switched Capacitor Gain: $1/2x$

Figure 11

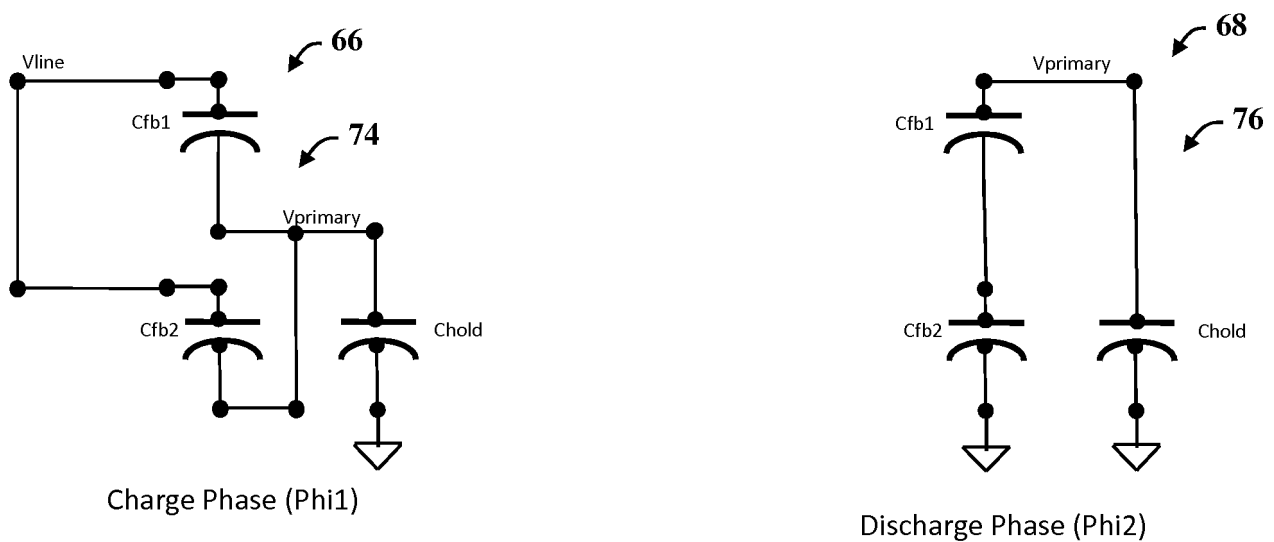
Switched Capacitor Gain: $2/3x$

Figure 12

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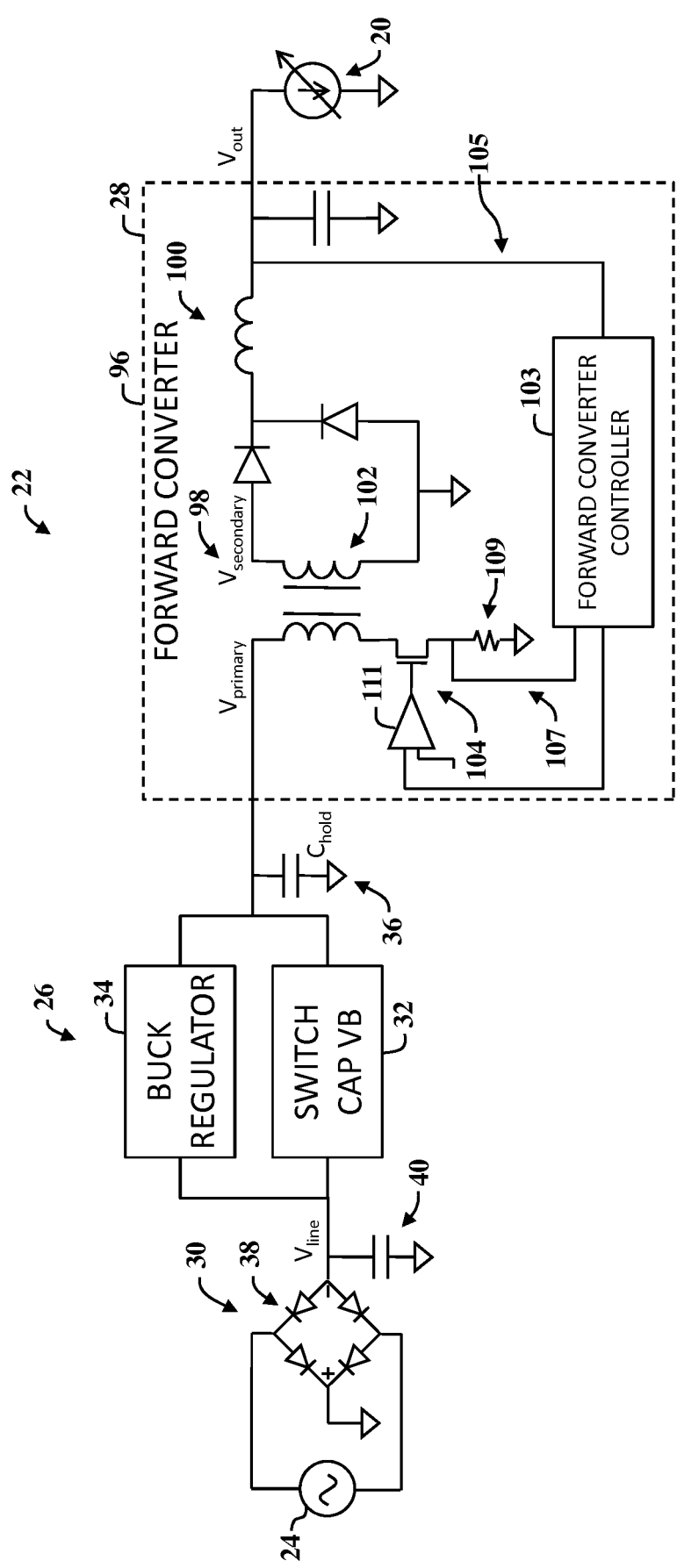


Figure 13

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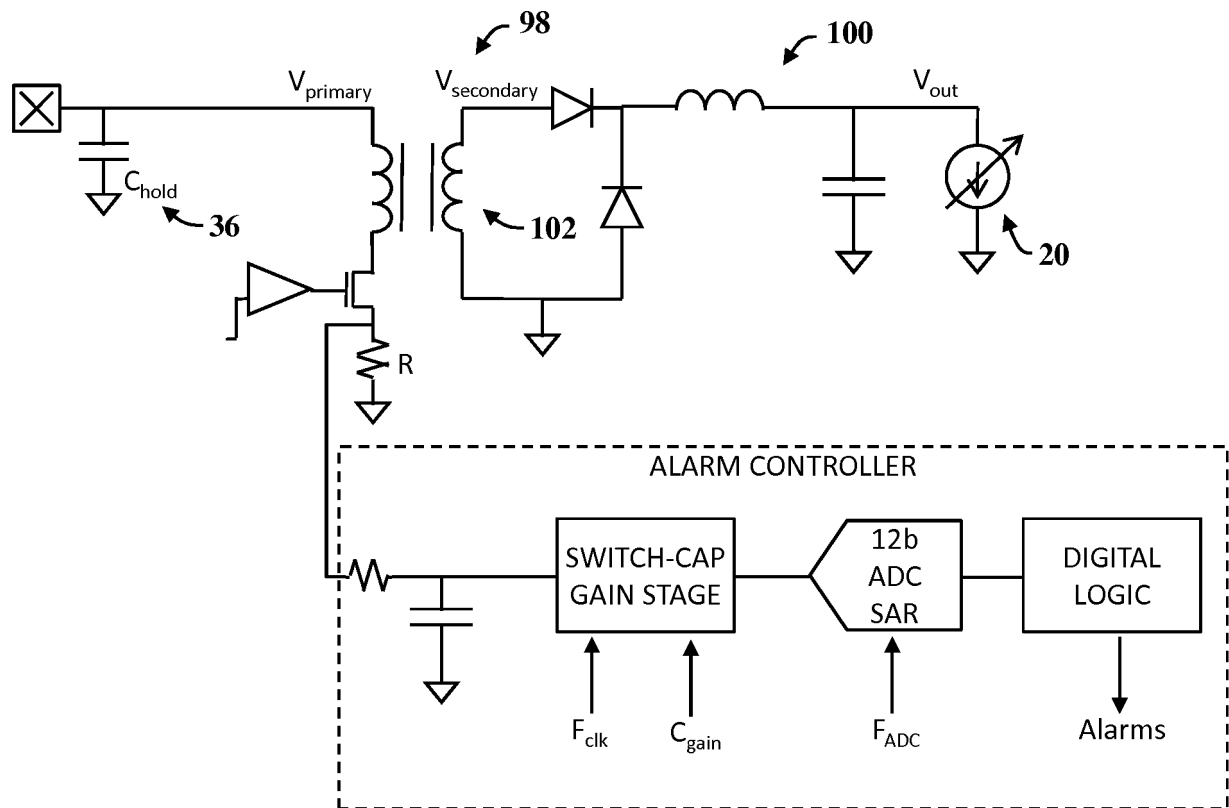


Figure 14

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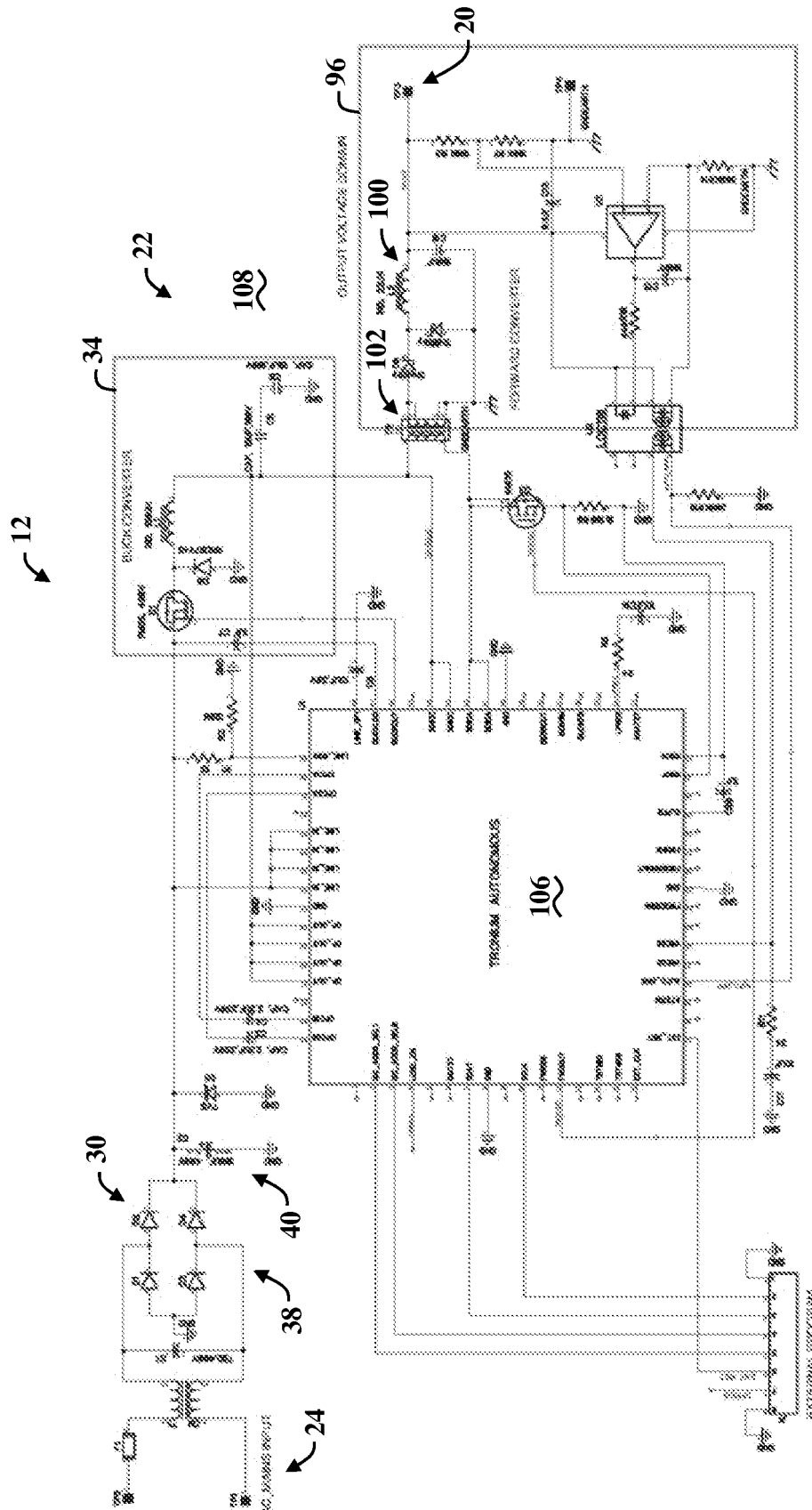


Figure 15

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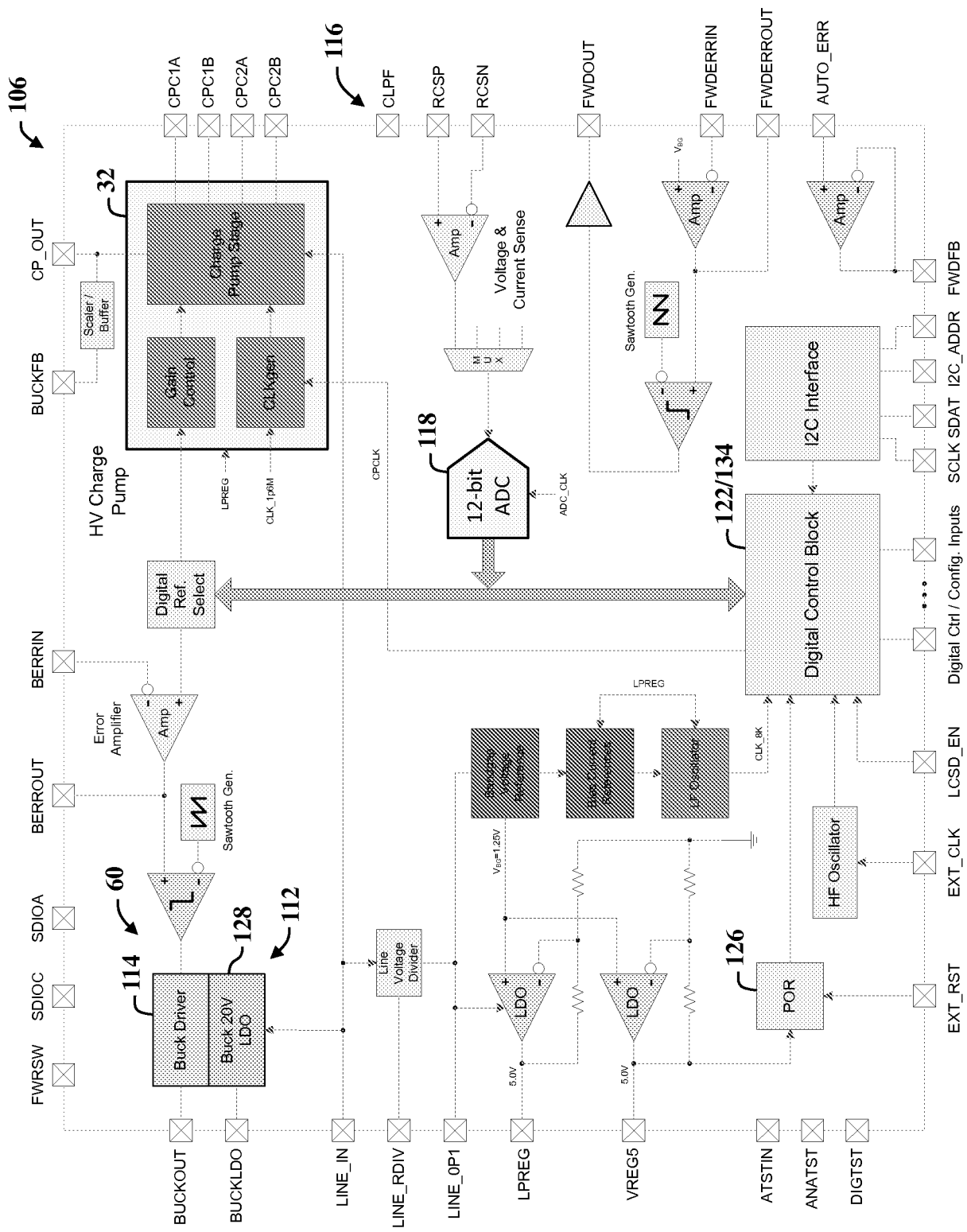


Figure 16

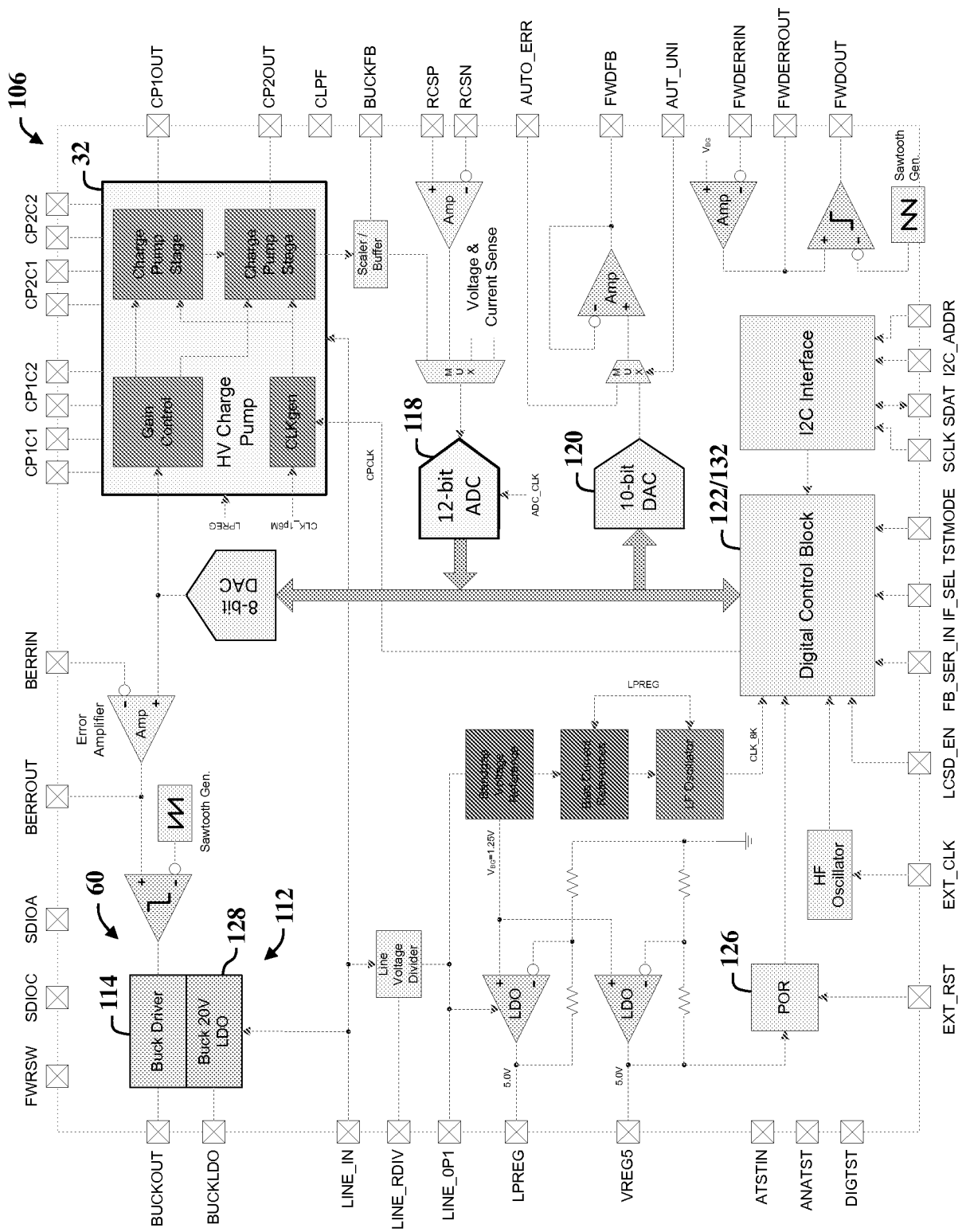


Figure 17A

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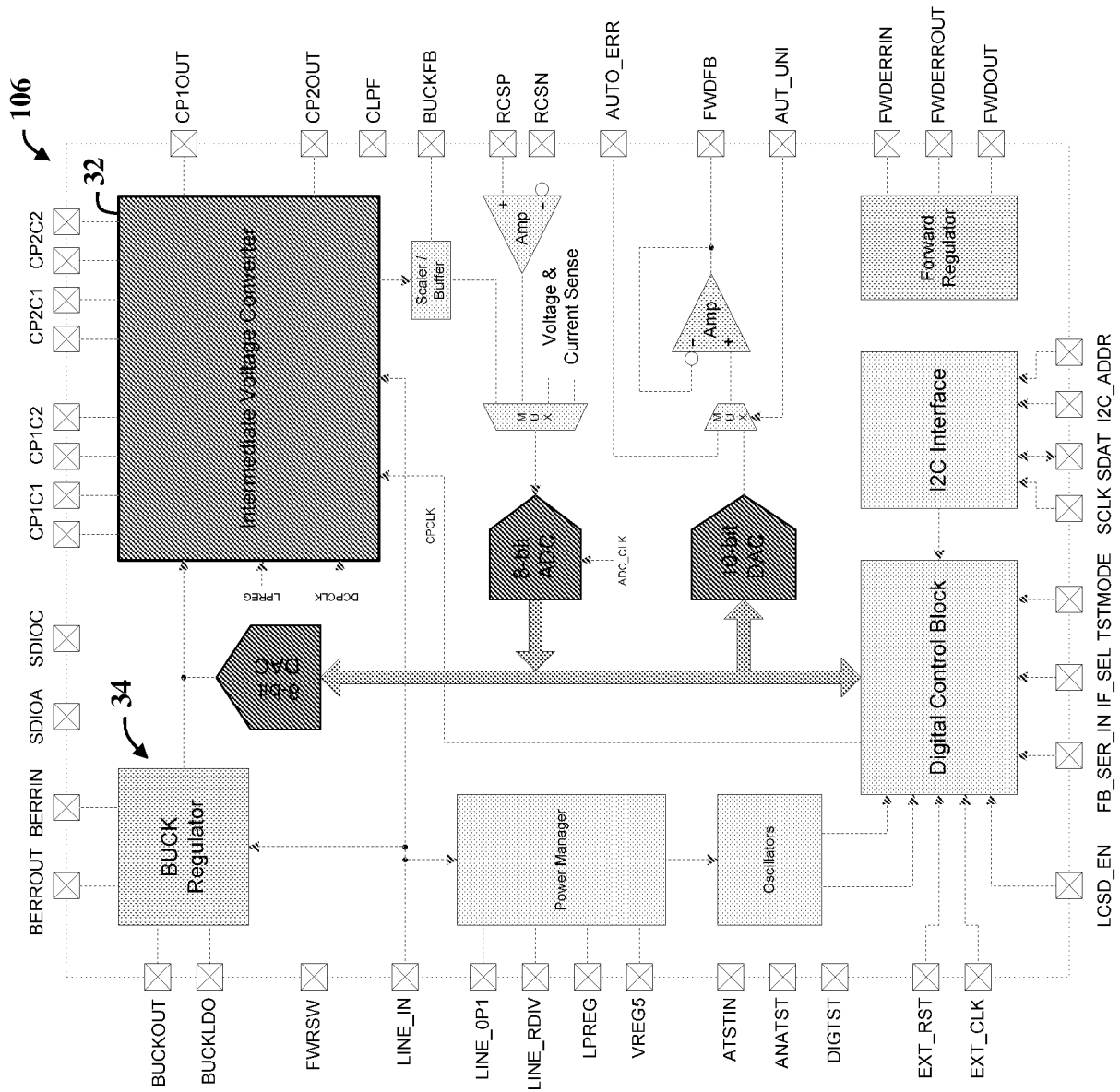


Figure 17B

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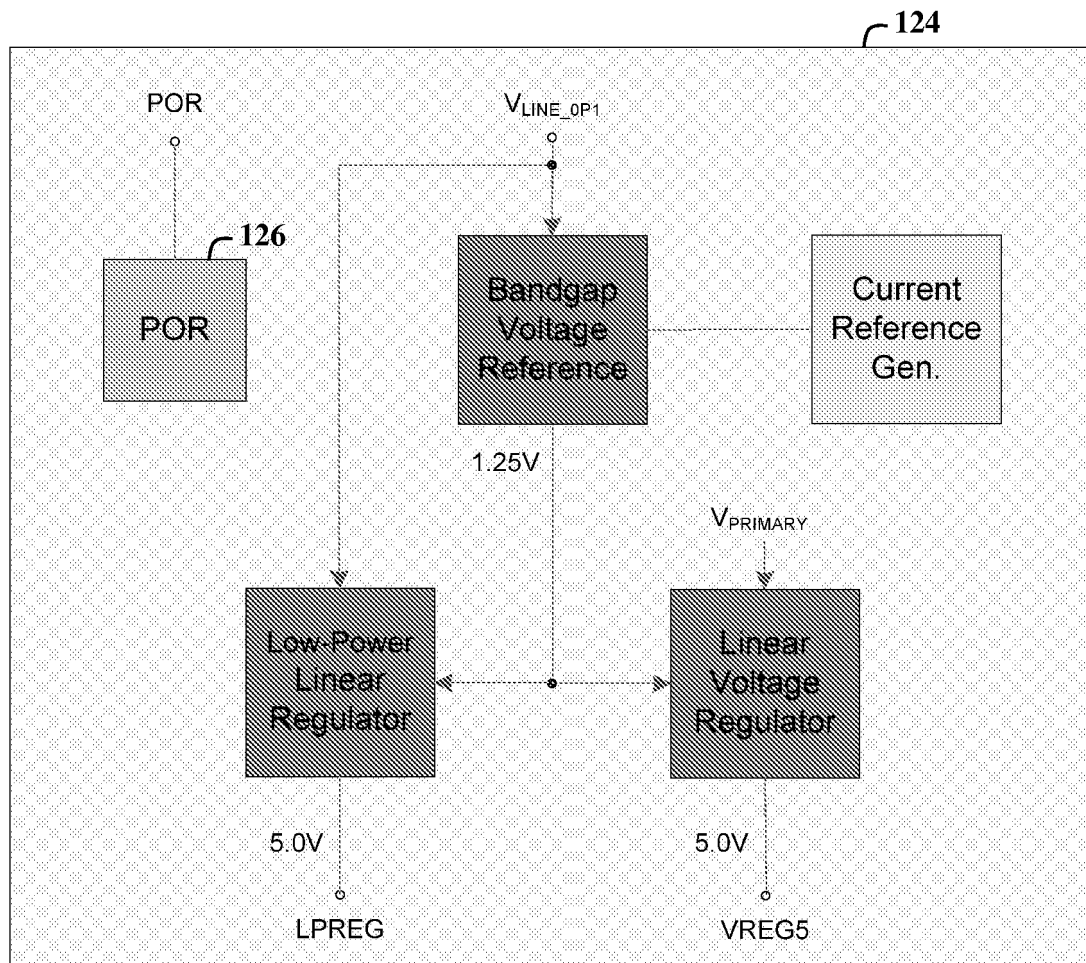
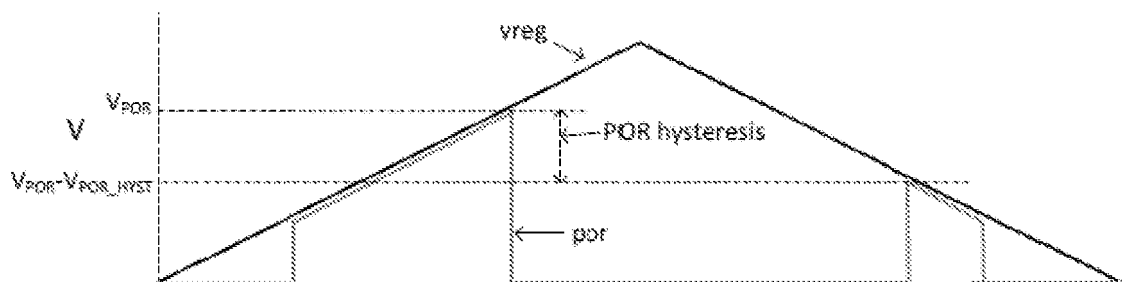


Figure 18



POR Threshold Voltages

Figure 19

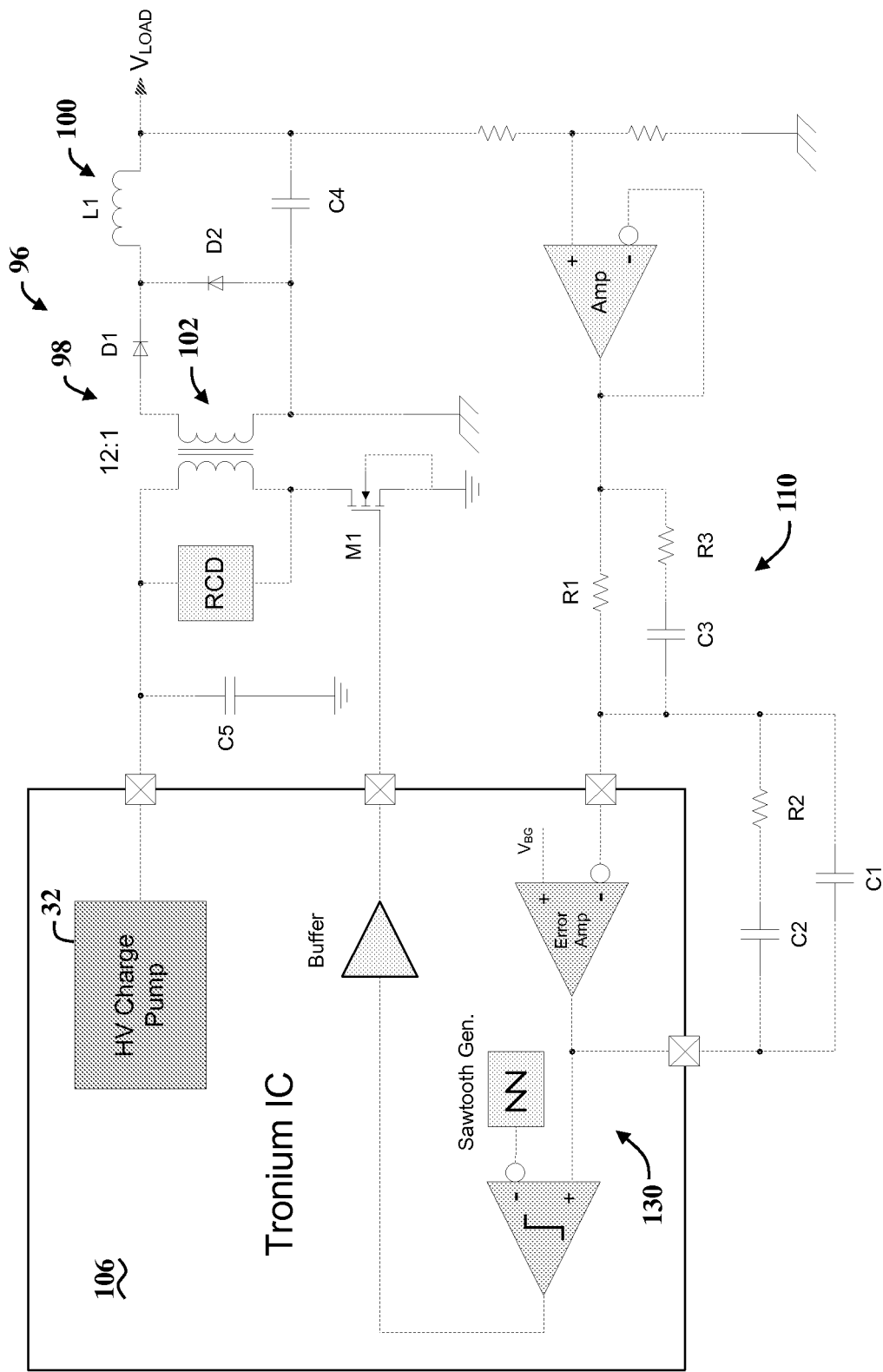


Figure 20

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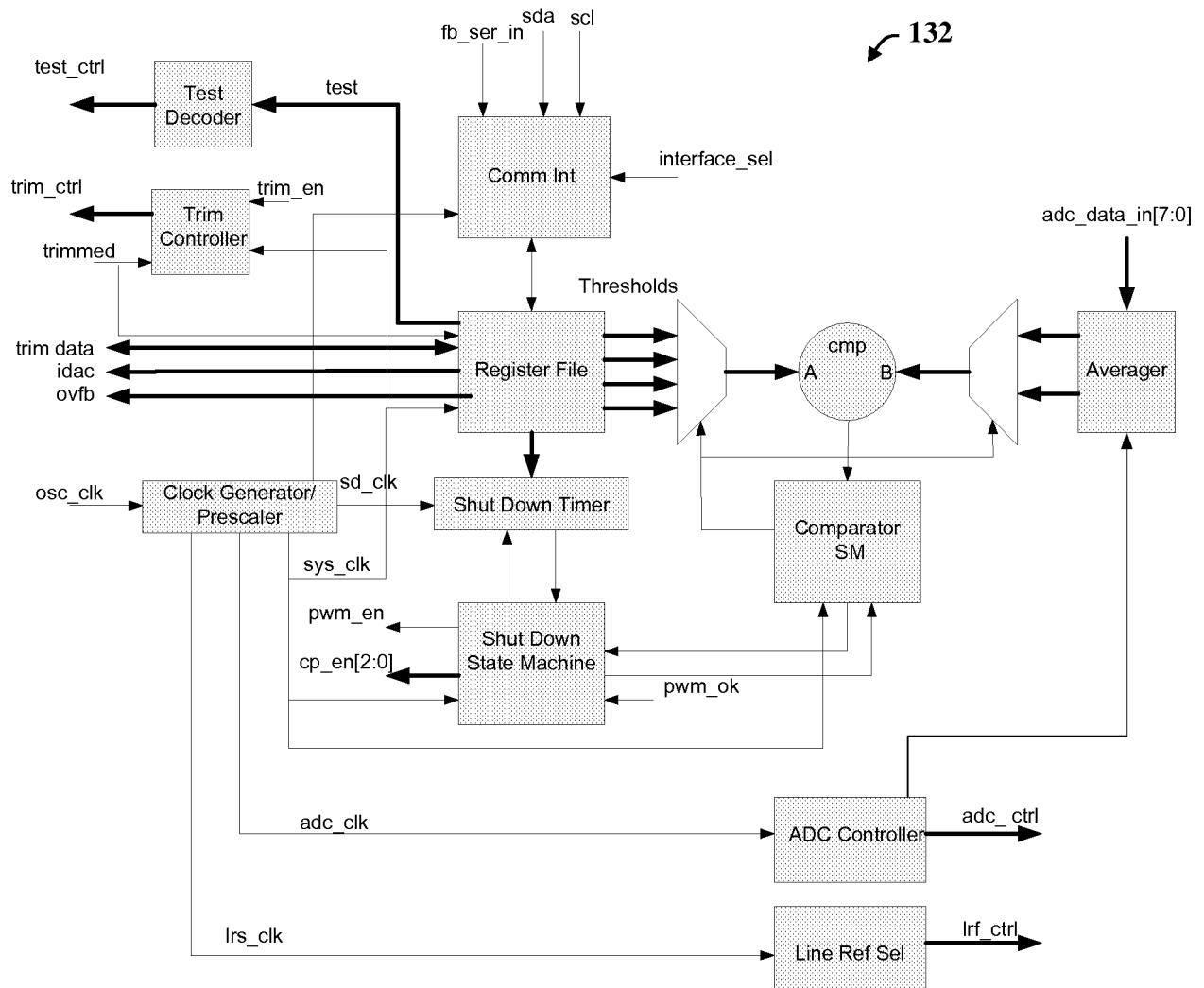


Figure 21

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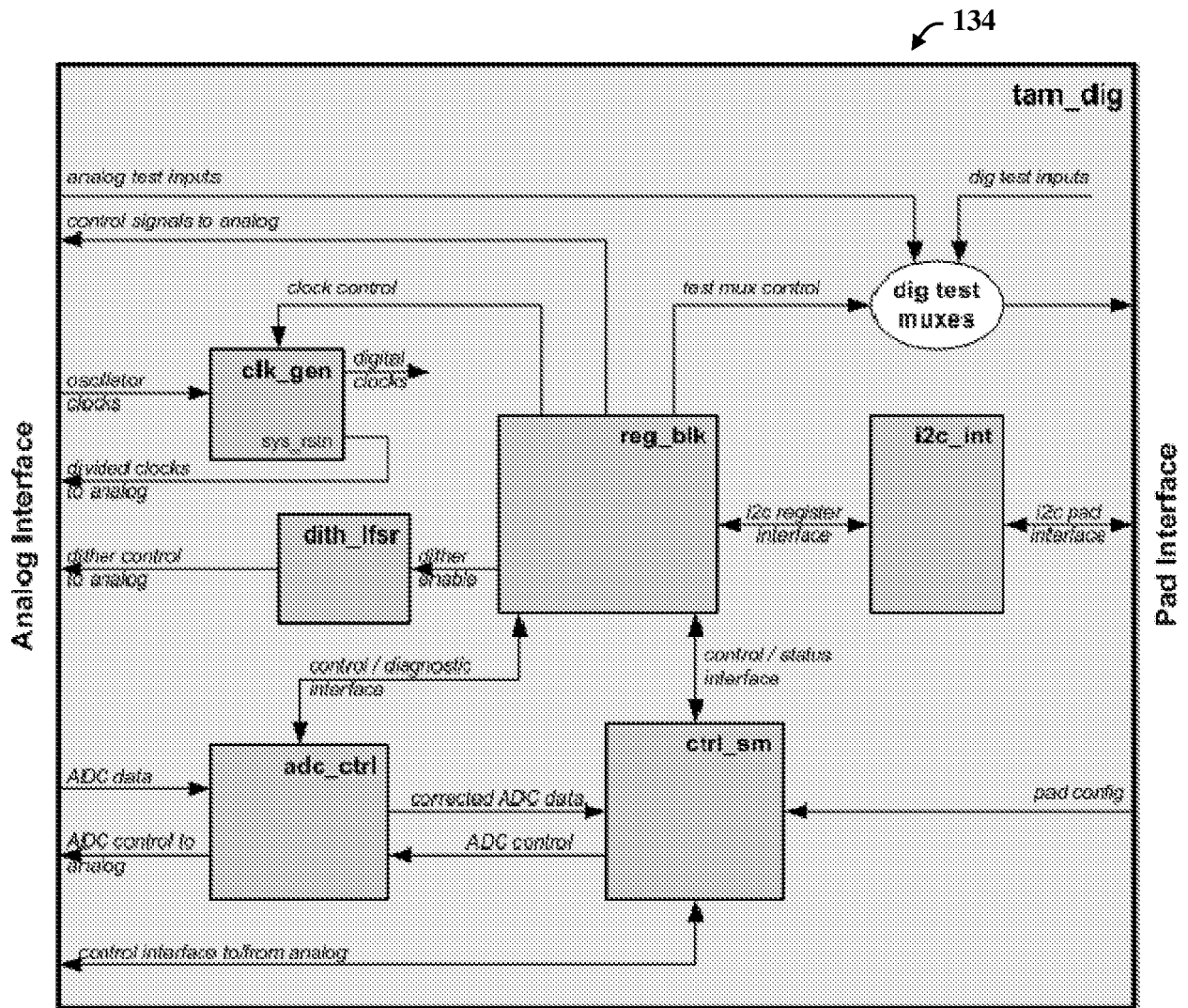


Figure 22

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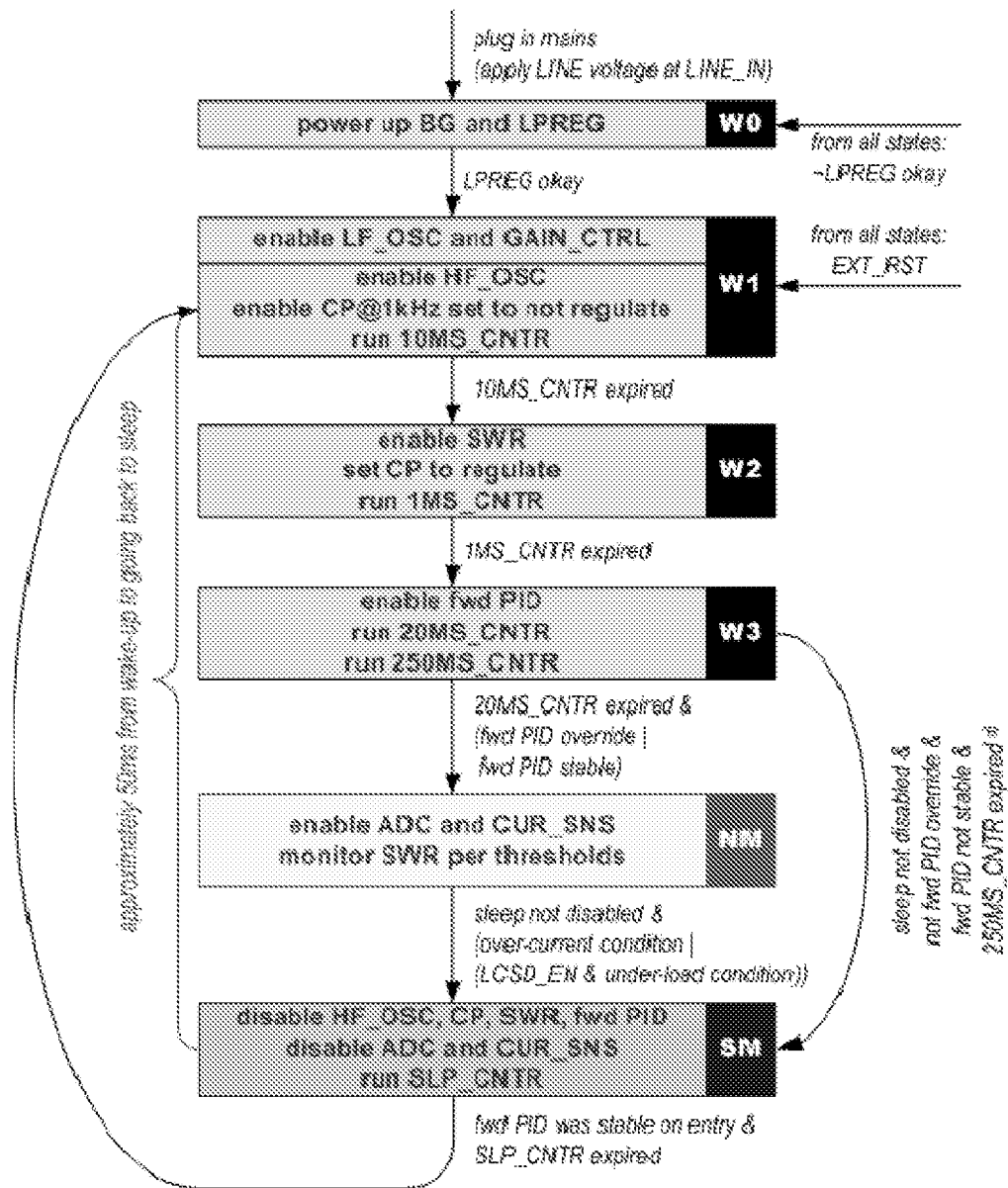


Figure 23

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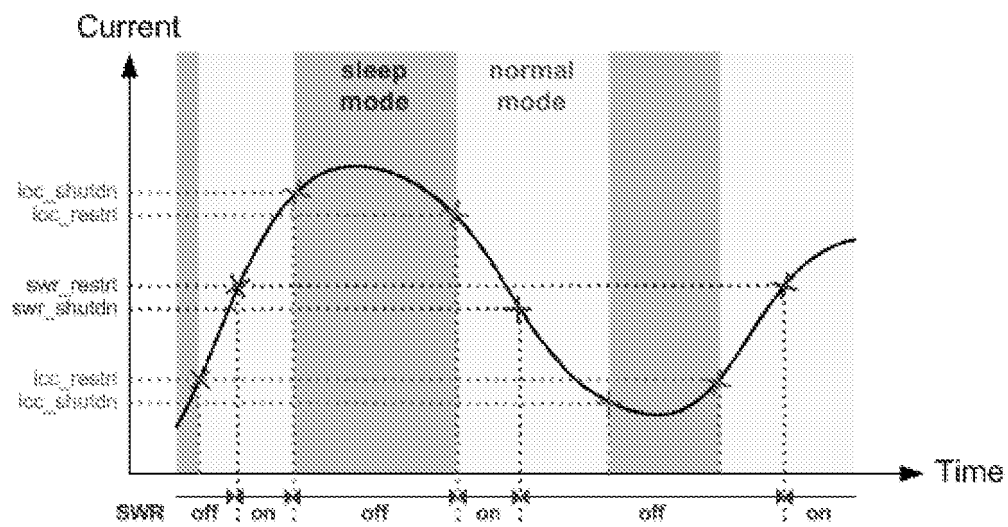


Figure 24

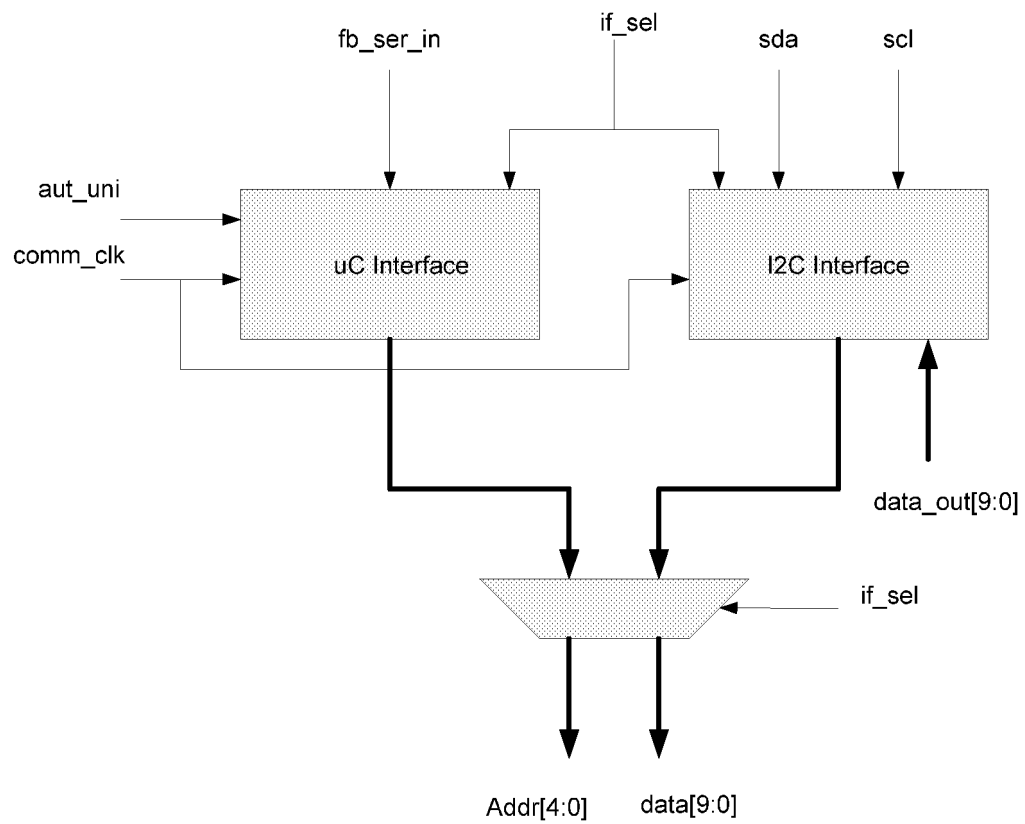


Figure 25

Packet Format

START	R/W	Addr[4:0]	Data[9:0]	IDLE

Start = Not Idle State
Idle = Inactive state from Optical Isolator
R/W = 1 for Read

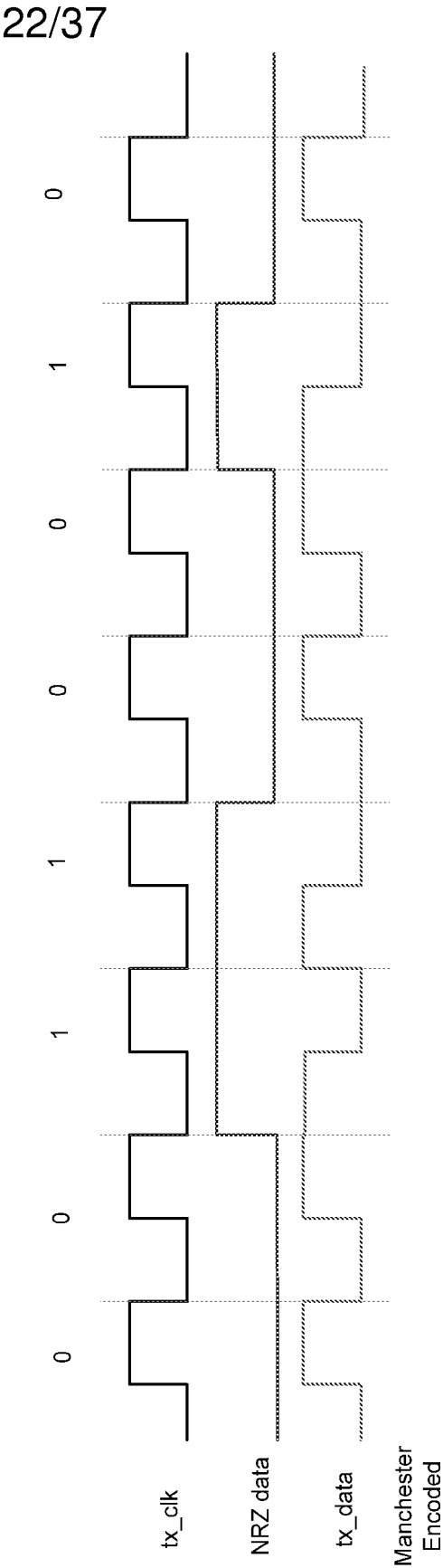
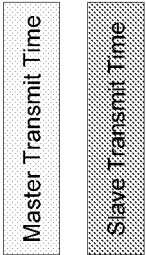


Figure 26



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I2C Transmit
RWB = '0'

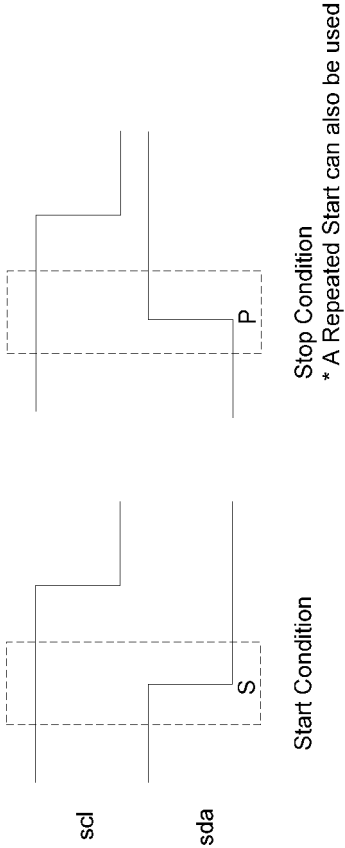
S	Slave Address[6:0]	R/A W/C B K	Byte 1 Addr[7:0]	A C K	Byte 2 Data[7:0]	* P
---	--------------------	-------------------	------------------	-------	------------------	-----

Tronium RF
Address = [4:0]

I2C Receive
RWB = '1'

S	Slave Address[6:0]	R/A W/C B K	Byte 1 Data[7:0]	N A C K	* P
---	--------------------	-------------------	------------------	---------	-----

Start and Stop Waveforms



I2C Bit Transfer Timing

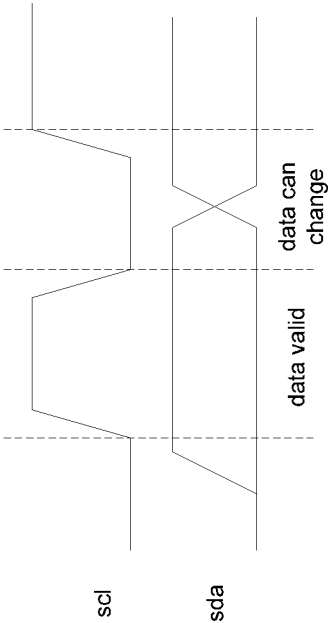


Figure 27

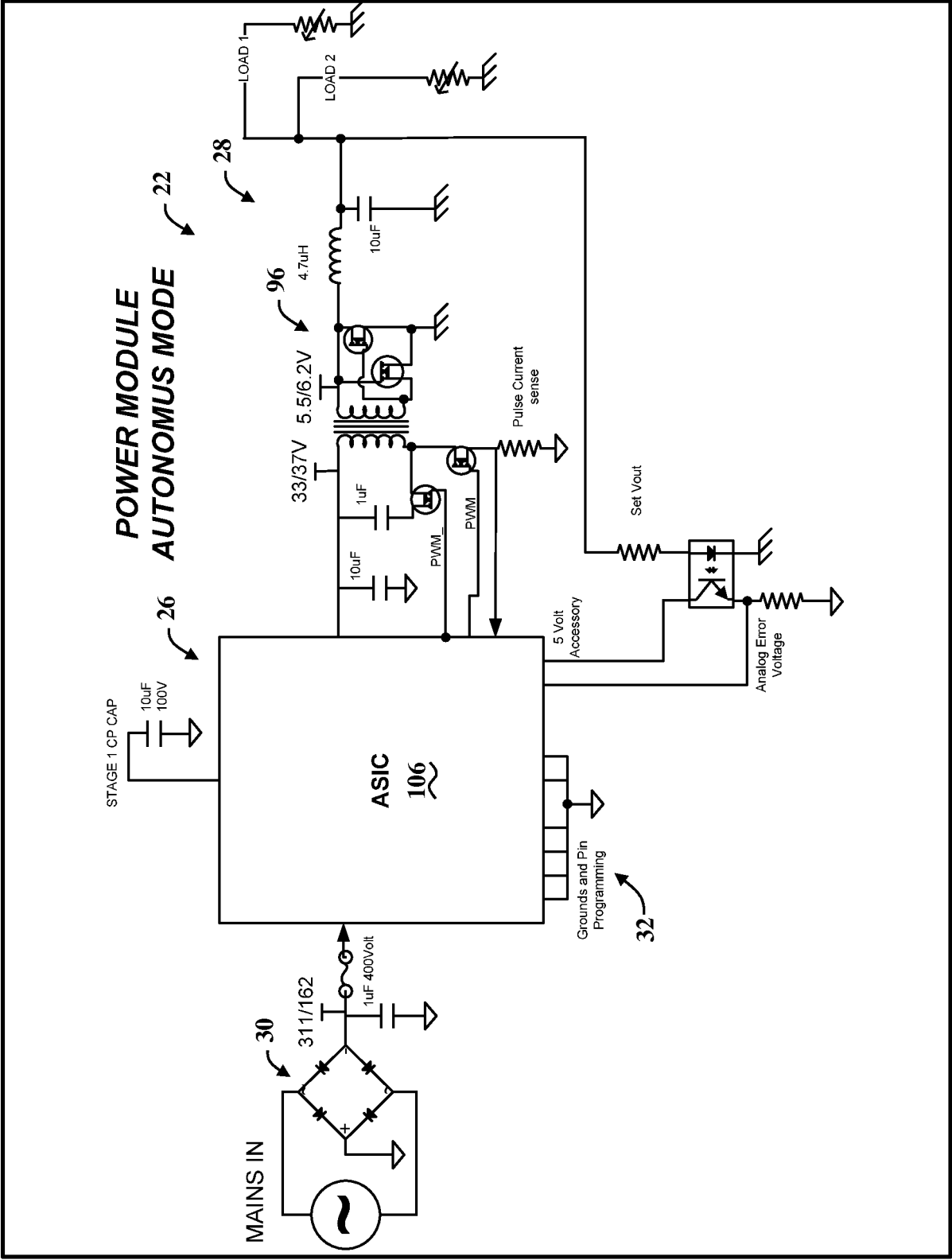


Figure 28

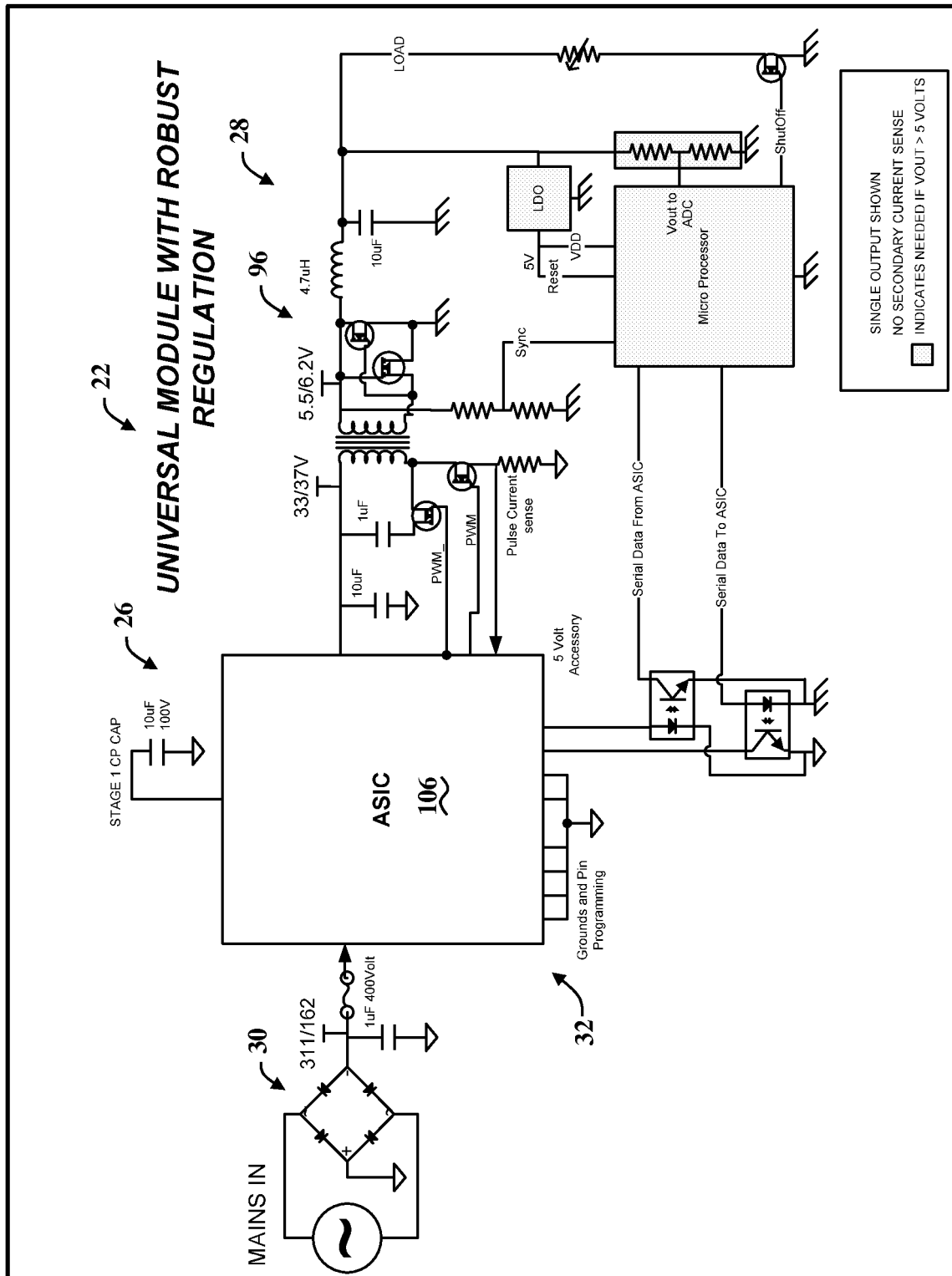


Figure 29

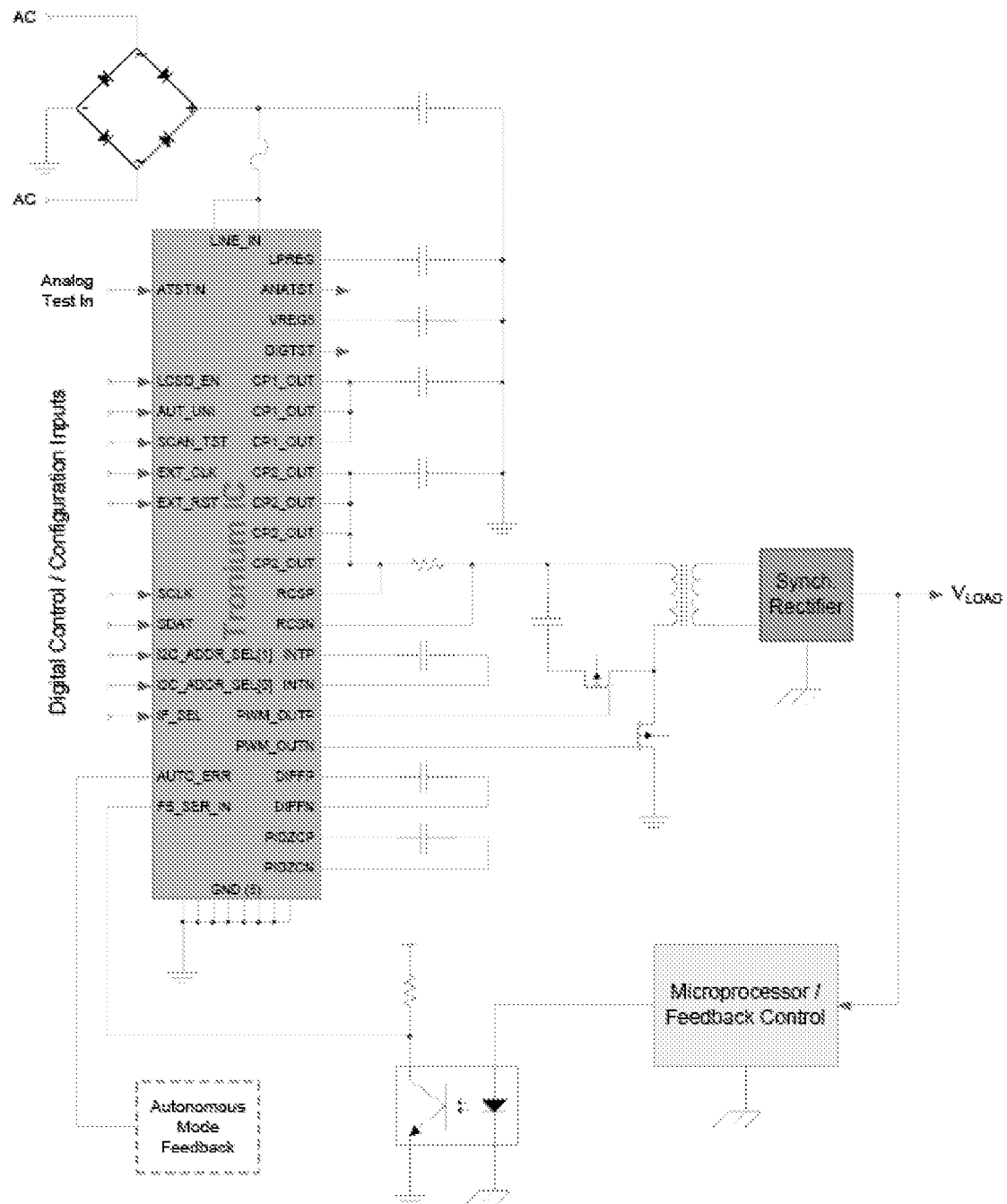


Figure 30

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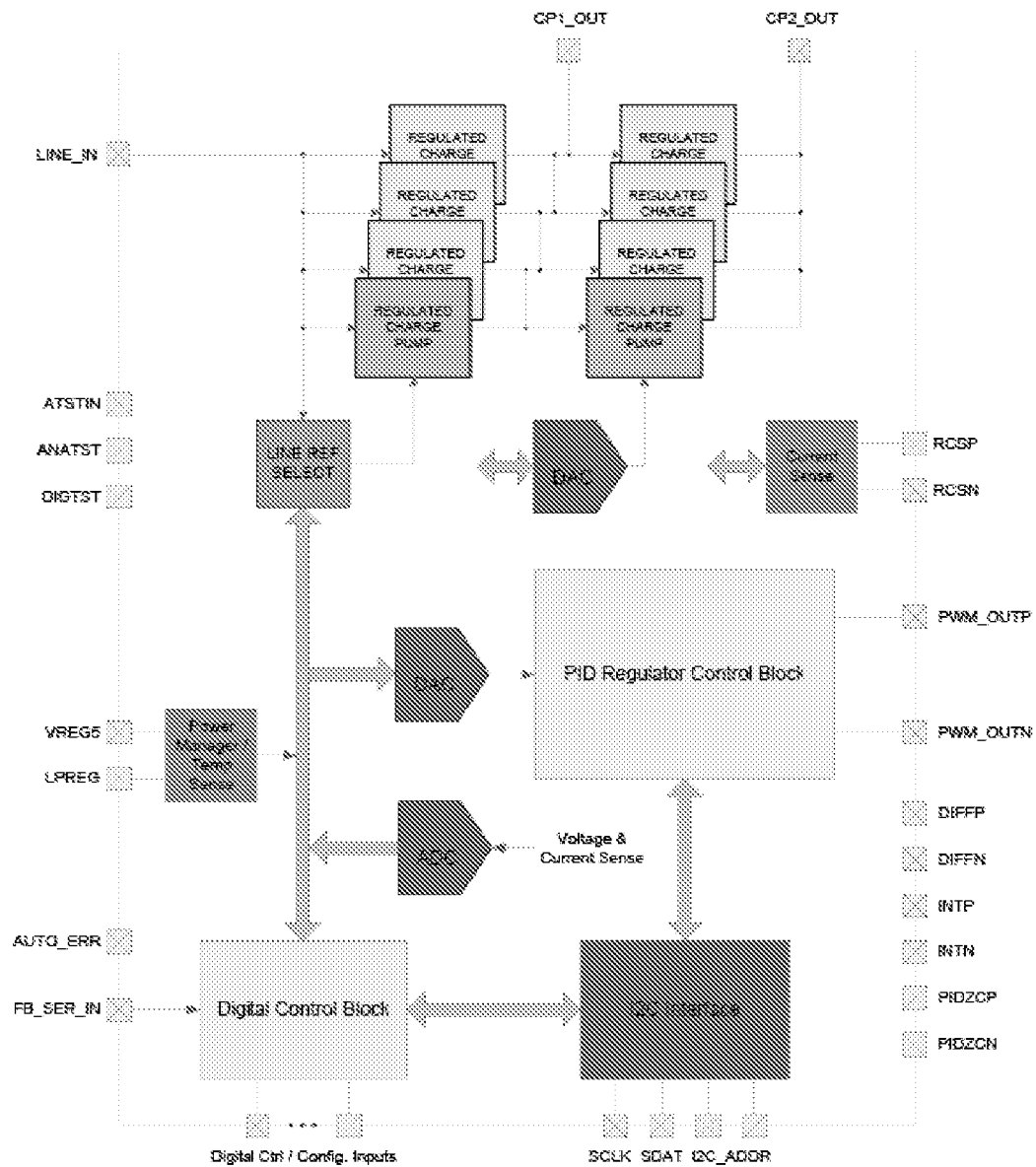


Figure 31

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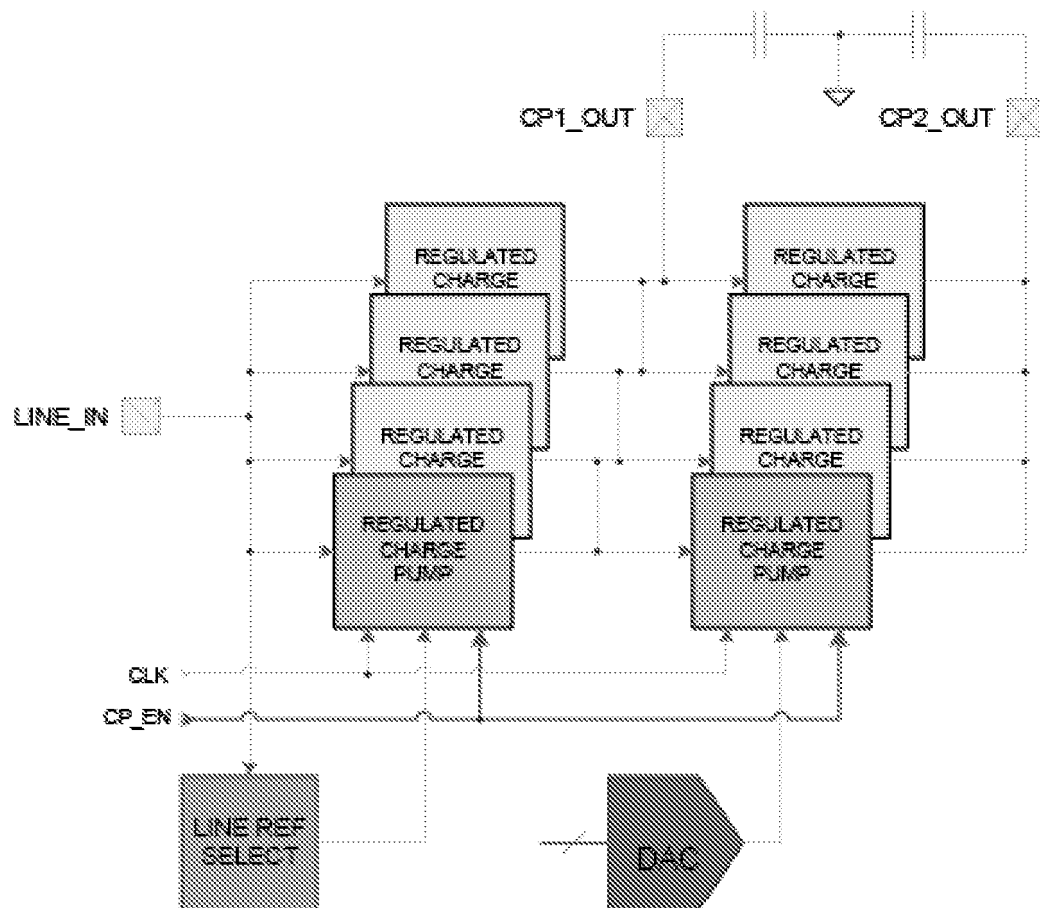


Figure 32

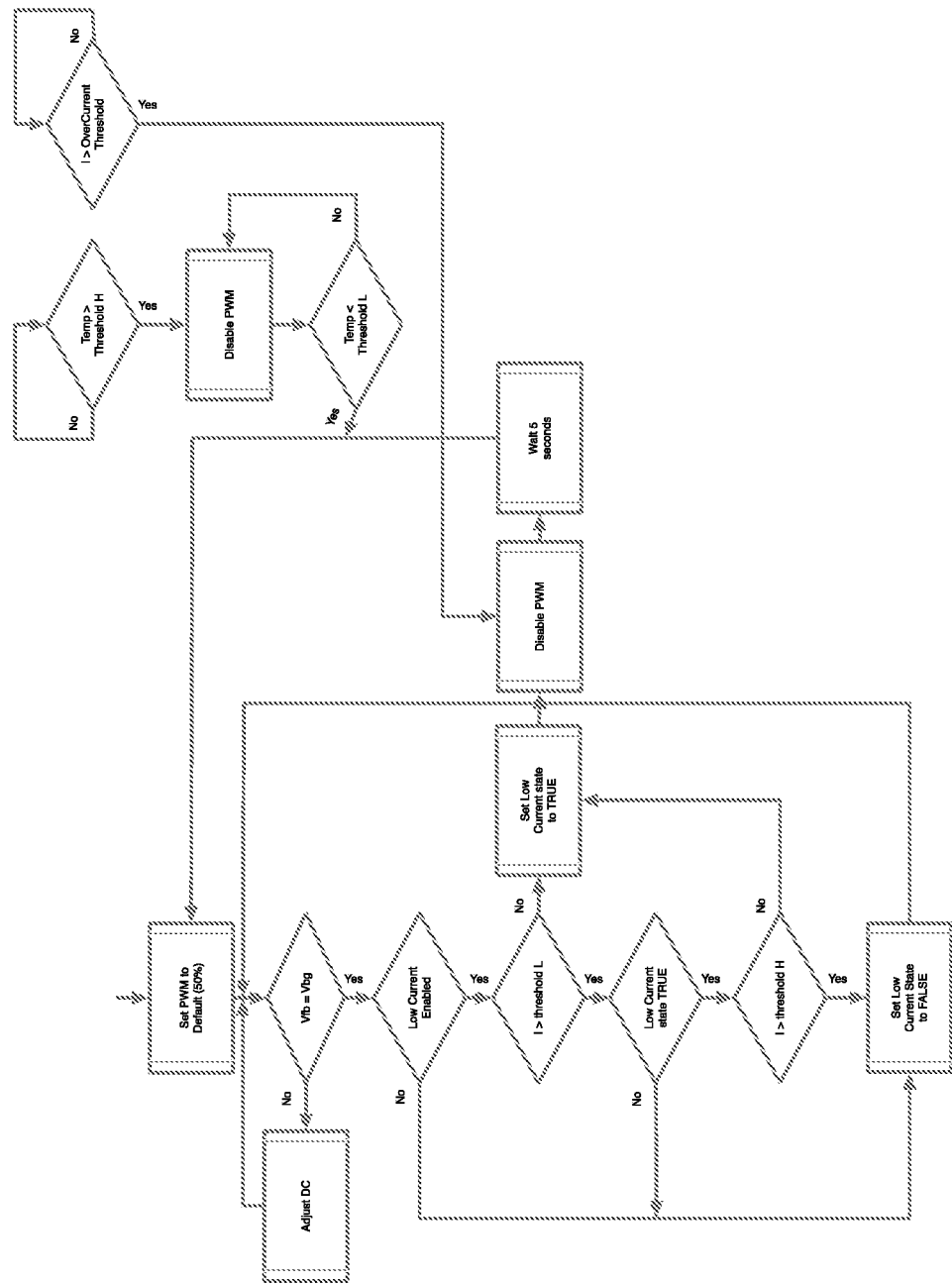


Figure 33

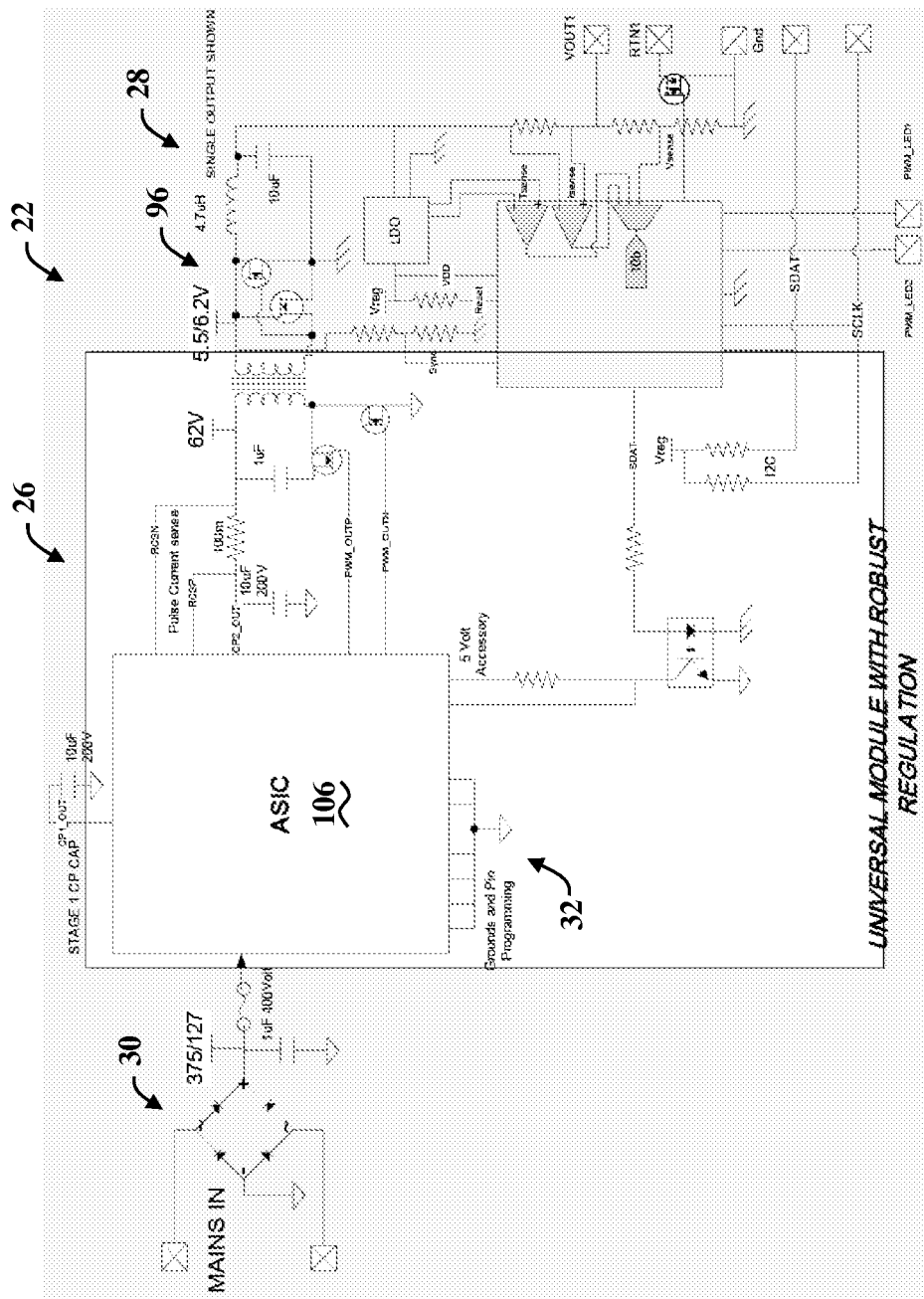


Figure 34

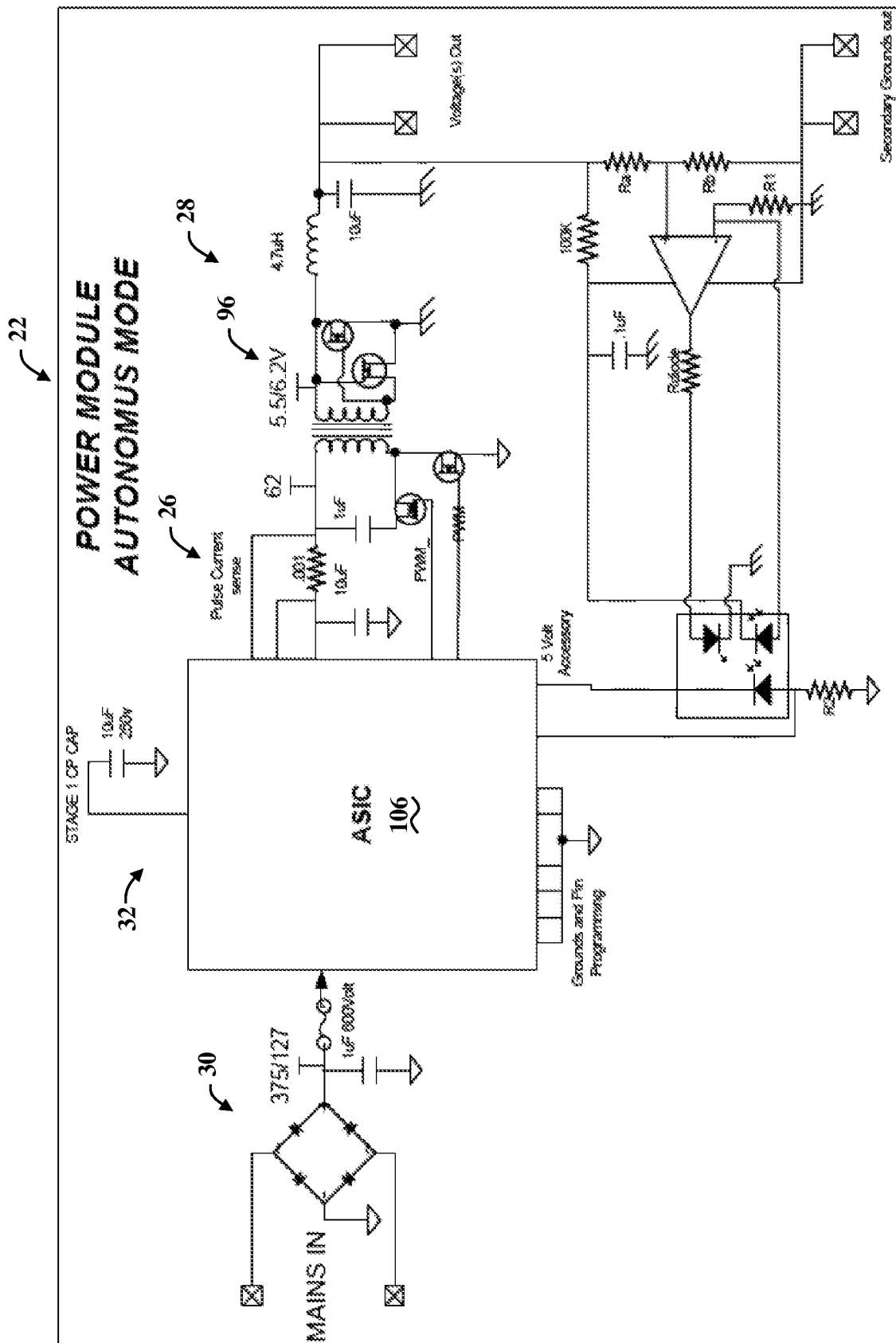


Figure 35

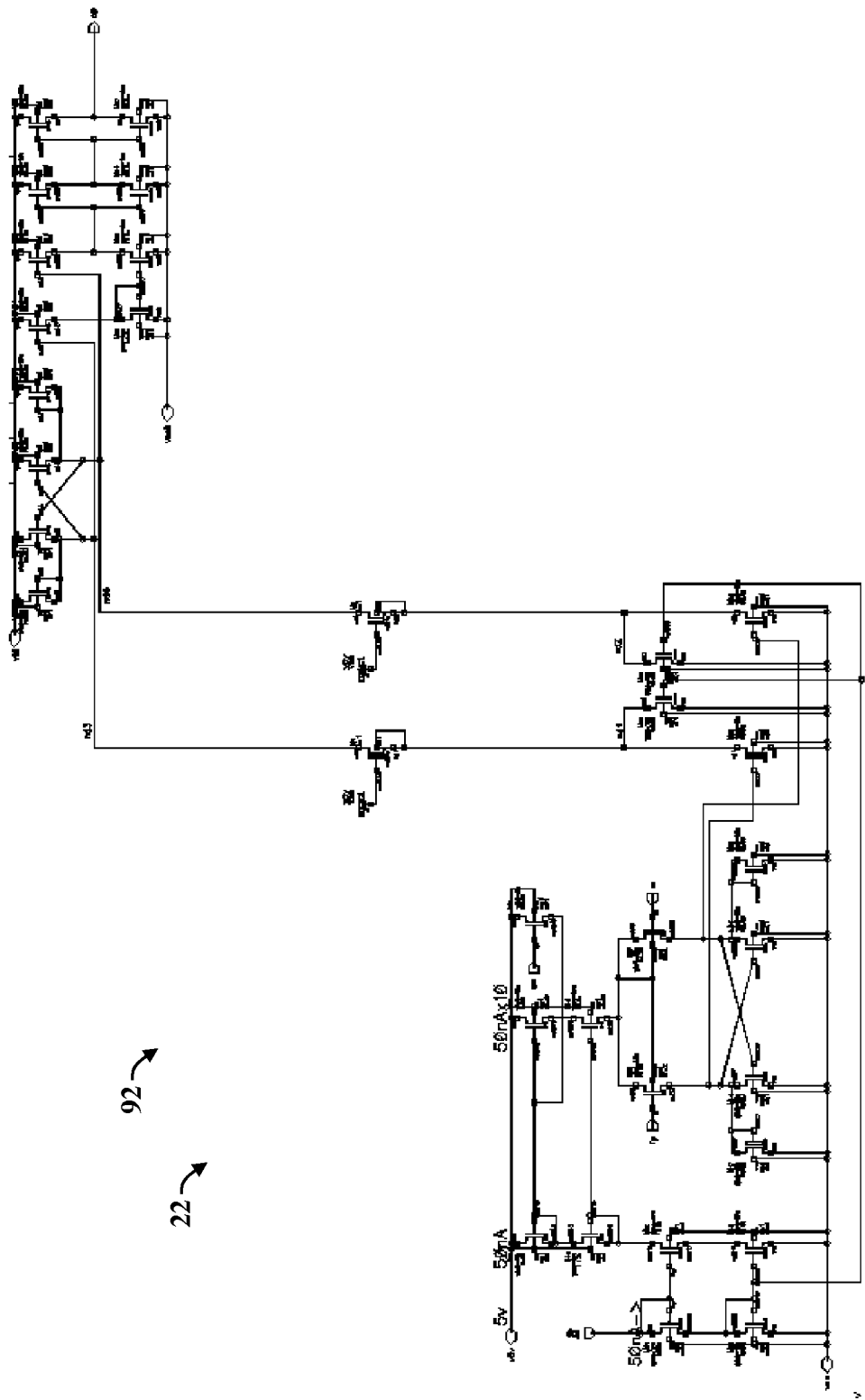


Figure 36

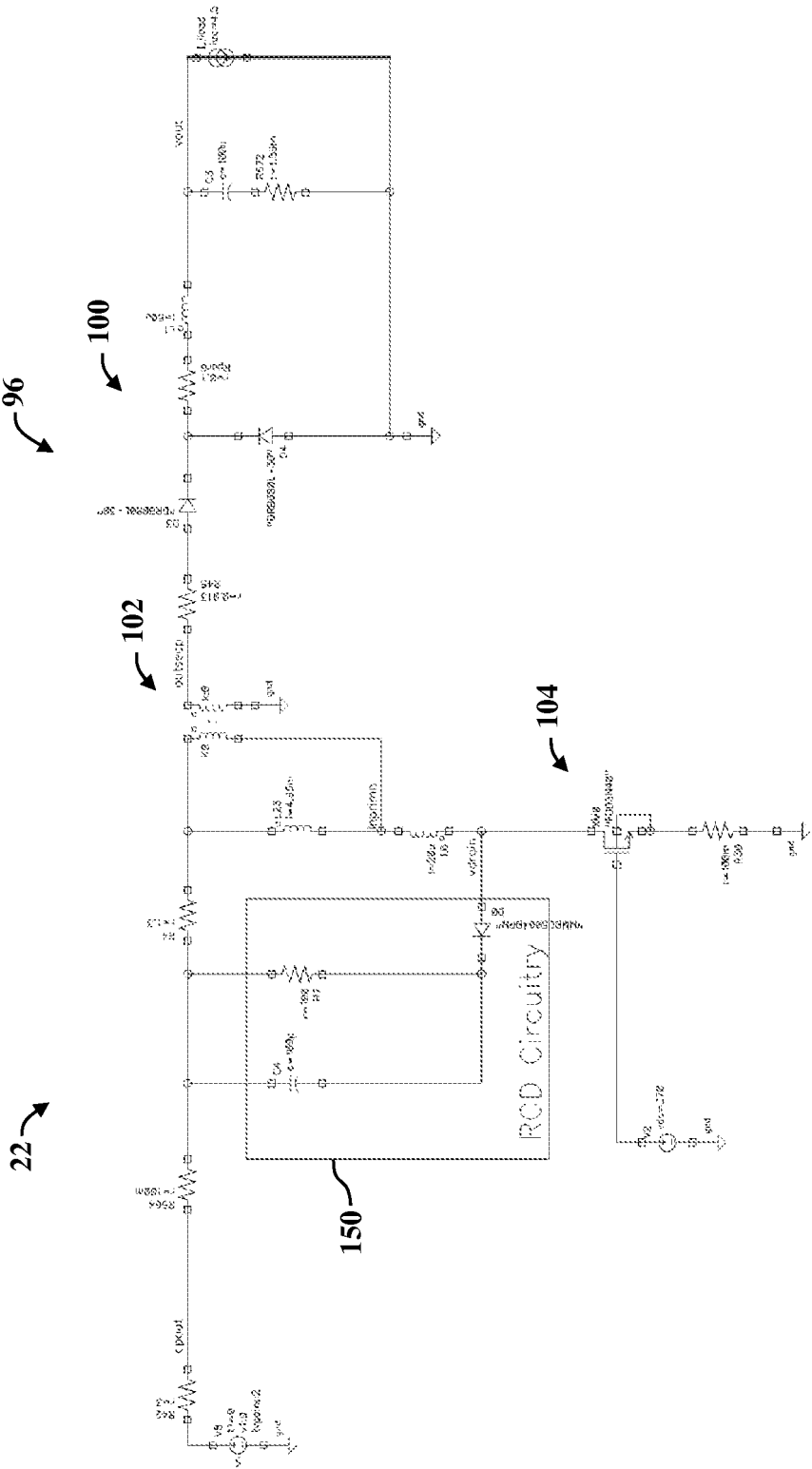


Figure 37

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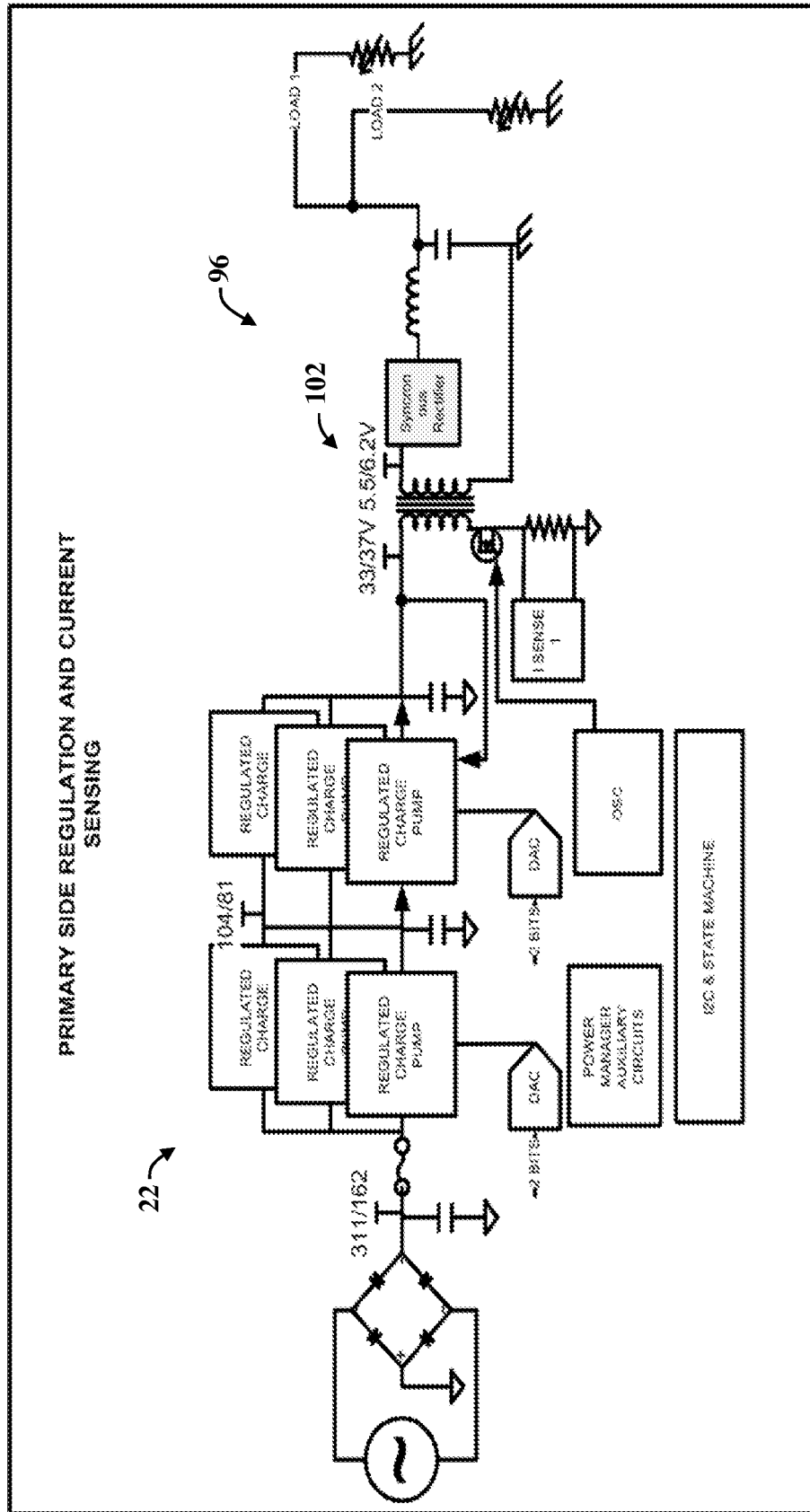


Figure 38

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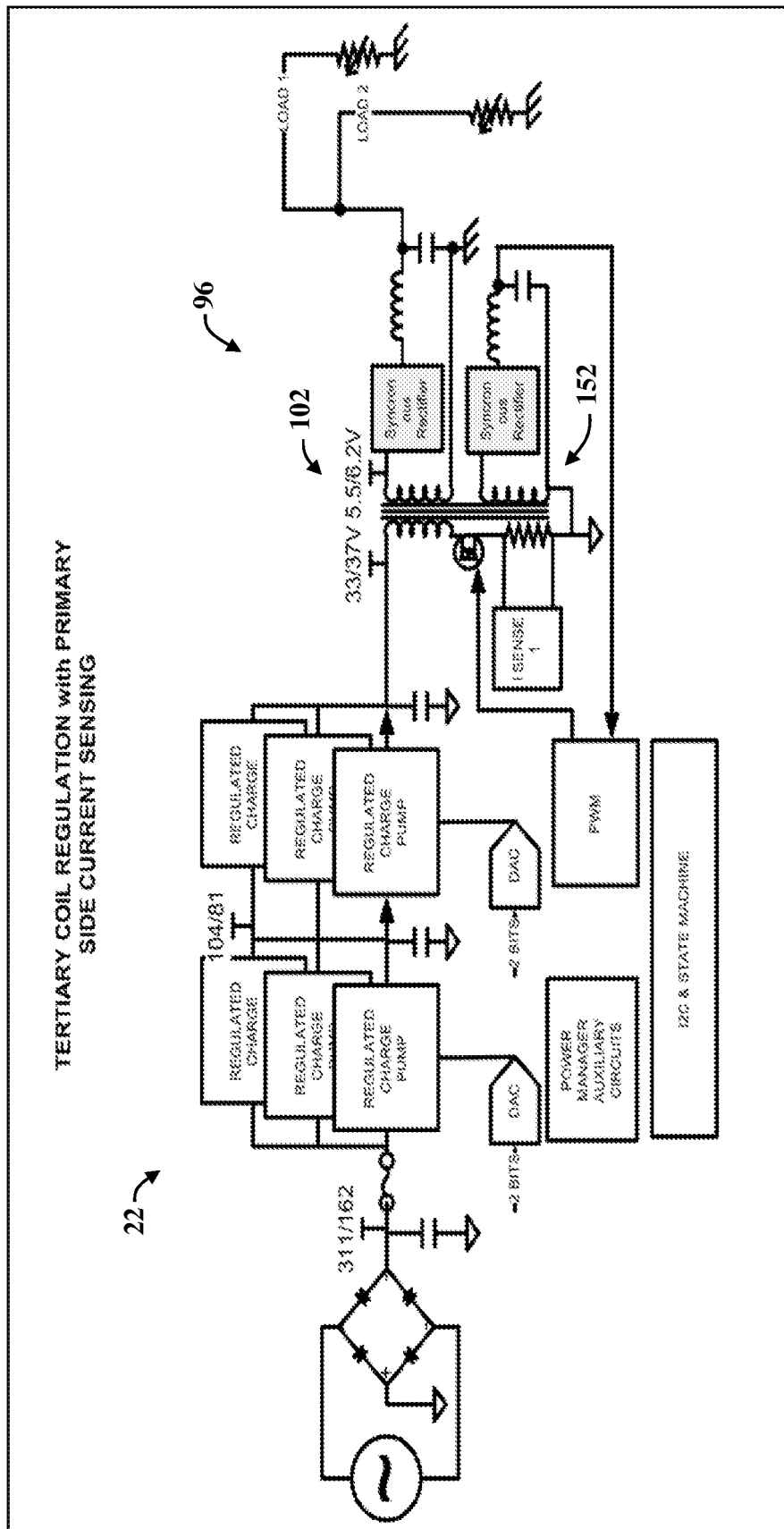


Figure 39

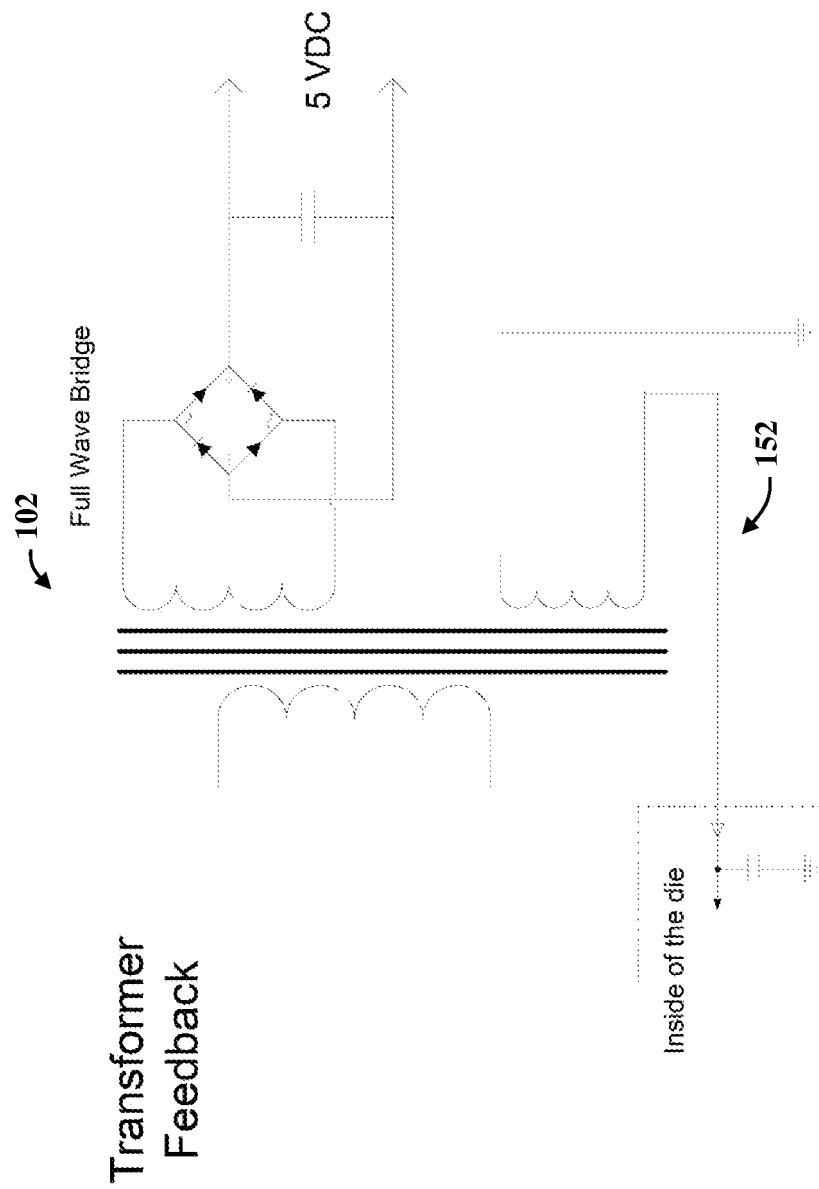


Figure 40

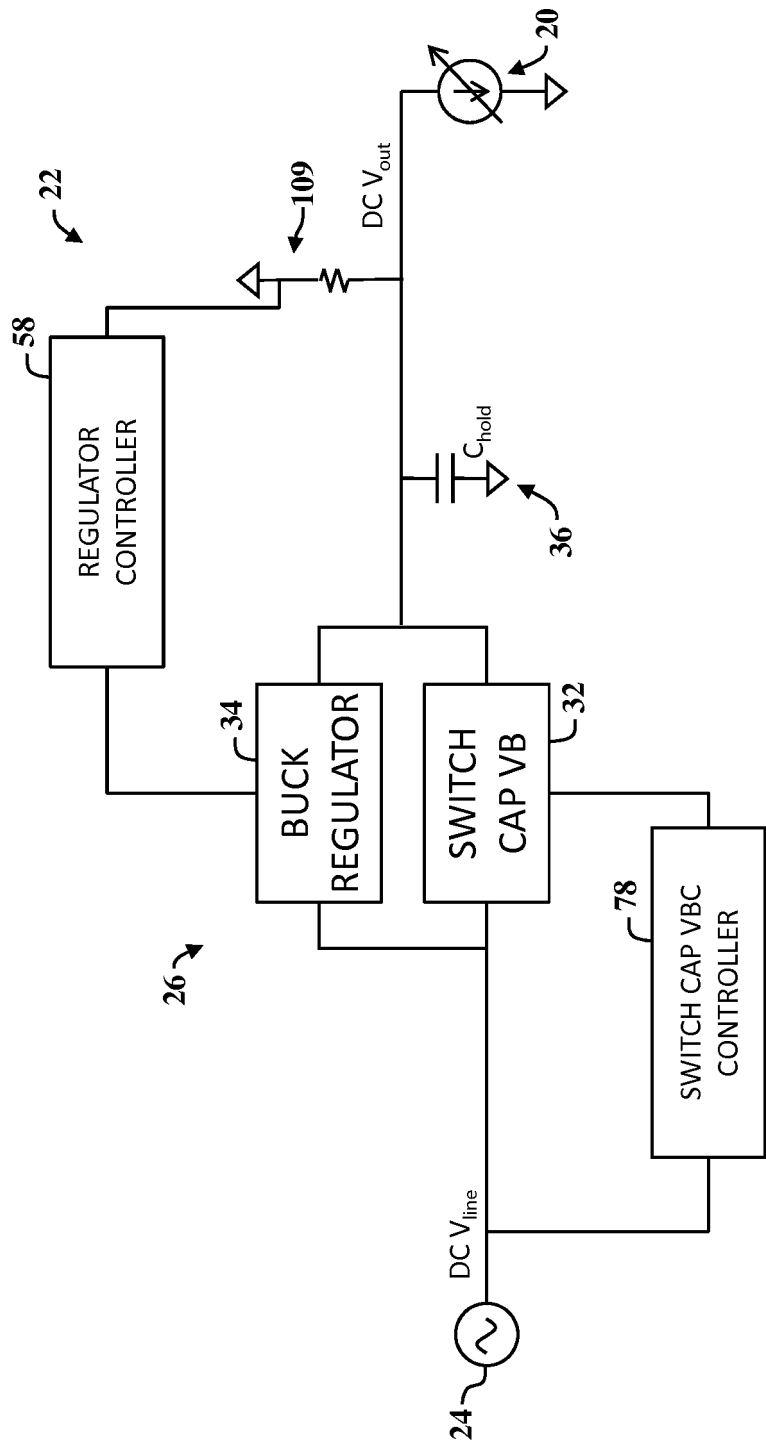


Figure 41