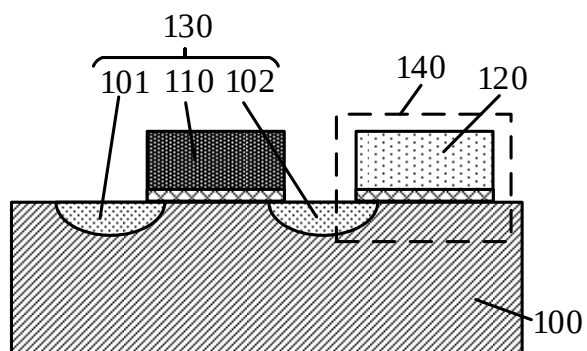
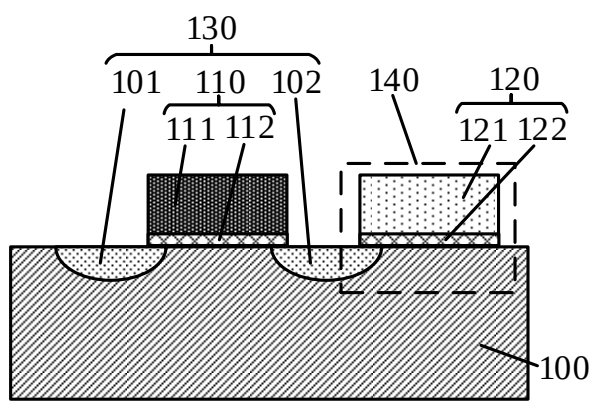




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FIG. 1



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FIG. 2

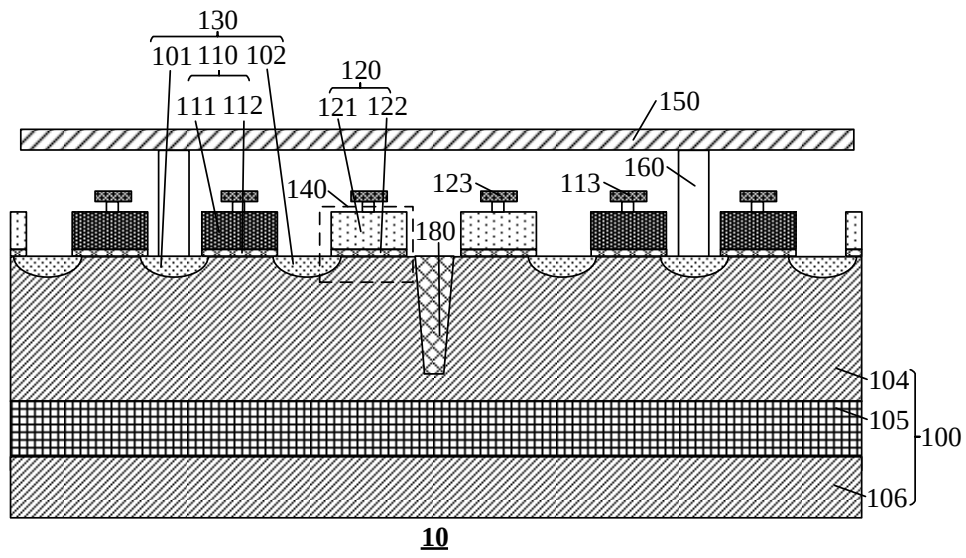


FIG. 3

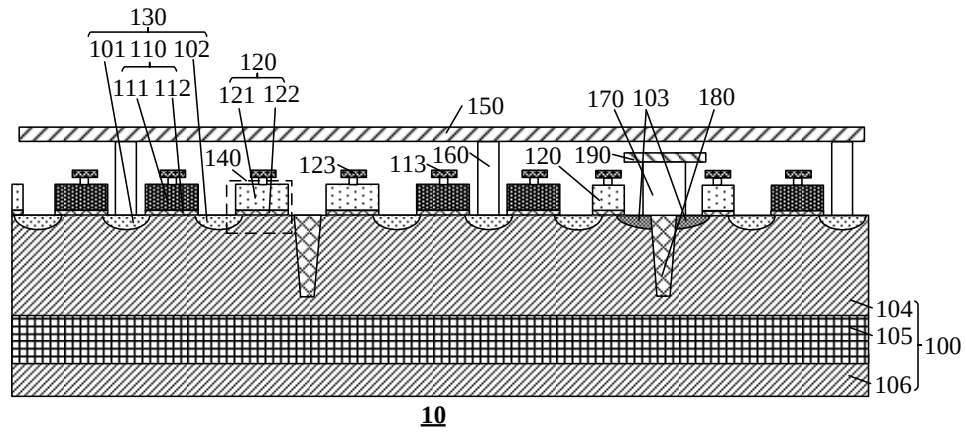


FIG. 4

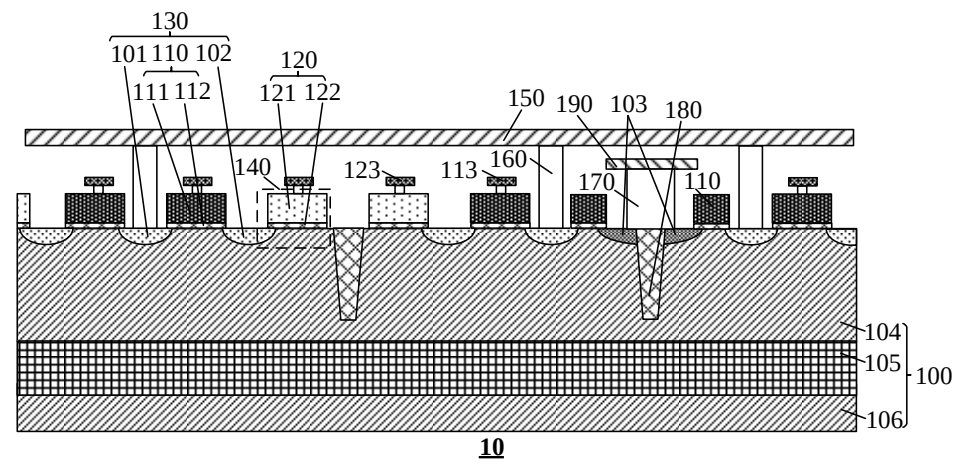


FIG. 5

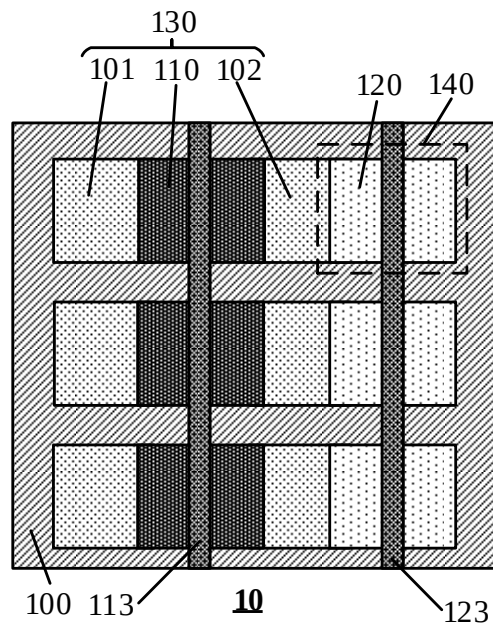


FIG. 6

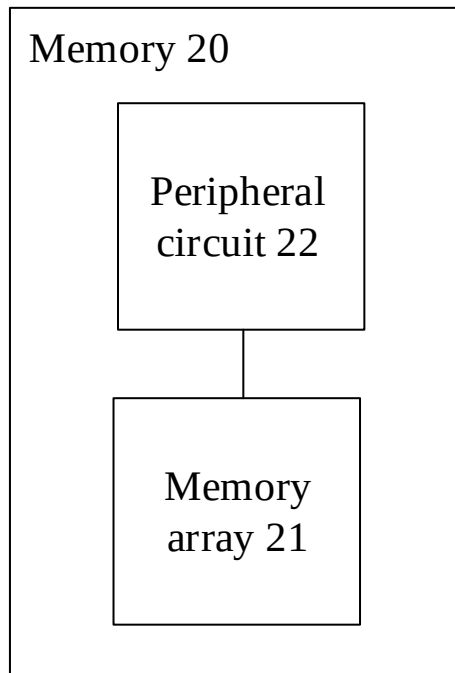
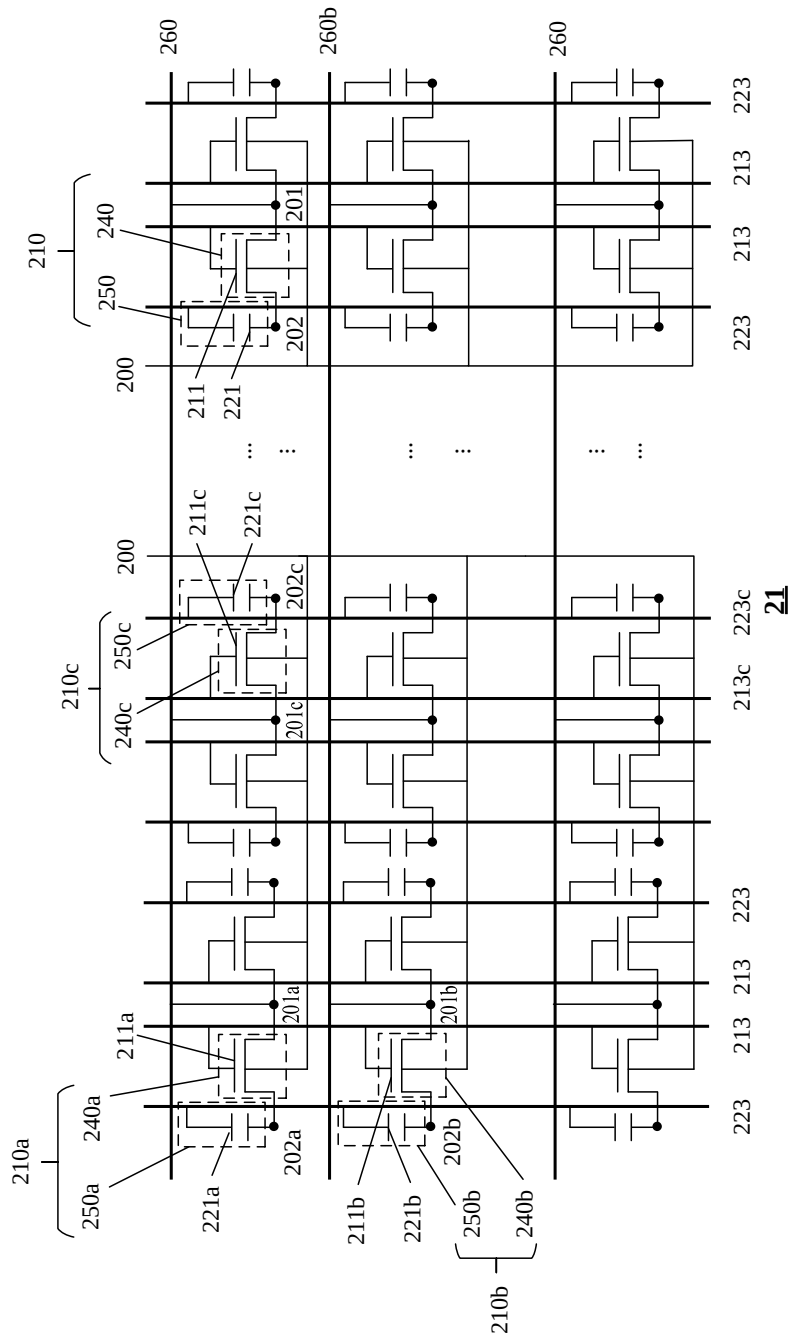


FIG. 7



According to data to be written in, a target antifuse bit structure among antifuse bit structures of the memory is broken down, and a non-target antifuse bit structure is kept in a non-breakdown state

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