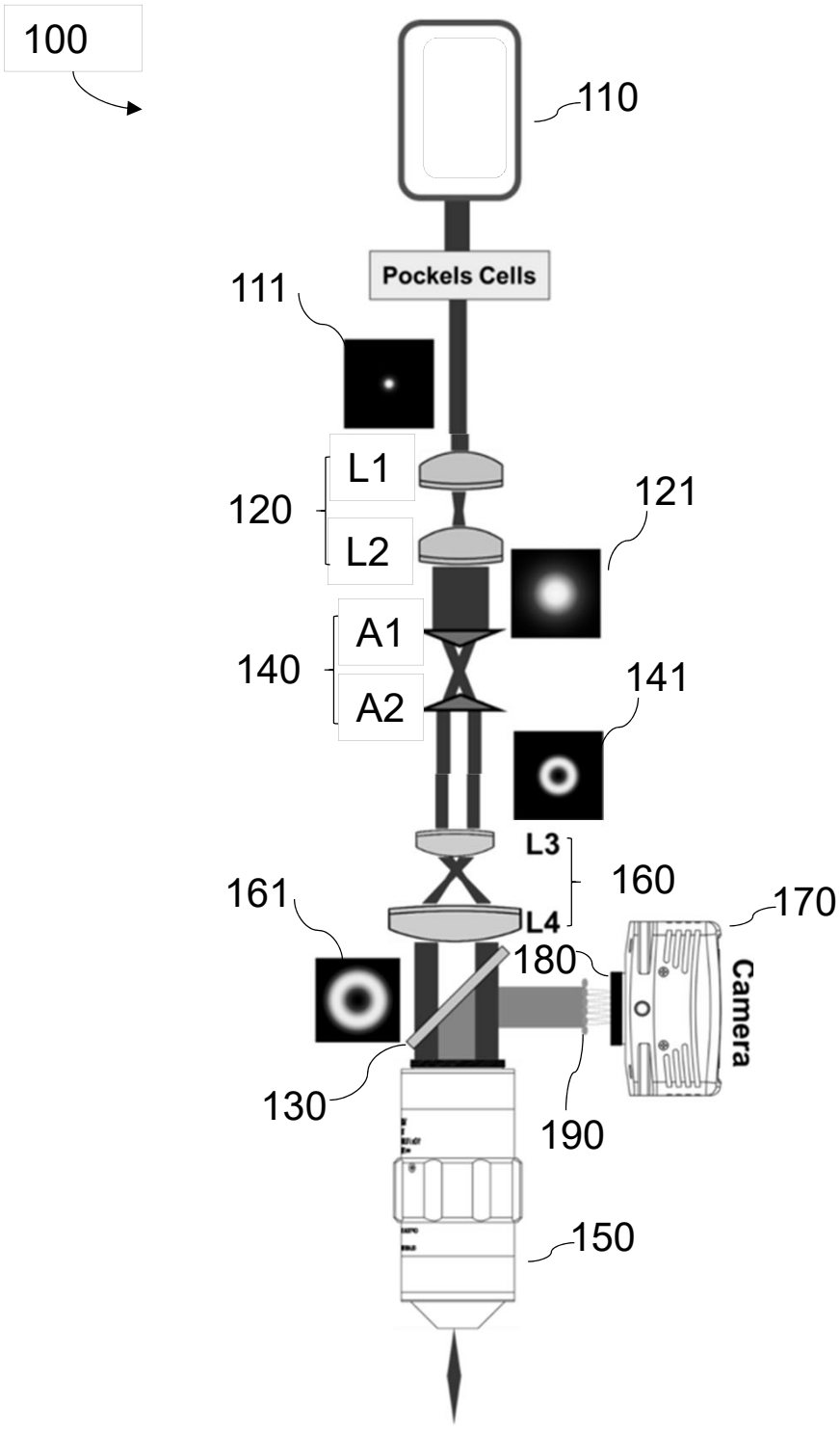

100
110
Pockels Cells
111
L1
120
L2
121
A1
140
A2
141
L3
160
161
L4
170
180
Camera
130
190
150

FIG. 1A

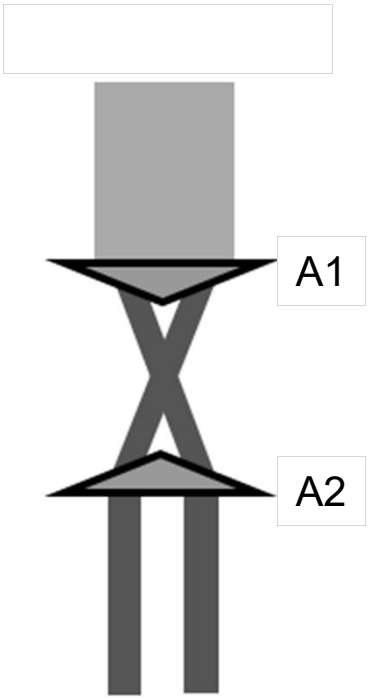


FIG. 1B

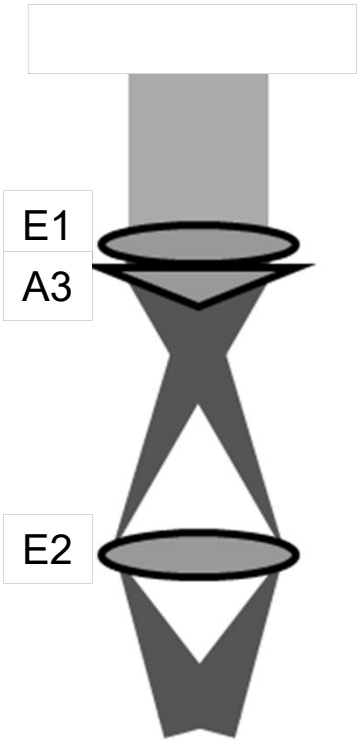


FIG. 1C

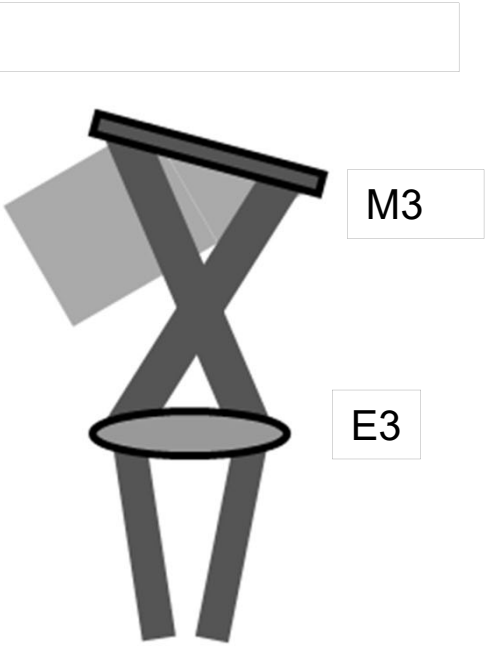


FIG. 1D

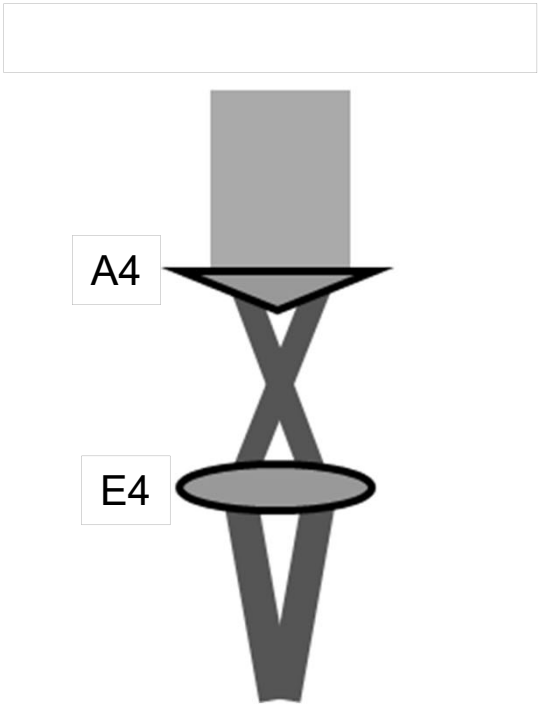


FIG. 1E

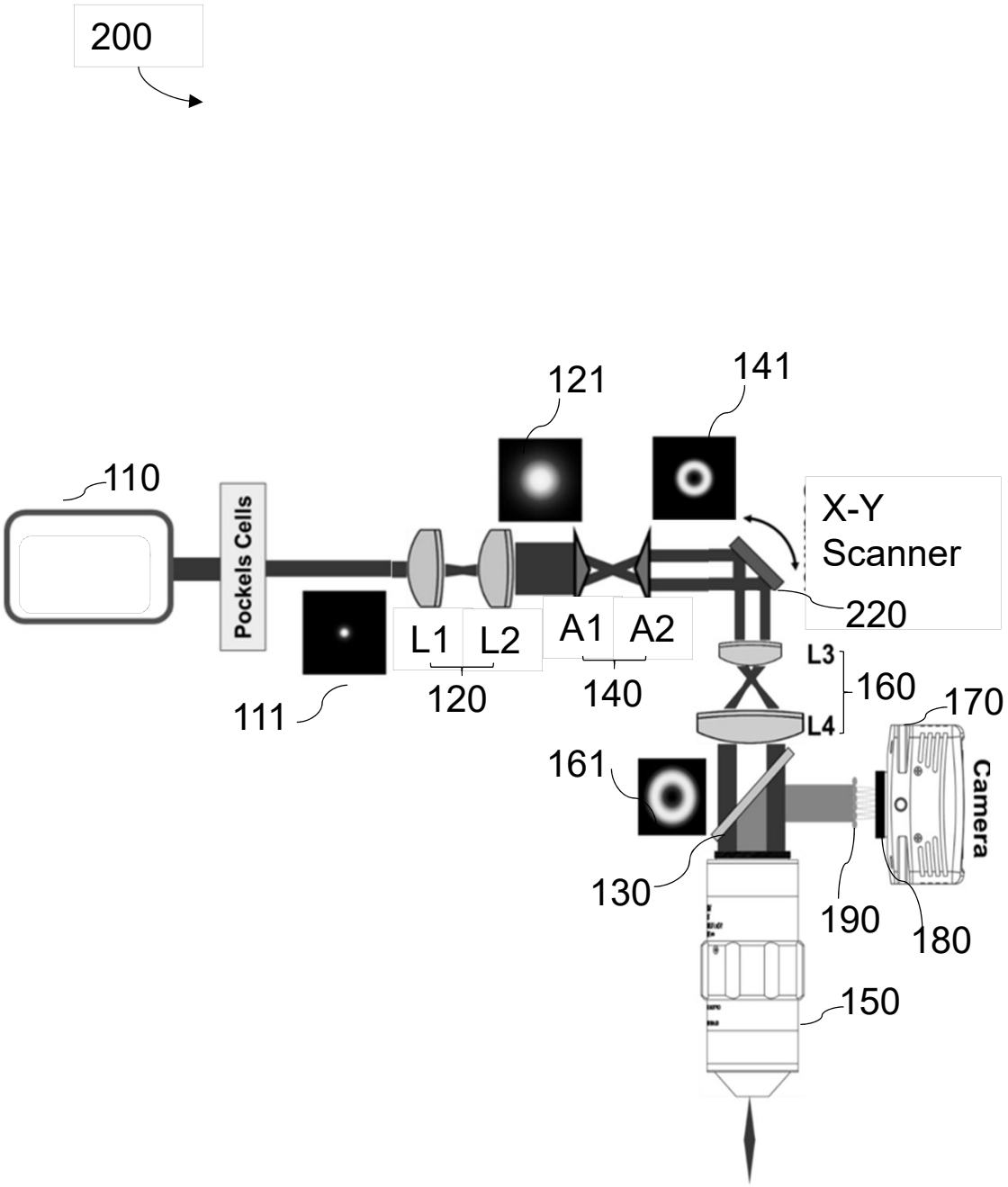

200
110
Pockels Cells
121
141
X-Y
Scanner
220
111
L1
L2
120
A1
A2
140
L3
L4
160
170
161
Camera
130
190
180
150

FIG. 2

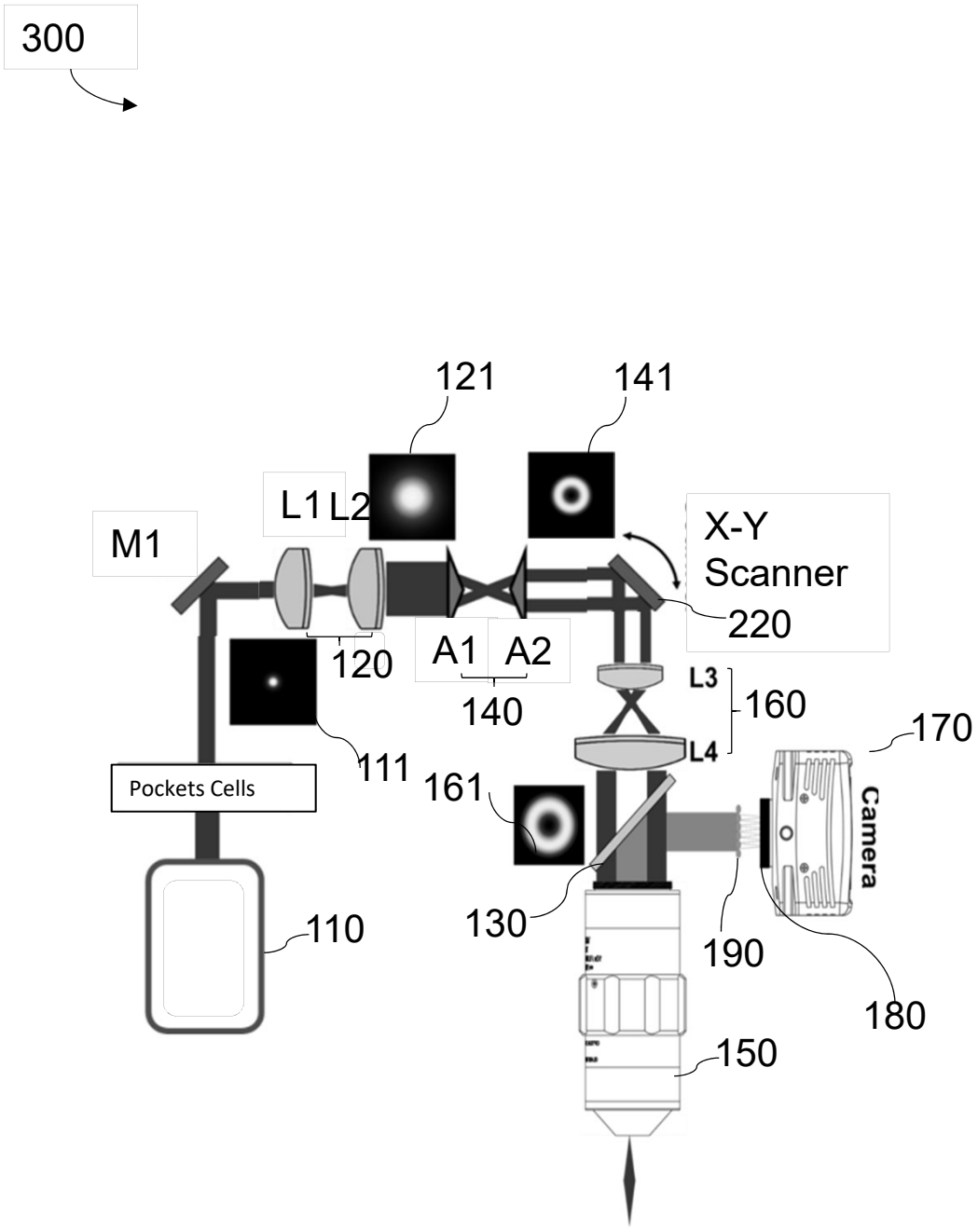

300
121
141
L1
L2
M1
X-Y
Scanner
220
120
A1
A2
140
L3
160
L4
170
111
Pockets Cells
161
Camera
110
130
190
180
150

FIG. 3

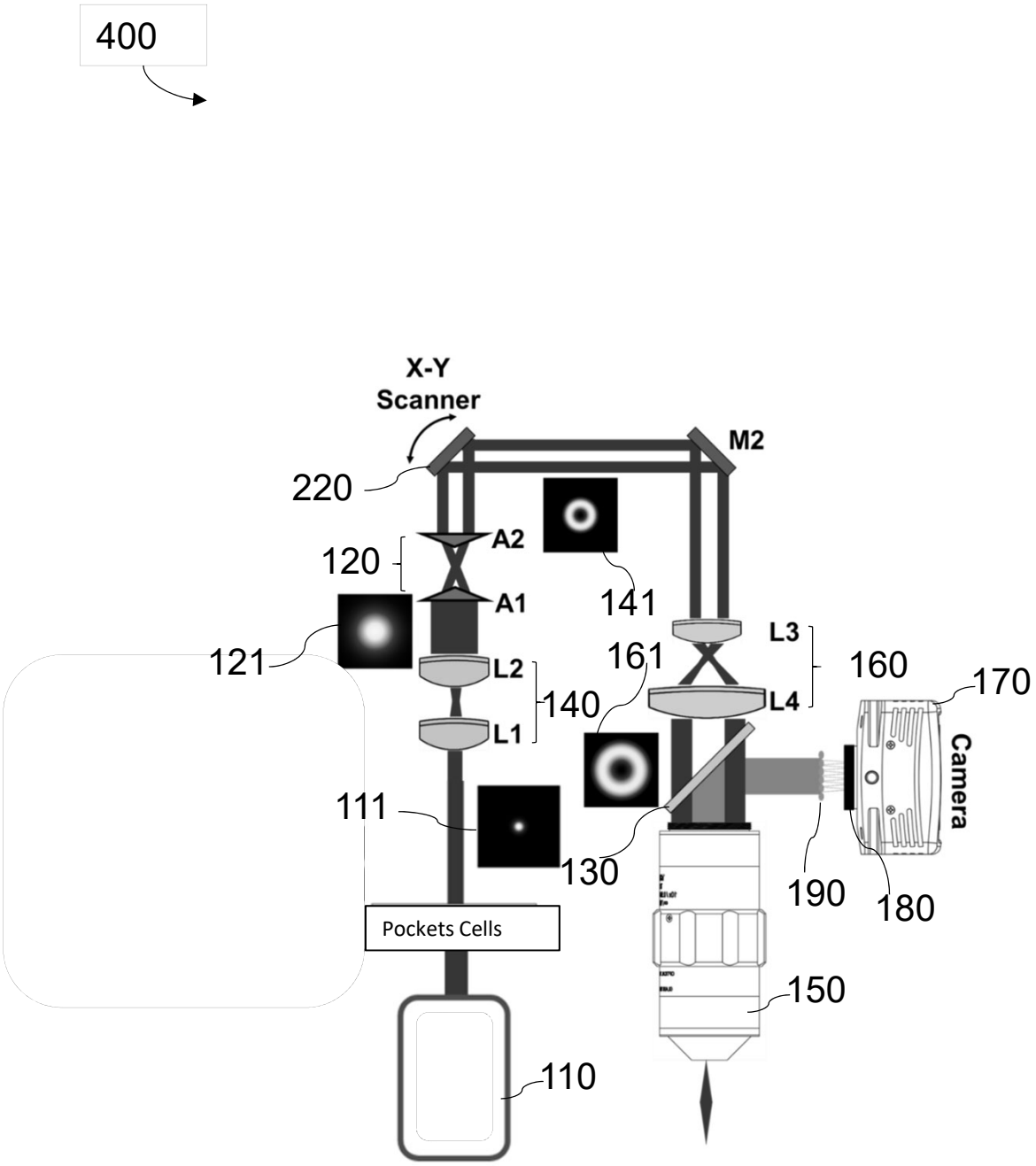

400
X-Y
Scanner
220
M2
120
A2
A1
141
121
L2
L1
140
161
L3
L4
160
170
Camera
111
130
190
180
Pockets Cells
150
110

FIG. 4

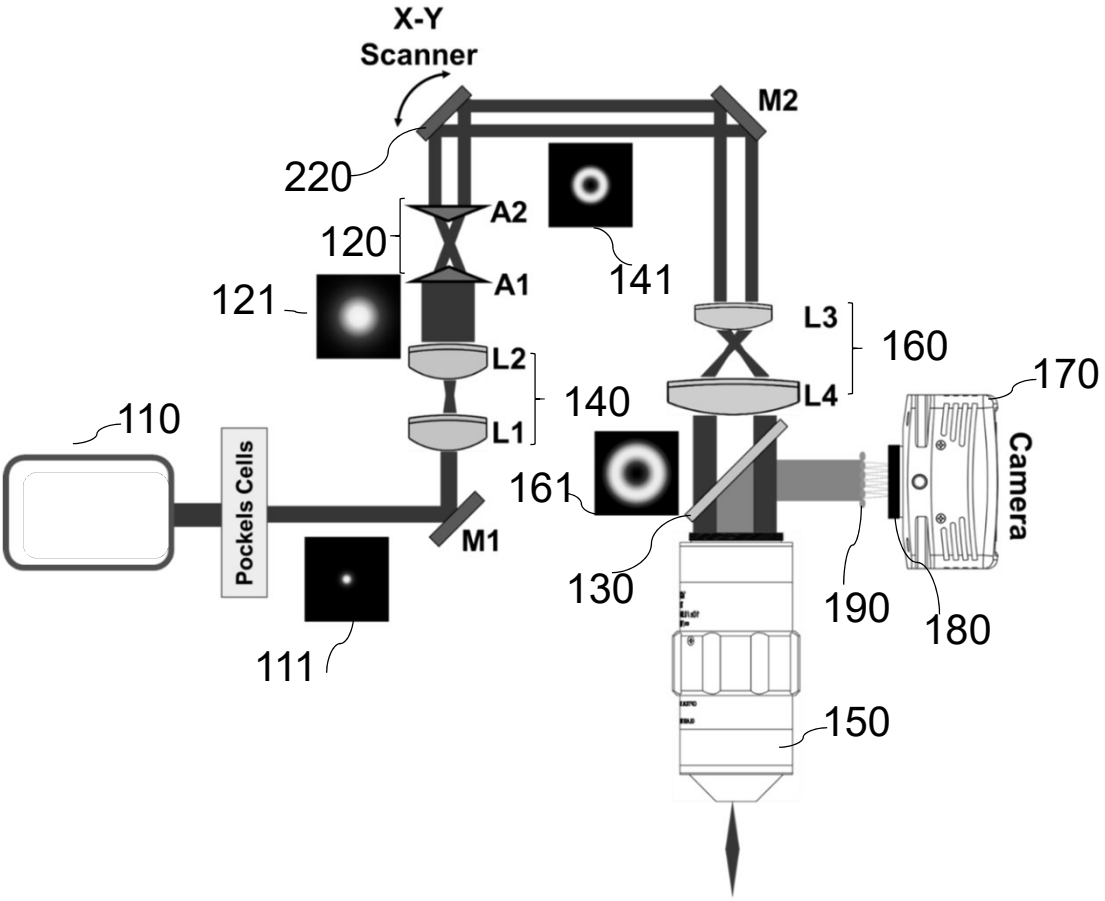

500
X-Y
Scanner
M2
220
A2
120
A1
121
141
L3
160
L2
140
L4
170
110
L1
Pockels Cells
161
Camera
M1
130
190
180
111
150

FIG. 5

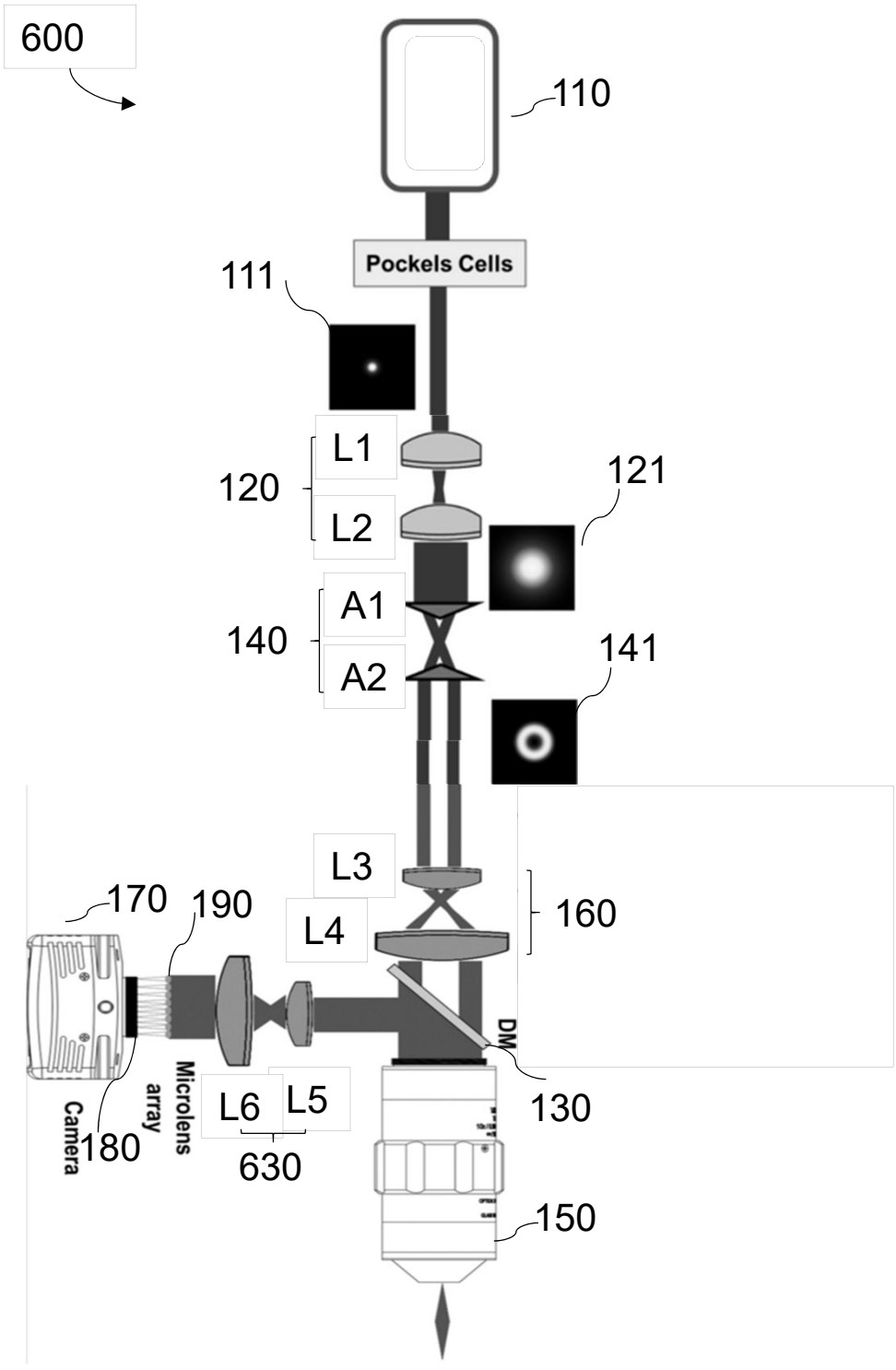

600
110
Pockels Cells
111
L1
120
L2
121
A1
140
A2
141
L3
160
170
190
L4
DM
130
Camera
180
Microlens array
L6
L5
630
150

FIG. 6

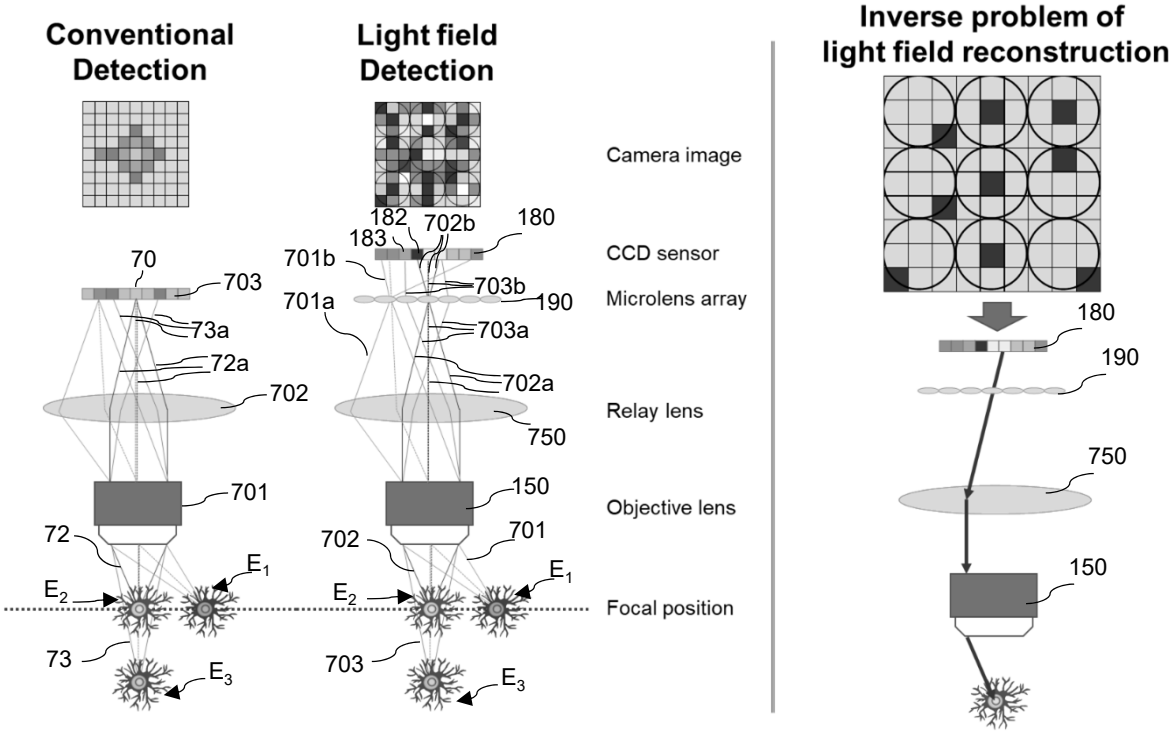


FIG. 7A (Prior Art)

FIG. 7B

FIG. 7C

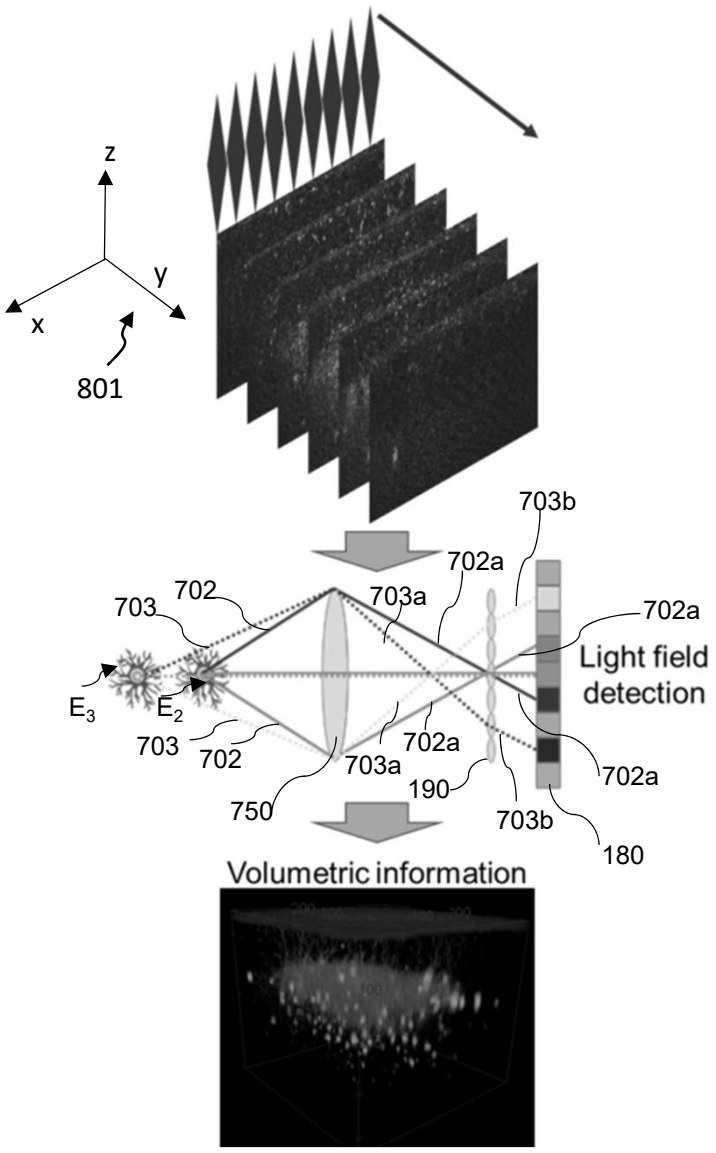

z
y
x
801
703b
702a
703
702
703a
702a
Light field
detection
E_3
E_2
703
702
702a
703a
190
702a
750
703b
180
Volumetric information

FIG. 8

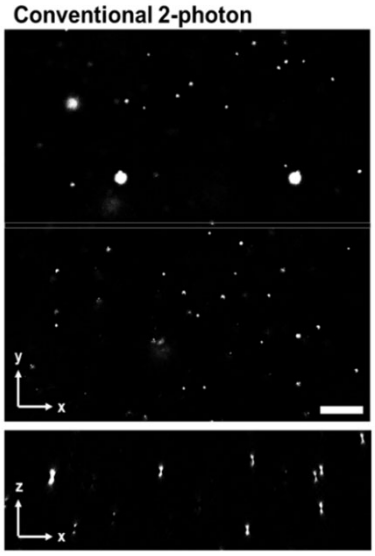


FIG. 9A
(Prior Art)

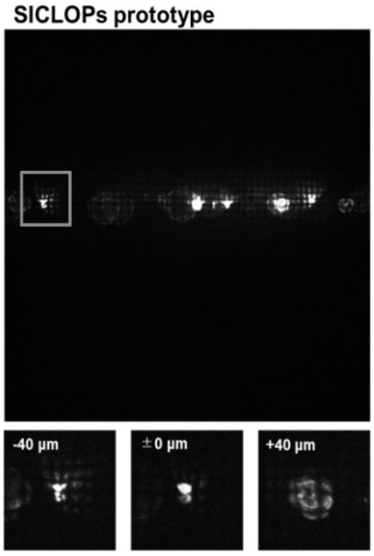


FIG. 9B



FIG. 9C

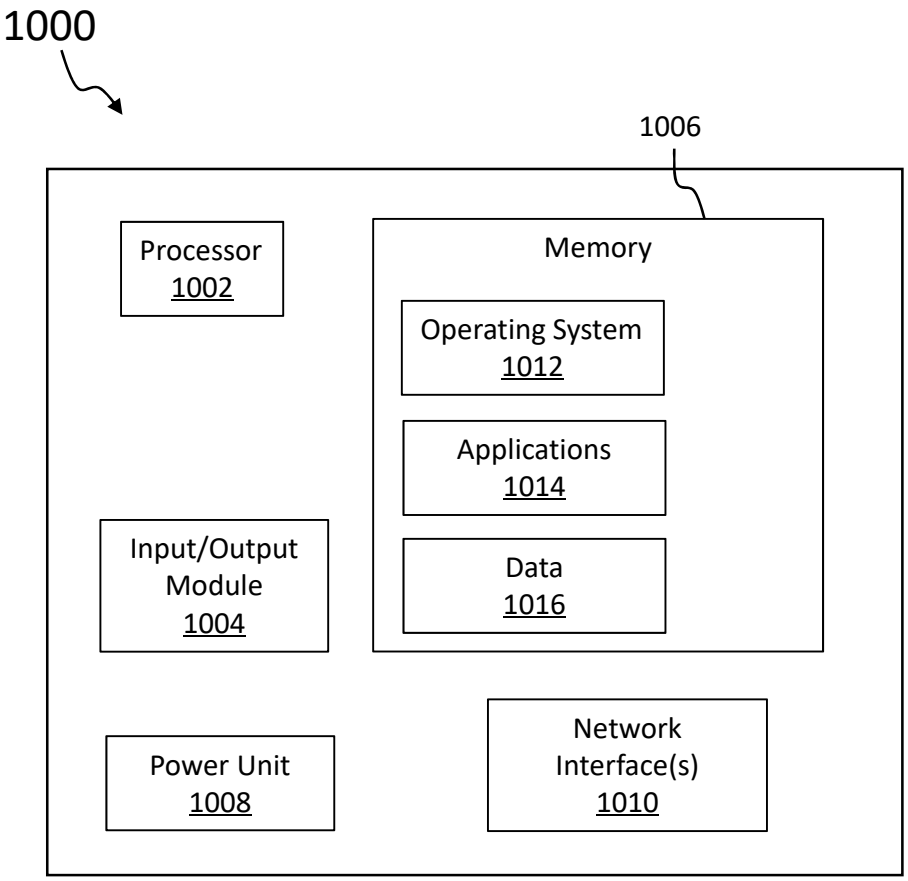

1000
1006
Processor
1002
Memory
Operating System
1012
Applications
1014
Data
1016
Input/Output
Module
1004
Power Unit
1008
Network
Interface(s)
1010

FIG. 10