PCT WORLD INTELLECTUAL PROPERTY ORGANIZATION
International Bureau

INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(51) **International Patent Classification** 6 :
H01L 23/64, 27/02

A1

(11) **International Publication Number:** **WO 96/28847**

(43) **International Publication Date:** 19 September 1996 (19.09.96)

(21) **International Application Number:** PCT/IB96/00112

(22) **International Filing Date:** 15 February 1996 (15.02.96)

(30) **Priority Data:**
95200600.5 13 March 1995 (13.03.95) EP

(34) Countries for which the regional or international application was filed: NL et al.

(71) **Applicant:** PHILIPS ELECTRONICS N.V. [NL/NL]; Groenewoudseweg 1, NL-5621 BA Eindhoven (NL).

(71) **Applicant** *(for SE only)*: PHILIPS NORDEN AB [SE/SE]; Kottbygatan 5, Kista, S-164 85 Stockholm (SE).

(72) **Inventors:** HURKX, Godefridus, Adrianus, Maria; Groenewoudseweg 1, NL-5621 BA Eindhoven (NL). BALTUS, Petrus, Gerardus, Maria; Groenewoudseweg 1, NL-5621 BA Eindhoven (NL). KNUVERS, Marinus, Petrus, Gerardus; Groenewoudseweg 1, NL-5621 BA Eindhoven (NL). HART, Cornelis, Maria; Groenewoudseweg 1, NL-5621 BA Eindhoven (NL).

(74) **Agent:** SCHOONHEIJM, Harry, B.; Internationaal Octrooibureau B.V., P.O. Box 220, NL-5600 AE Eindhoven (NL).

(81) **Designated States:** JP, KR, SG, European patent (AT, BE, CH, DE, DK, ES, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE).

Published
With international search report.
Before the expiration of the time limit for amending the claims and to be republished in the event of the receipt of amendments.

(54) **Title:** ELECTRONIC DEVICE COMPRISING MEANS FOR COMPENSATING AN UNDESIRED CAPACITANCE

(57) **Abstract**

A device comprising a first (20) and a second (21) node, between said nodes an undesired capacitance (9) being present, is equipped with a diode (11), being driven in breakthrough, said diode (11) being coupled between the first (20) and the second (21) node. As the diode (11), driven in breakthrough, exhibits the behaviour of a negative capacitance, a compensation of the undesired capacitance (9) is achieved.