



LOW-TO-HIGH VOLTAGE CMOS DRIVER CIRCUIT FOR
DRIVING CAPACITIVE LOADS

ABSTRACT

5 A high speed, low-to-high voltage CMOS driver circuit
has a CMOS output stage, an intermediate voltage
translation stage, and an input stage. The input and
intermediate stages are designed to generate mutually
exclusive control signals which activate the PMOS and NMOS
10 transistors of the output stage. The control signals
operably turn "off" the active transistor before turning
"on" the inactive transistor. Independent control signals
substantially reduce or eliminate crossing current in the
output stage to thereby reduce energy power waste.

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