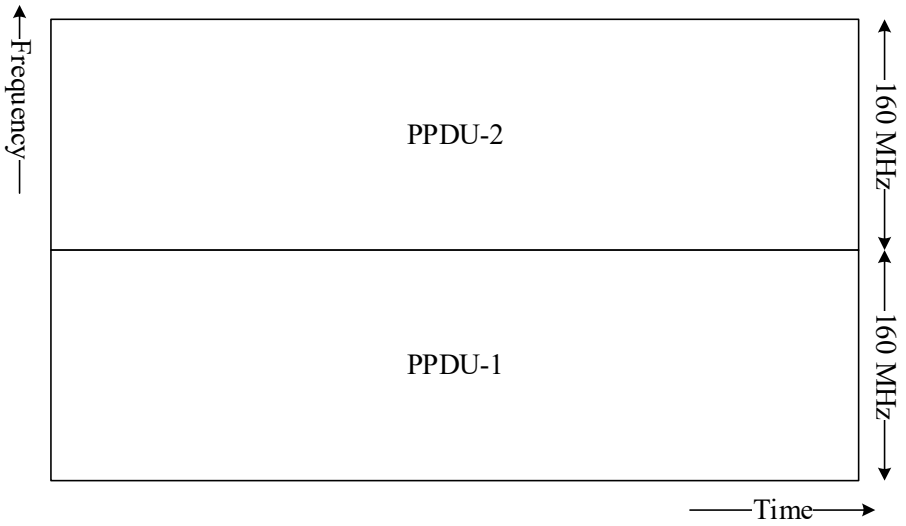


FIG. 1

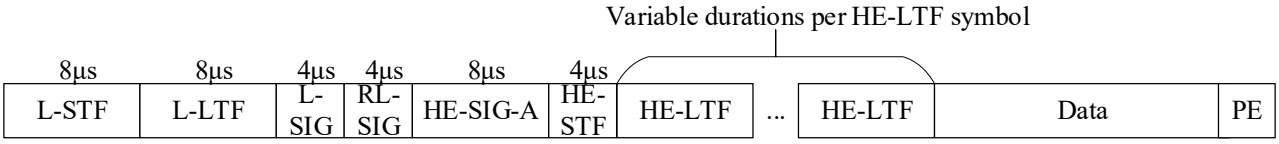


FIG. 2A

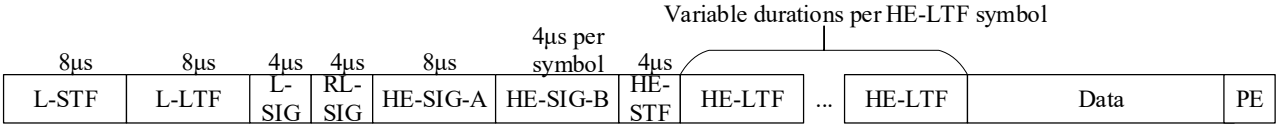


FIG. 2B

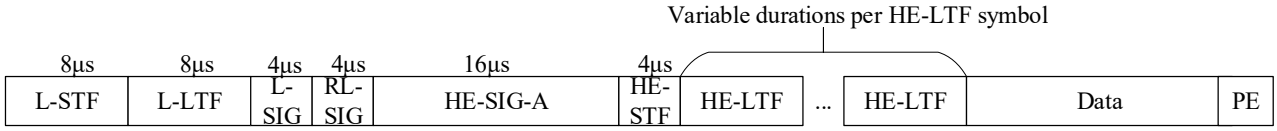


FIG. 2C

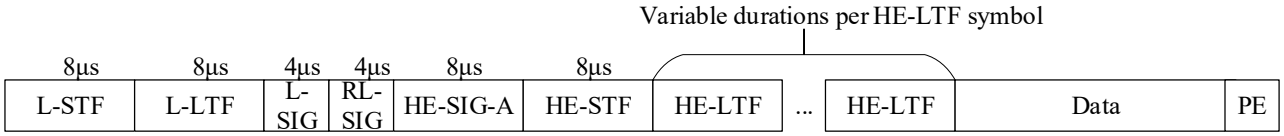


FIG. 2D

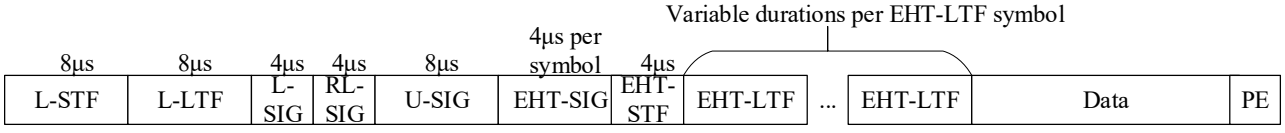


FIG. 3A

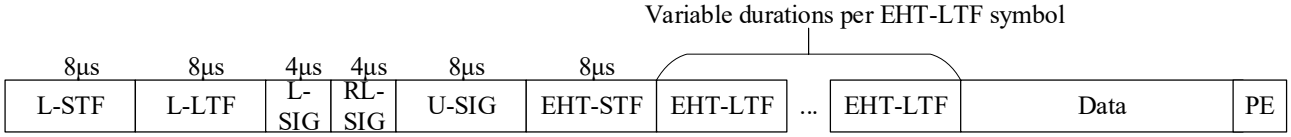


FIG. 3B

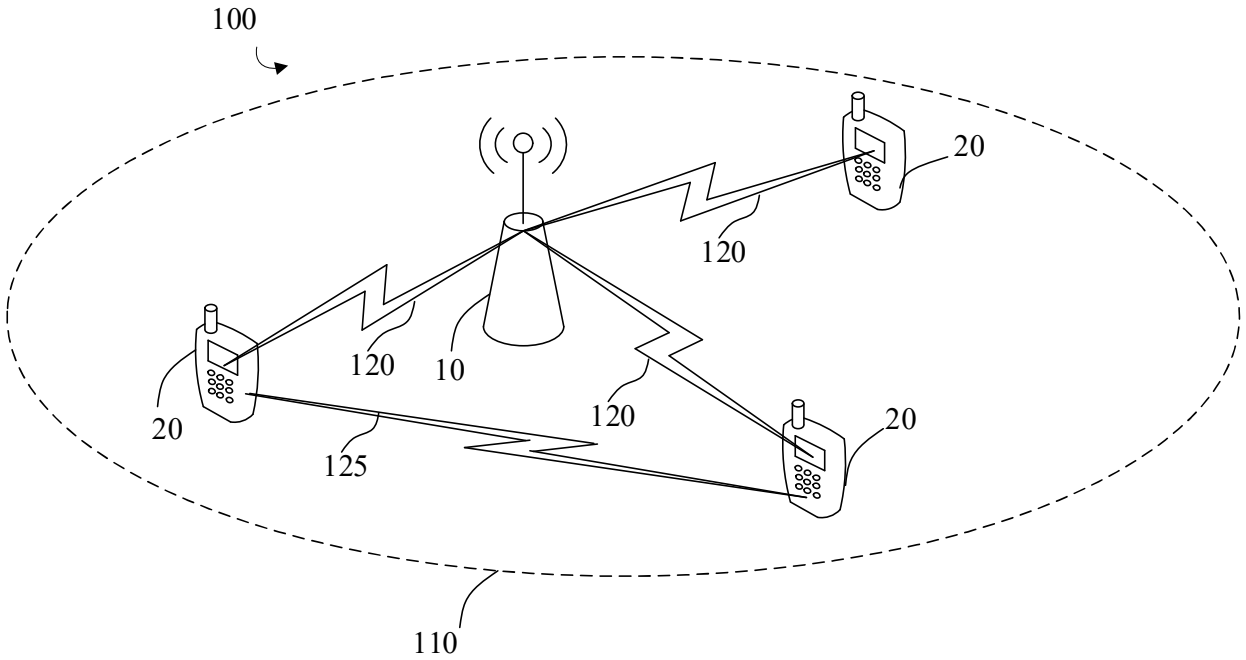

100
20
120
120
10
120
20
20
125
110

FIG. 4

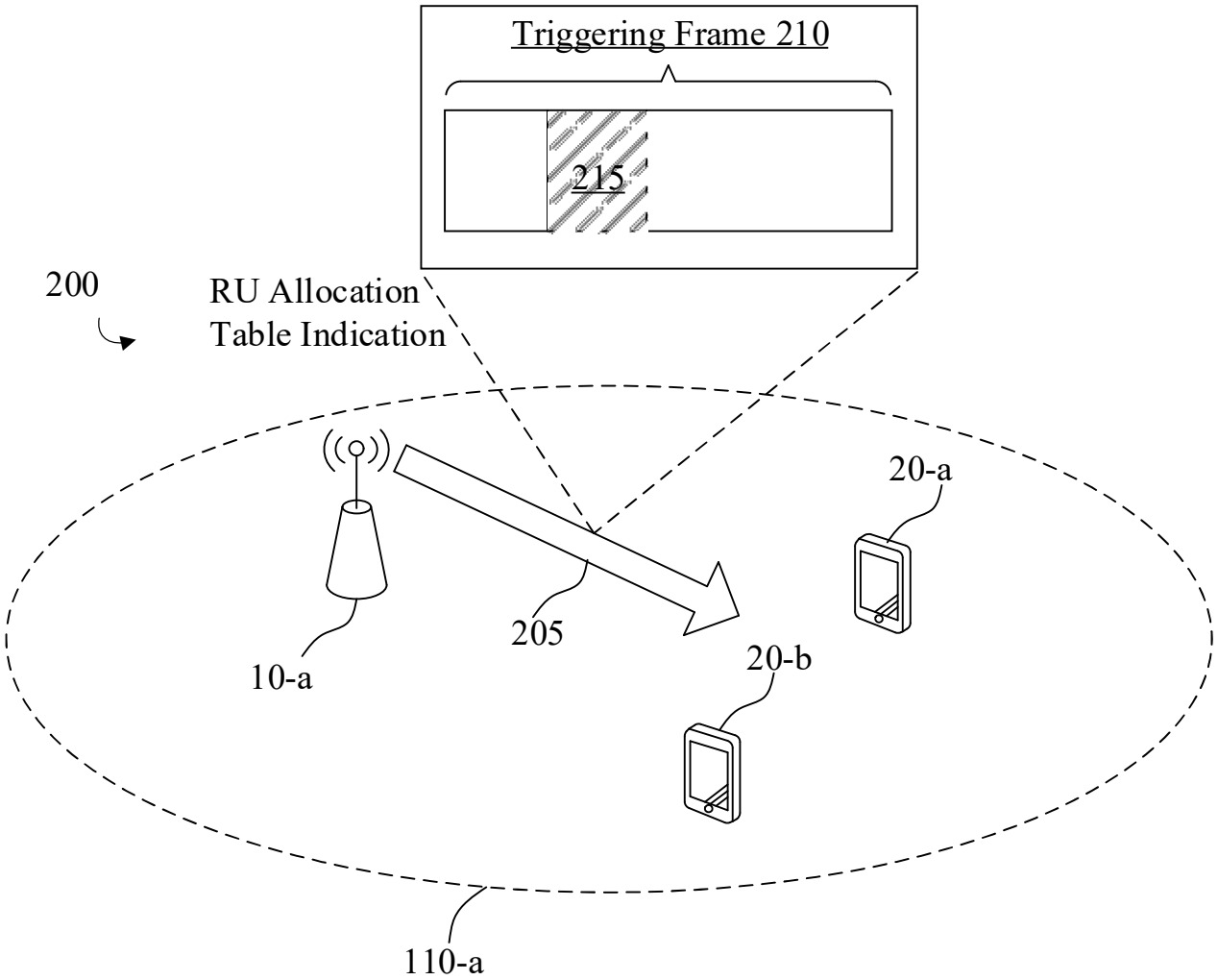

Triggering Frame 210
215
200
RU Allocation
Table Indication
20-a
205
10-a
20-b
110-a

FIG. 5

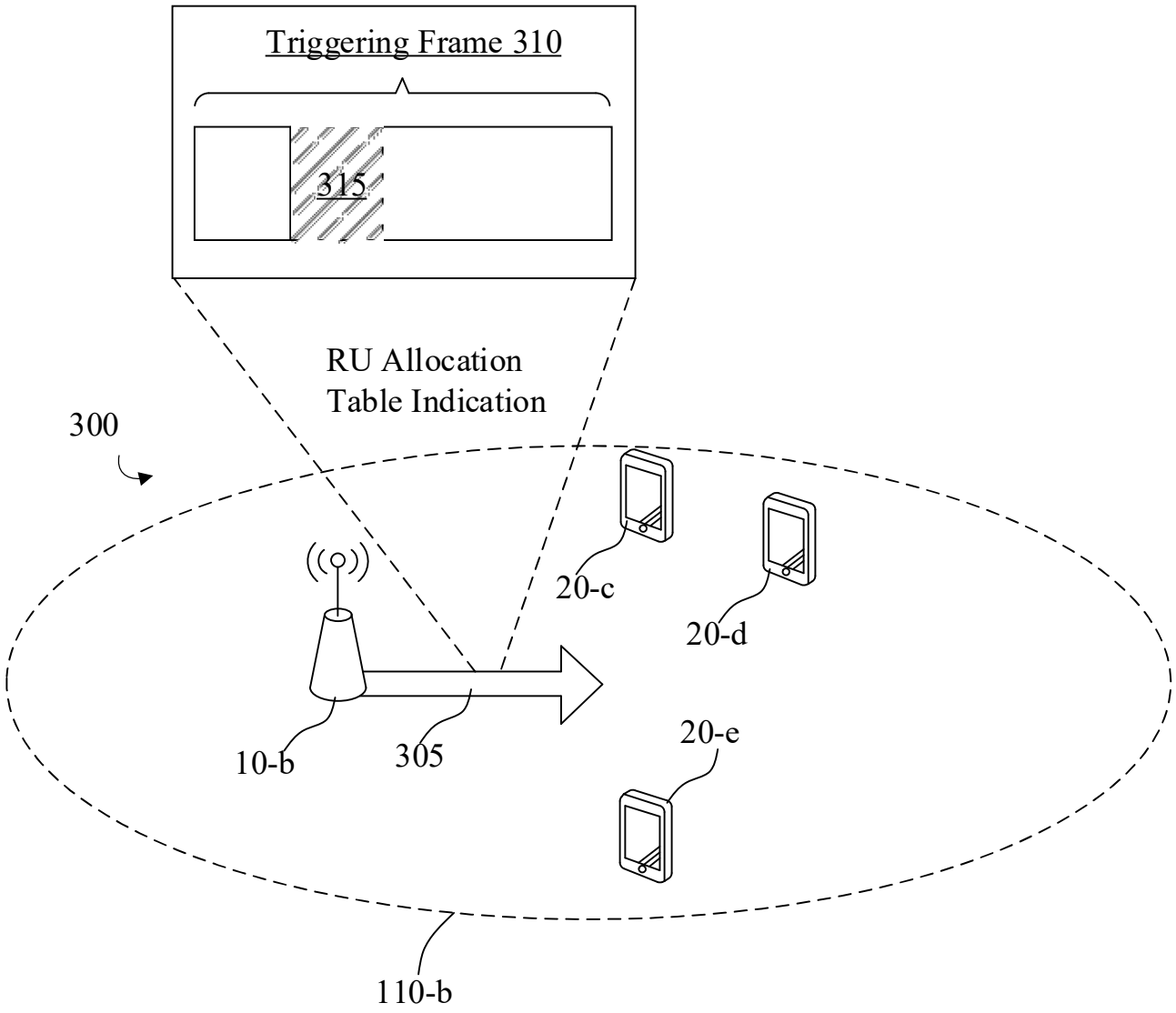

Triggering Frame 310
315
RU Allocation
Table Indication
300
20-c
20-d
20-e
10-b
305
110-b

FIG. 6

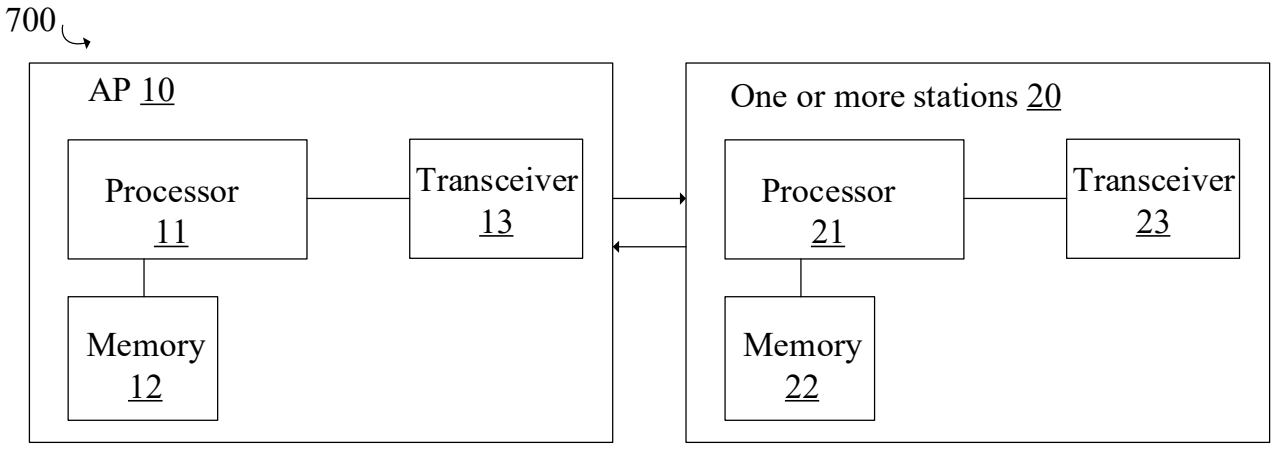

700
AP 10
Processor
11
Transceiver
13
Memory
12
One or more stations 20
Processor
21
Transceiver
23
Memory
22

FIG. 7

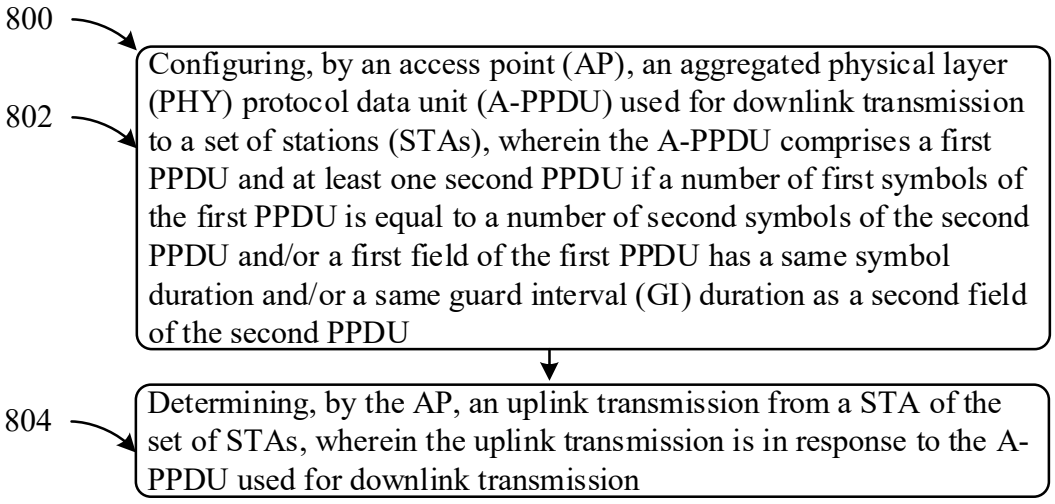


FIG. 8

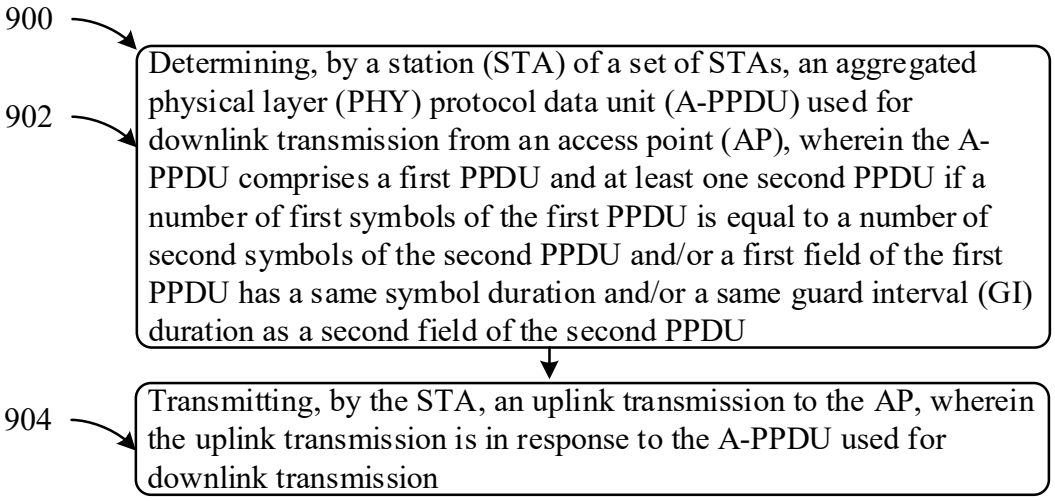


FIG. 9

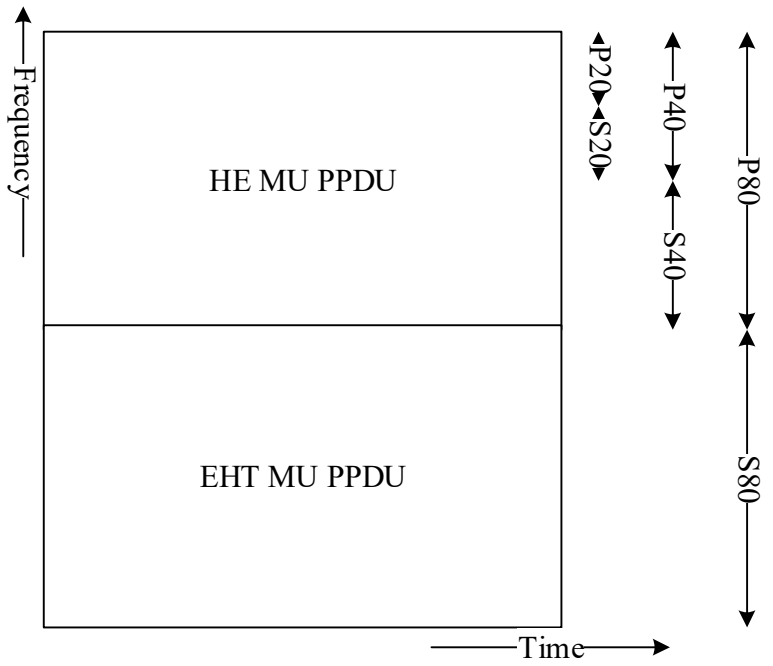


FIG. 10

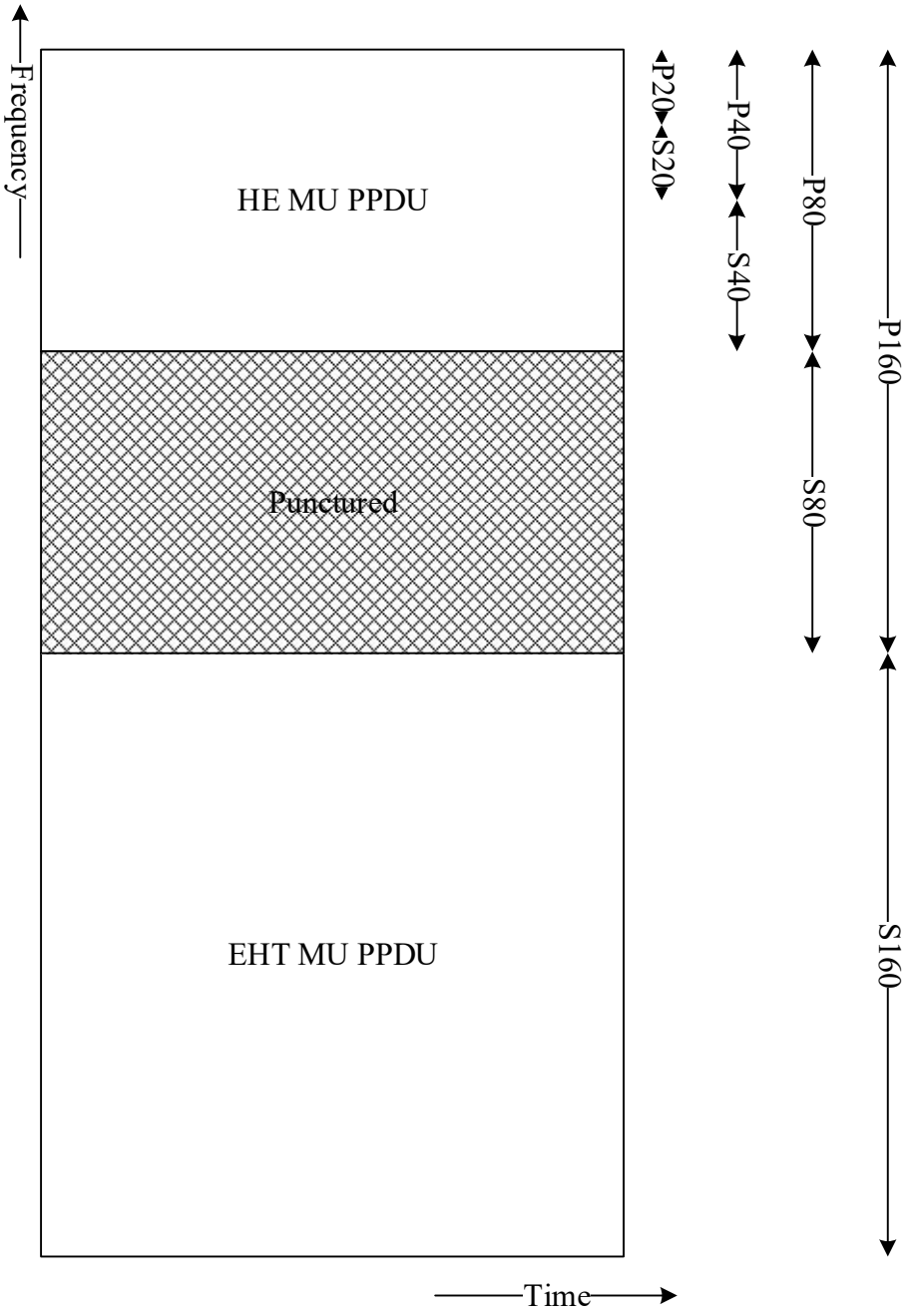

Frequency
HE MU PPDU
Punctured
EHT MU PPDU
Time
P20
S20
P40
S40
P80
S80
P160
S160

FIG. 11A

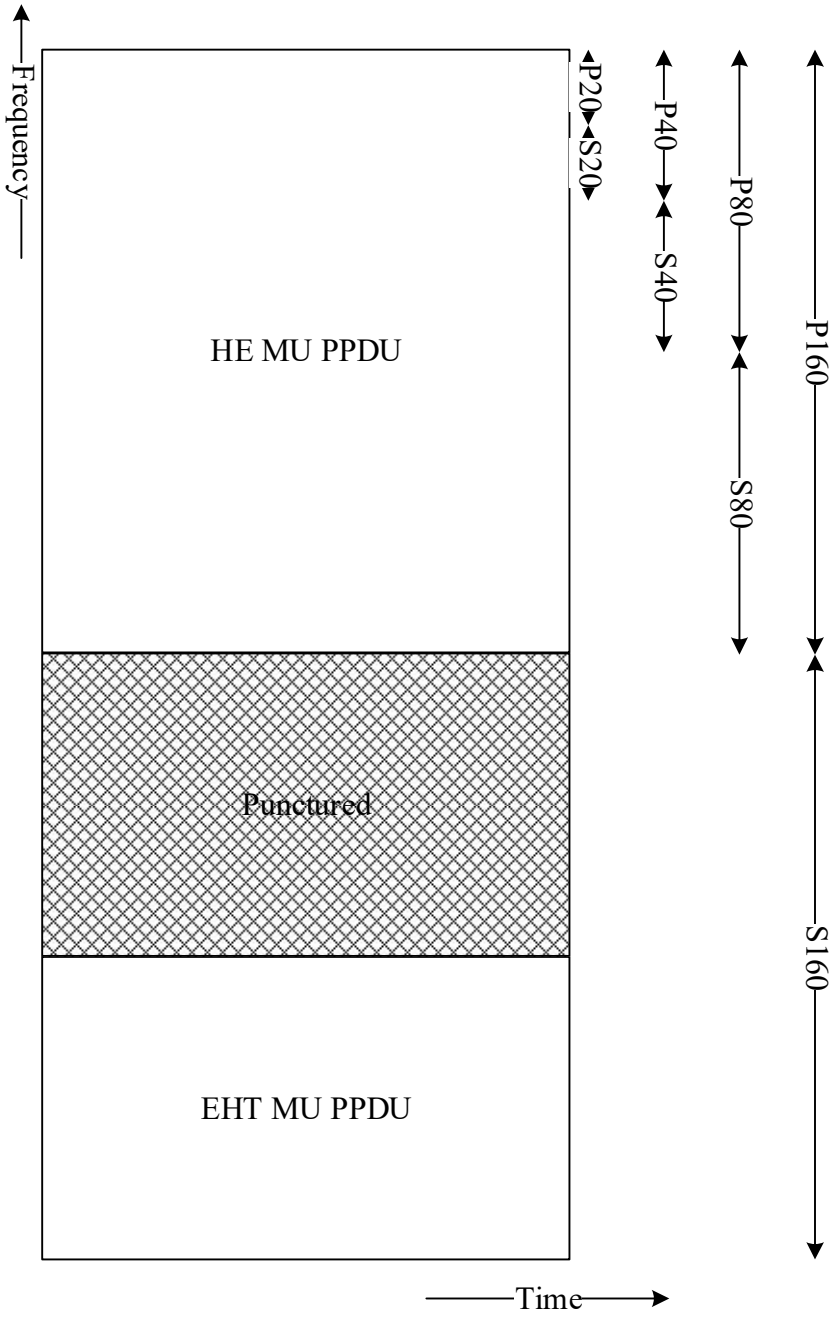

Frequency
P20
S20
P40
S40
P80
S80
P160
S160
HE MU PPDU
Punctured
EHT MU PPDU
Time

FIG. 11B

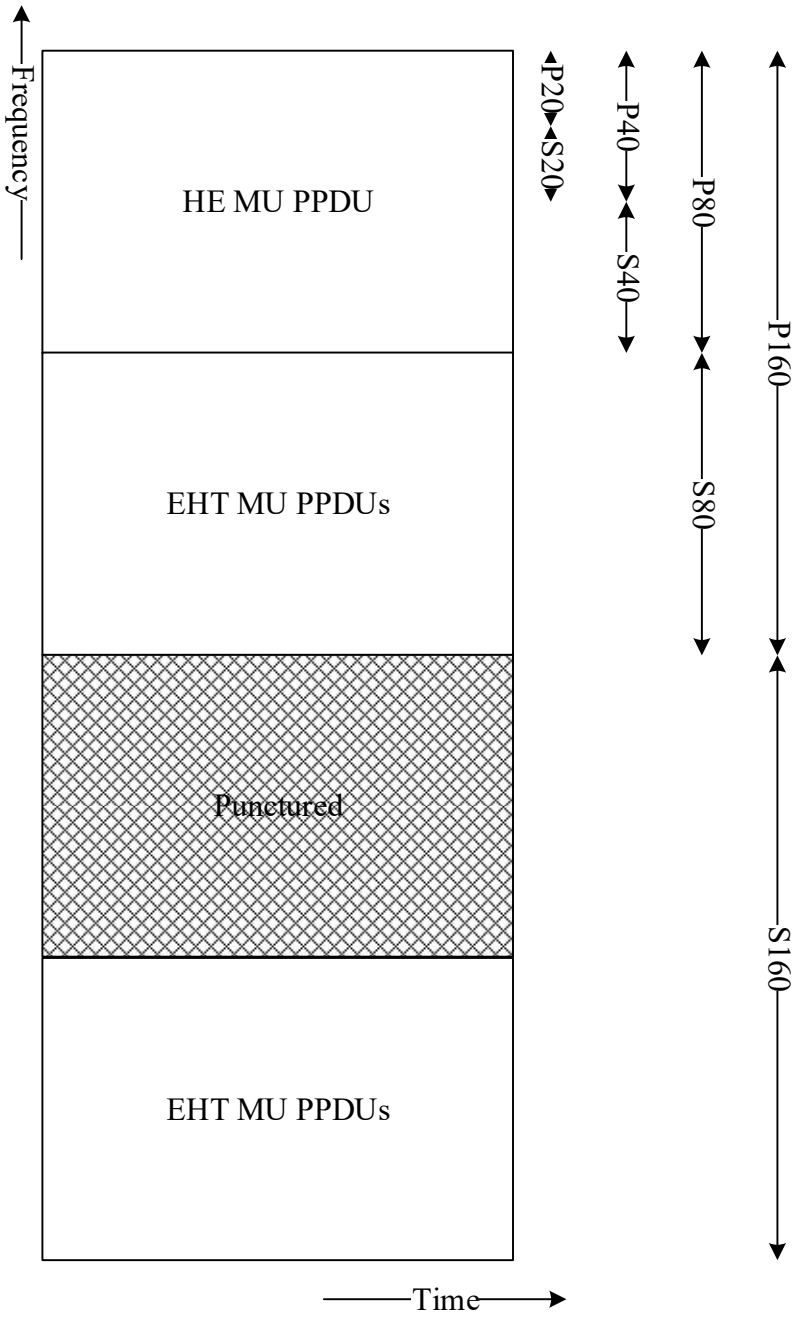

Frequency
HE MU PPDU
EHT MU PPDUs
Punctured
EHT MU PPDUs
Time
P20
S20
P40
S40
P80
S80
P160
S160

FIG. 11C

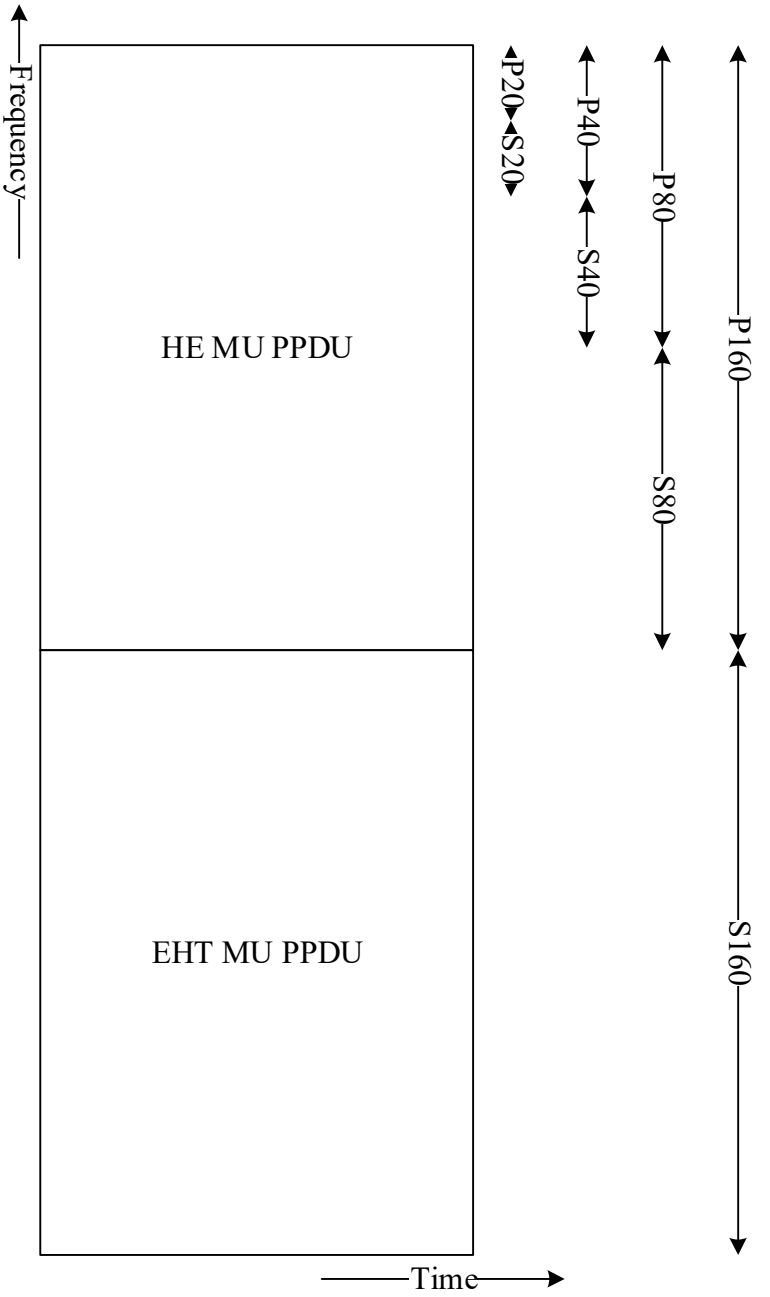

Frequency
HE MU PPDU
EHT MU PPDU
Time
P20
S20
P40
S40
P80
S80
P160
S160

FIG. 11D

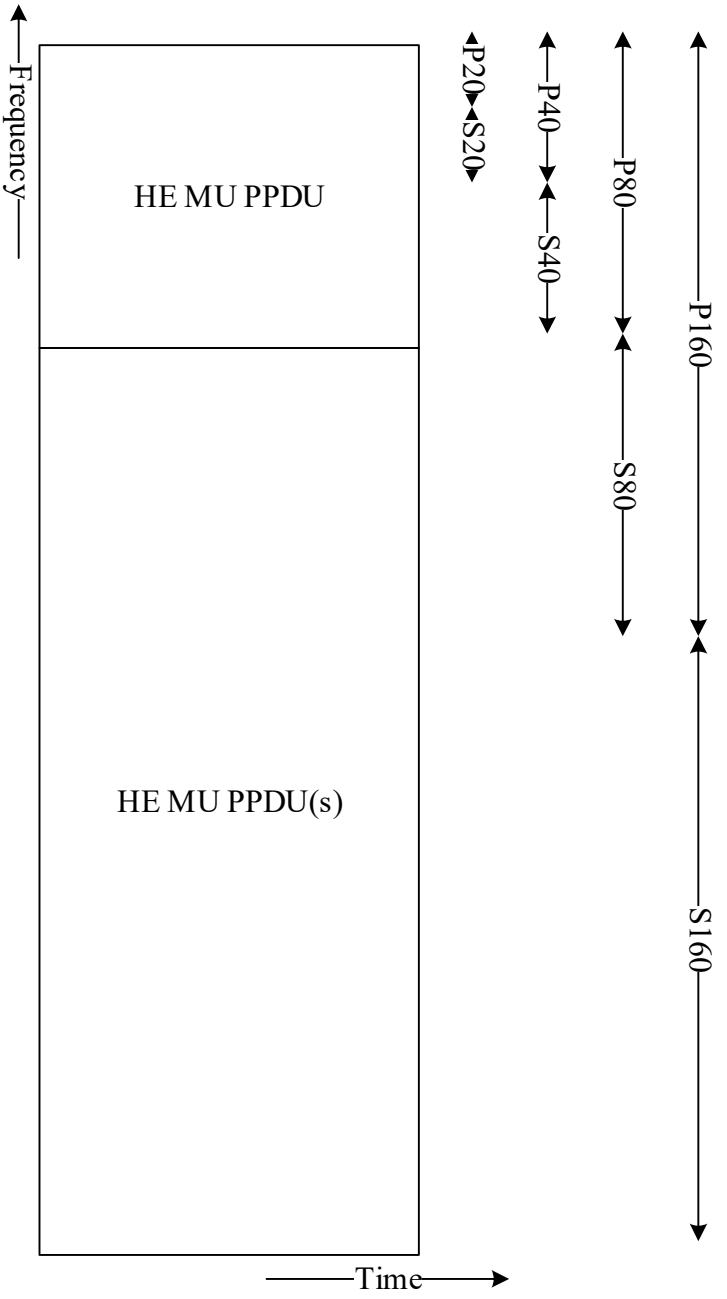

Frequency
HE MU PPDU
HE MU PPDU(s)
Time
P20
S20
P40
S40
P80
S80
P160
S160

FIG. 11E

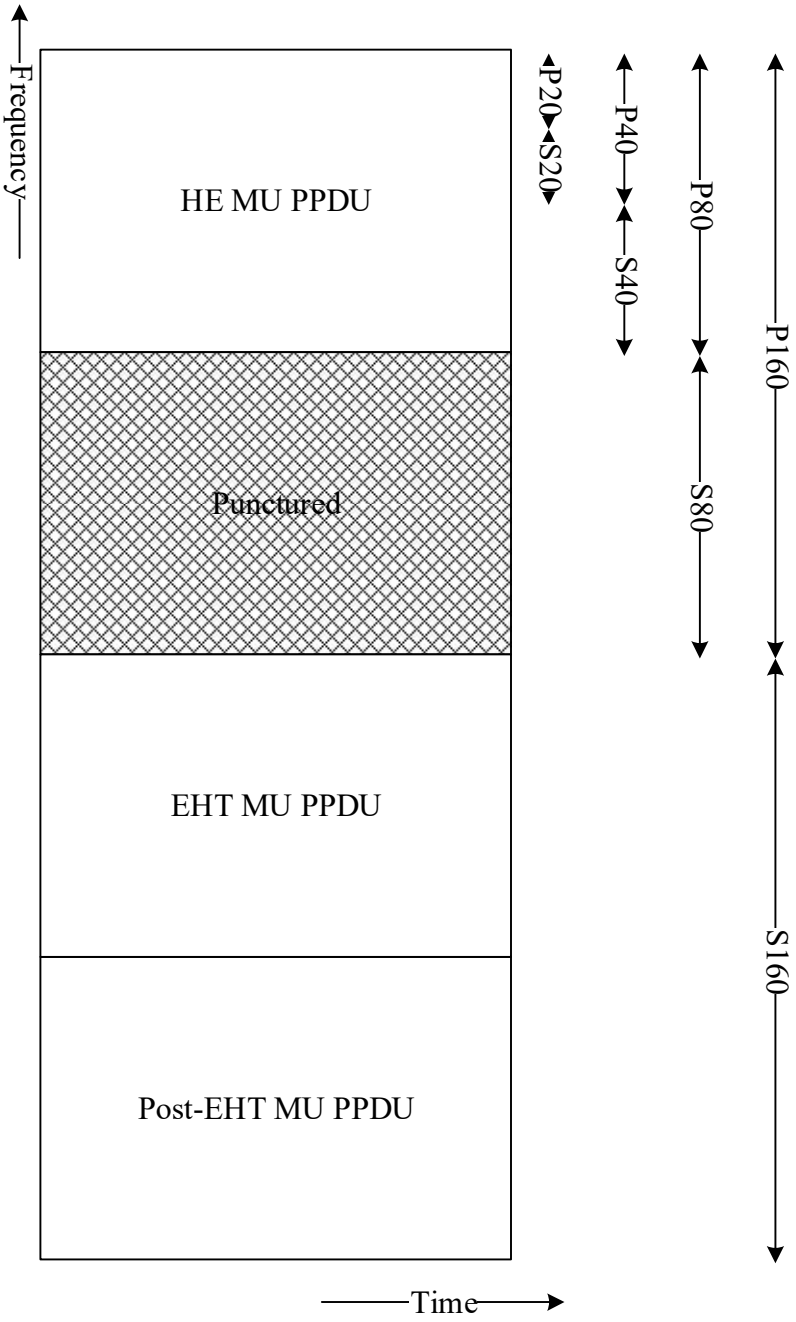

Frequency
HE MU PPDU
Punctured
EHT MU PPDU
Post-EHT MU PPDU
Time
P20
S20
P40
S40
P80
S80
P160
S160

FIG. 12A

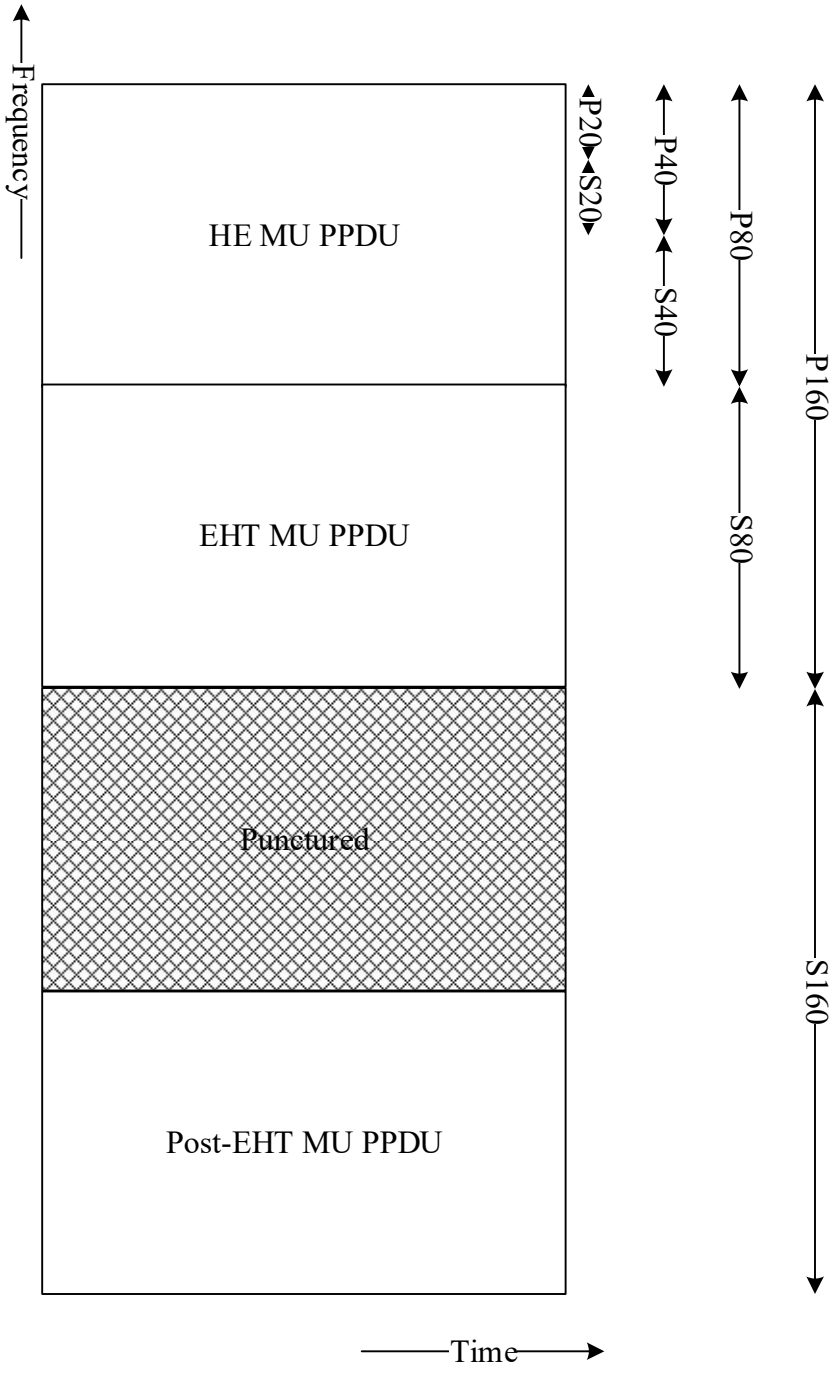

Frequency
HE MU PPDU
EHT MU PPDU
Punctured
Post-EHT MU PPDU
Time
P20
S20
P40
S40
P80
S80
P160
S160

FIG. 12B

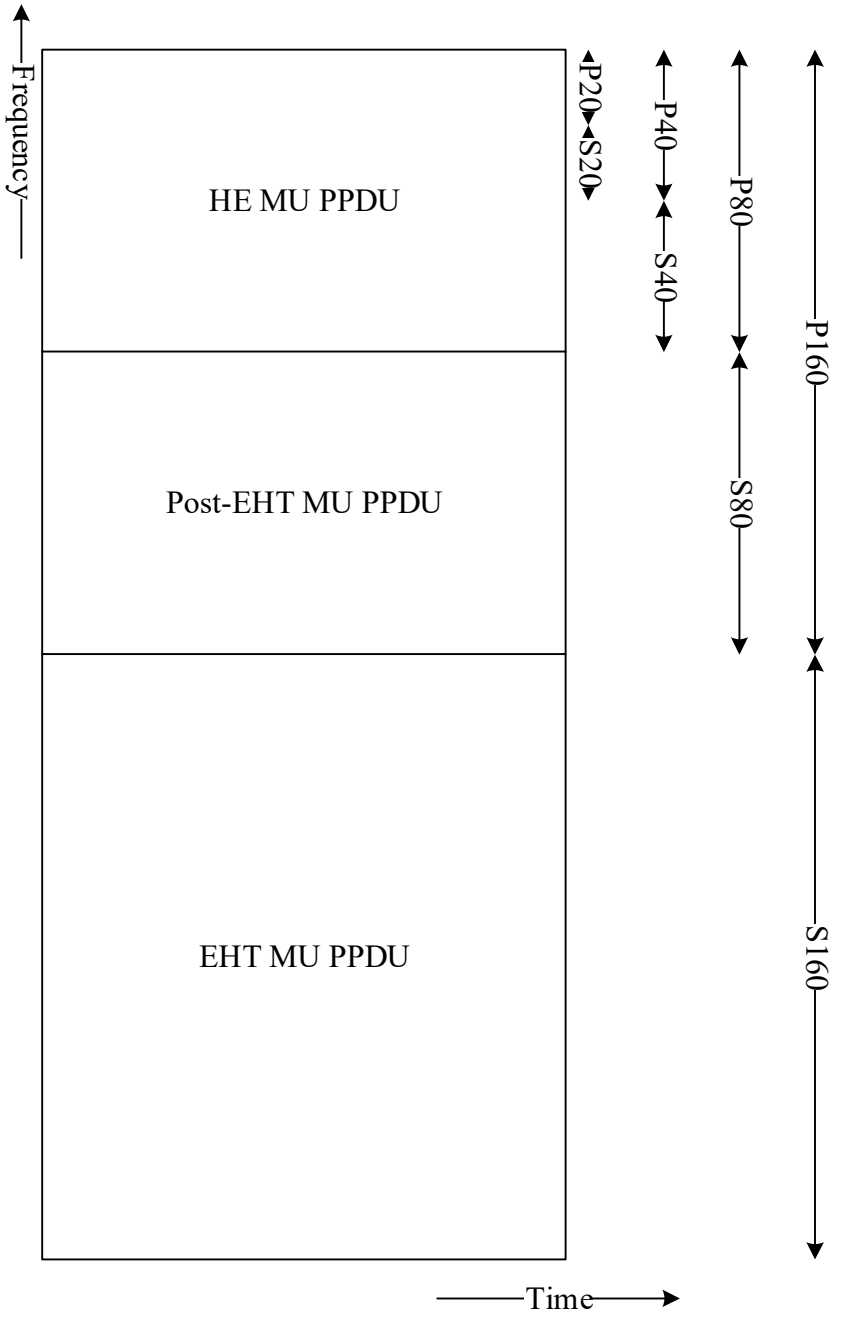

Frequency
HE MU PPDU
Post-EHT MU PPDU
EHT MU PPDU
P20
S20
P40
S40
P80
S80
P160
S160
Time

FIG. 12C

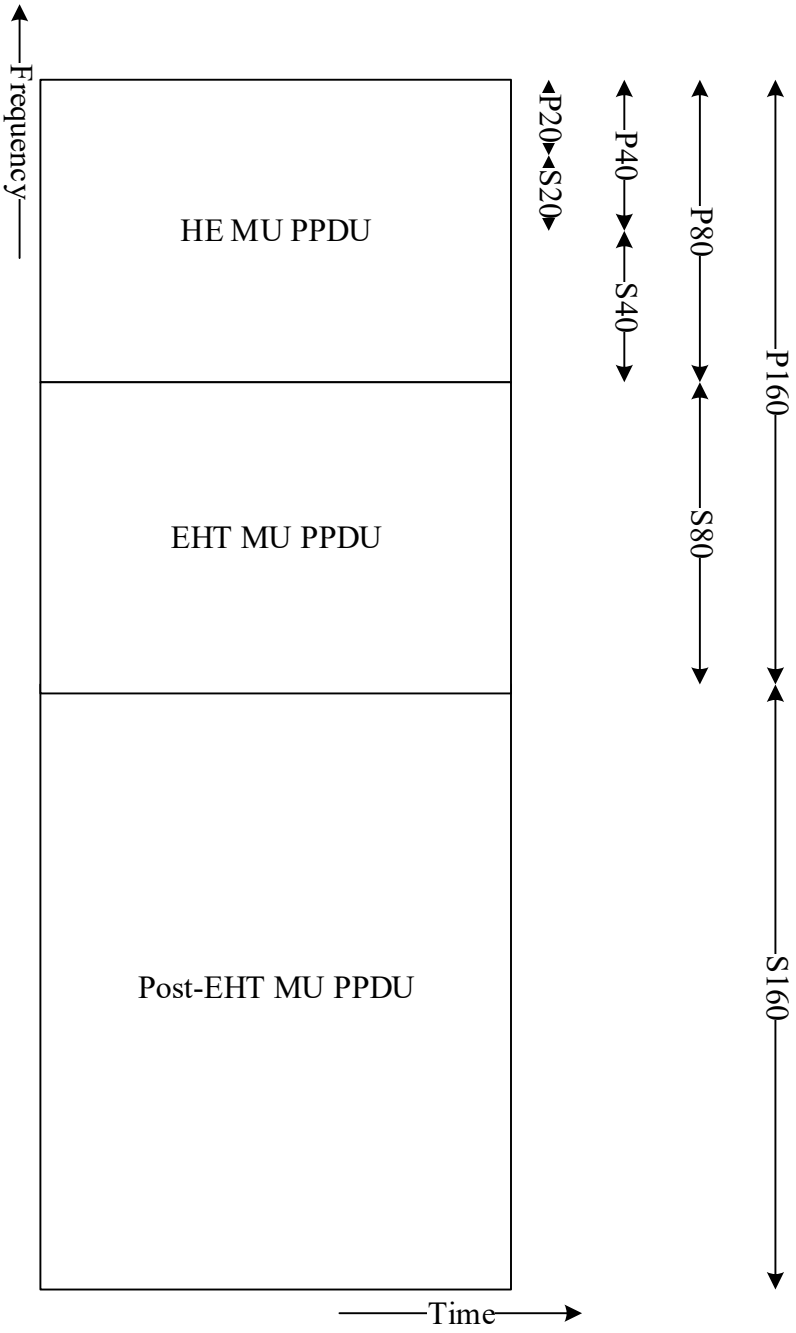

Frequency
HE MU PPDU
EHT MU PPDU
Post-EHT MU PPDU
Time
P20
S20
P40
S40
P80
S80
P160
S160

FIG. 12D

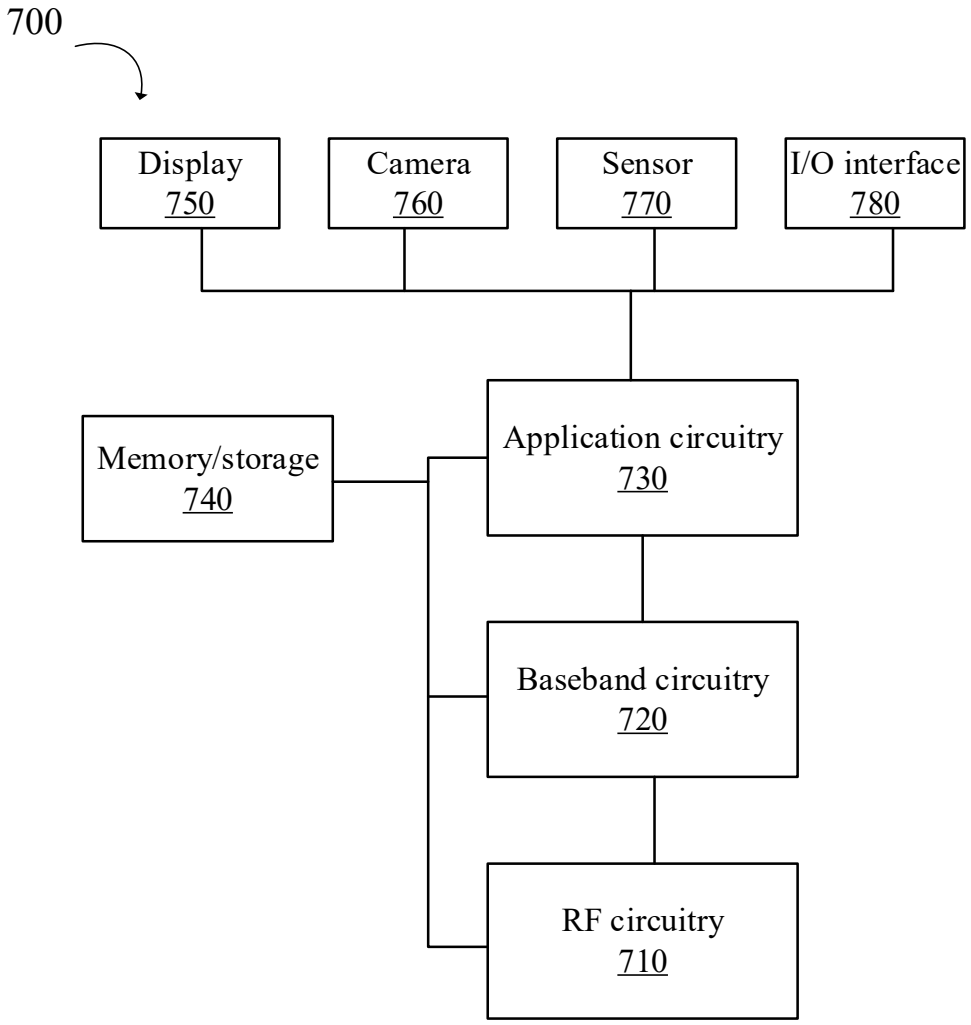

700
Display
750
Camera
760
Sensor
770
I/O interface
780
Memory/storage
740
Application circuitry
730
Baseband circuitry
720
RF circuitry
710

FIG. 13