

Abstract of the Disclosure

A non-linear (e.g., "white stretch") processor (50) and a brightness control processor (54) are coupled in cascade between an input (30) for receiving a video input signal (Y1) to be processed and an output (32) for providing a processed video output signal (Y2). A control signal (Vc) for controlling the transfer characteristics of the non-linear processor is provided by an amplifier (42) having a first input (44) coupled to receive an average picture level (APL) indicating signal derived from the processed video output signal and having a second input (46) coupled to receive a reference level signal (Vr) supplied thereto. A brightness control signal (BC) is applied to a control input (55) of the brightness control processor for controlling the brightness level of the processed video output signal. Circuit means (80) are provided for deriving at least a portion of the reference level signal for the amplifier from the brightness control signal supplied to the brightness control processor thereby reducing a tendency for variations in the brightness control signal to alter the transfer characteristics of the non-linear processor.

This Sheet Has Been Replaced

