



ABSTRACT

DELTA-SIGMA PULSE WIDTH MODULATOR CONTROL CIRCUIT

5 A delta sigma pulse width modulator control circuit uses a delta sigma
modulator (201) as a first stage to create a sequence of pulses representing an input
control signal. A pulse width modulator (205) accumulates the sequence of pulses
and defines a pulse width modulated output signal from the accumulated pulses.
The pulse width modulated signal is given a randomly generated offset to the time
10 of pulse value transition and an adjacent pulse value matching technique is used to
reduce harmonically related noise generation.

(Figure 5)