

**IMPROVED MEMORY SYSTEM
APPARATUS AND METHOD**



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Abstract of the Disclosure

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A memory system and a manner of operating the memory system are provided. The memory system includes several arrays arranged in groups of columns having a column address, and rows having a row address. Operating the system involves storing blocks of data in the arrays in a way that provides contiguous logical addresses for the blocks of data. A block of data is stored in a row of a group of columns in an array. The row address is incremented and the process repeated until a row limit is reached. Next, the process is repeated with another array until blocks of data have been stored in all rows of a corresponding group of columns in the array. The column address is incremented and the row address reset, and the process repeated until a column limit is reached.

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