



INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(51) International Patent Classification 7 :

G11C 16/22, G06F 12/14

A1

(11) International Publication Number:

WO 00/65602

(43) International Publication Date:

2 November 2000 (02.11.00)

(21) International Application Number: PCT/JP00/02309

(22) International Filing Date: 10 April 2000 (10.04.00)

(30) Priority Data:

11/119441	27 April 1999 (27.04.99)	JP
11/374788	28 December 1999 (28.12.99)	JP

(71) Applicant: MATSUSHITA ELECTRIC INDUSTRIAL CO., LTD. [JP/JP]; 1006, Oaza Kadoma, Kadoma-shi, Osaka 571-8501 (JP).

(72) Inventors: HIROTA, Teruto; 1-20-1-306, Kaji-machi, Moriguchi-shi, Osaka 570-0015 (JP). TATEBAYASHI, Makoto; 1-16-21, Mefu, Takarazuka-shi, Hyogo 665-0852 (JP). YUGAWA, Taihei; 708-1-513, Mitsugarasu-cho, Nara-shi, Nara 631-0061 (JP). MINAMI, Masataka; Apartment #103, 1555 Scott Road, Burbank, CA 91504 (US). KOZUKA, Masayuki; 501 Coyle Avenue, Arcadia, CA 91008 (US).

(74) Agent: NAKAJIMA, Shiro; 6F Yodogawa 5-bankan, 2-1, Toyosaki 3-chome, Kita-ku, Osaka-shi, Osaka 531-0072 (JP).

(81) Designated States: AU, BR, CA, CN, ID, IN, KR, MX, RU, SG, VN.

Published

With international search report.

(54) Title: SEMICONDUCTOR MEMORY CARD AND DATA READING APPARATUS

(57) Abstract

A semiconductor memory card comprising a control IC (302), a flash memory (303), and a ROM (304). The ROM (304) holds information such as a medium ID (341) unique to the semiconductor memory card. The flash memory (303) includes an authentication memory (332) and a non-authentication memory (331). The authentication memory (332) can be accessed only by external devices which have been affirmatively authenticated. The non-authentication memory 331 can be accessed by external devices whether the external devices have been affirmatively authenticated or not. The control IC (302) includes control units (325) and (326), an authentication unit (321) and the like. The control units (325) and (326) control accesses to the authentication memory (332) and the non-authentication memory (331), respectively. The authentication unit (321) executes a mutual authentication with an external device.

