

METHOD FOR EXECUTING INSTRUCTIONS IN CPU

ABSTRACT

A method for executing instructions in a central processing unit (CPU), including fetching, from a current thread queue, instructions to form an instruction block and transmitting the instruction block to a CPU execution unit for execution, wherein the instruction block contains one jump instruction defined as a last instruction in the instruction block; replenishing the current thread queue with at least one instruction to form a thread queue to be executed; determining a target instruction of the jump instruction according to an execution result of the CPU execution unit; determining whether the target instruction is contained in the thread queue to be executed, if the target instruction is not contained in the thread queue to be executed, flushing the thread queue to be executed, obtaining the target instruction and adding the target instruction to the thread queue to be executed.

Figure 2