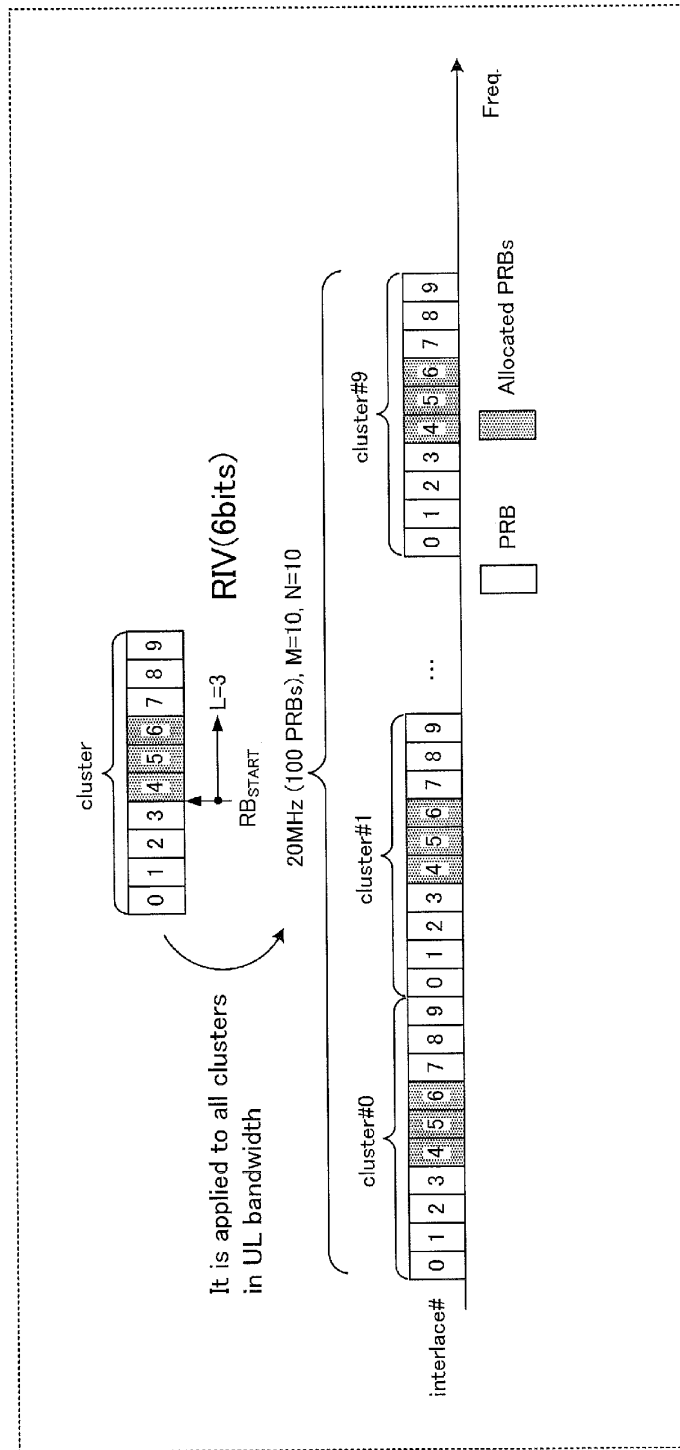


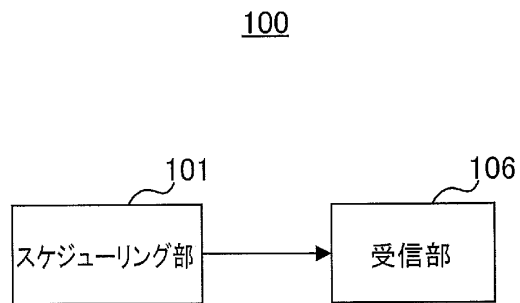
[図1]



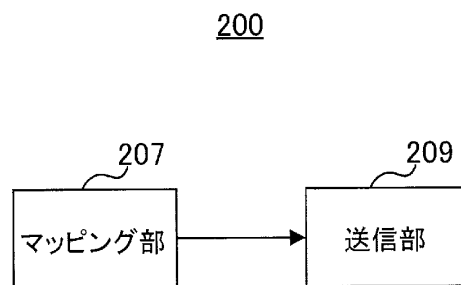
[図2]

SCS	M	N
15 kHz	12	8 or 9
	10	10 or 11
	8	13 or 14
30 kHz	6	8 or 9
	5	10 or 11
	4	12 or 13
60 kHz	4	6
	3	8
	2	12
60 kHz (if 26 PRBs is supported in a 20 MHz bandwidth)	4	6 or 7
	2	13
	3	8 or 9

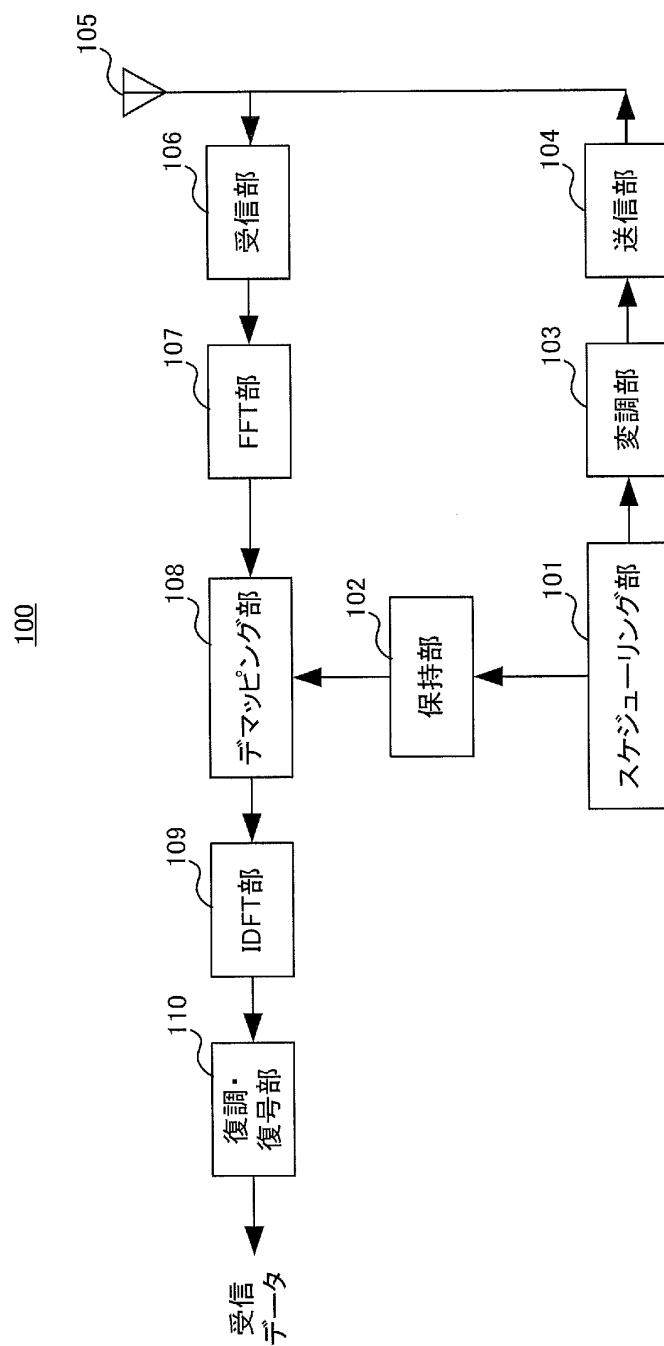
[図4]



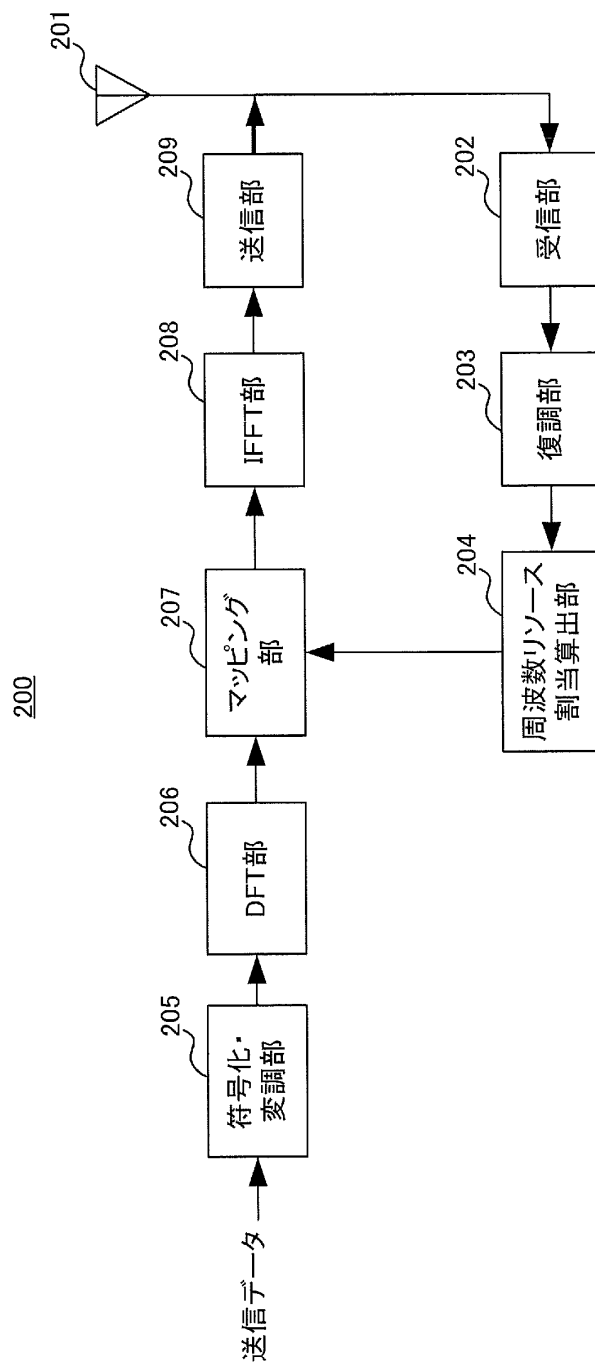
[図5]



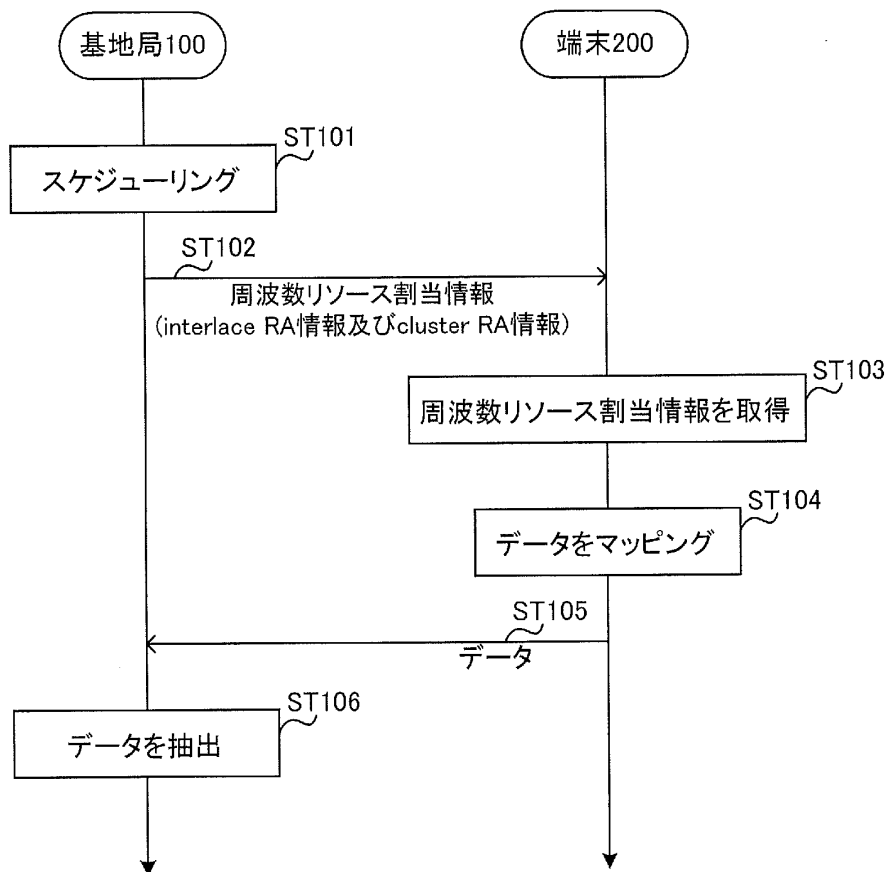
[図6]



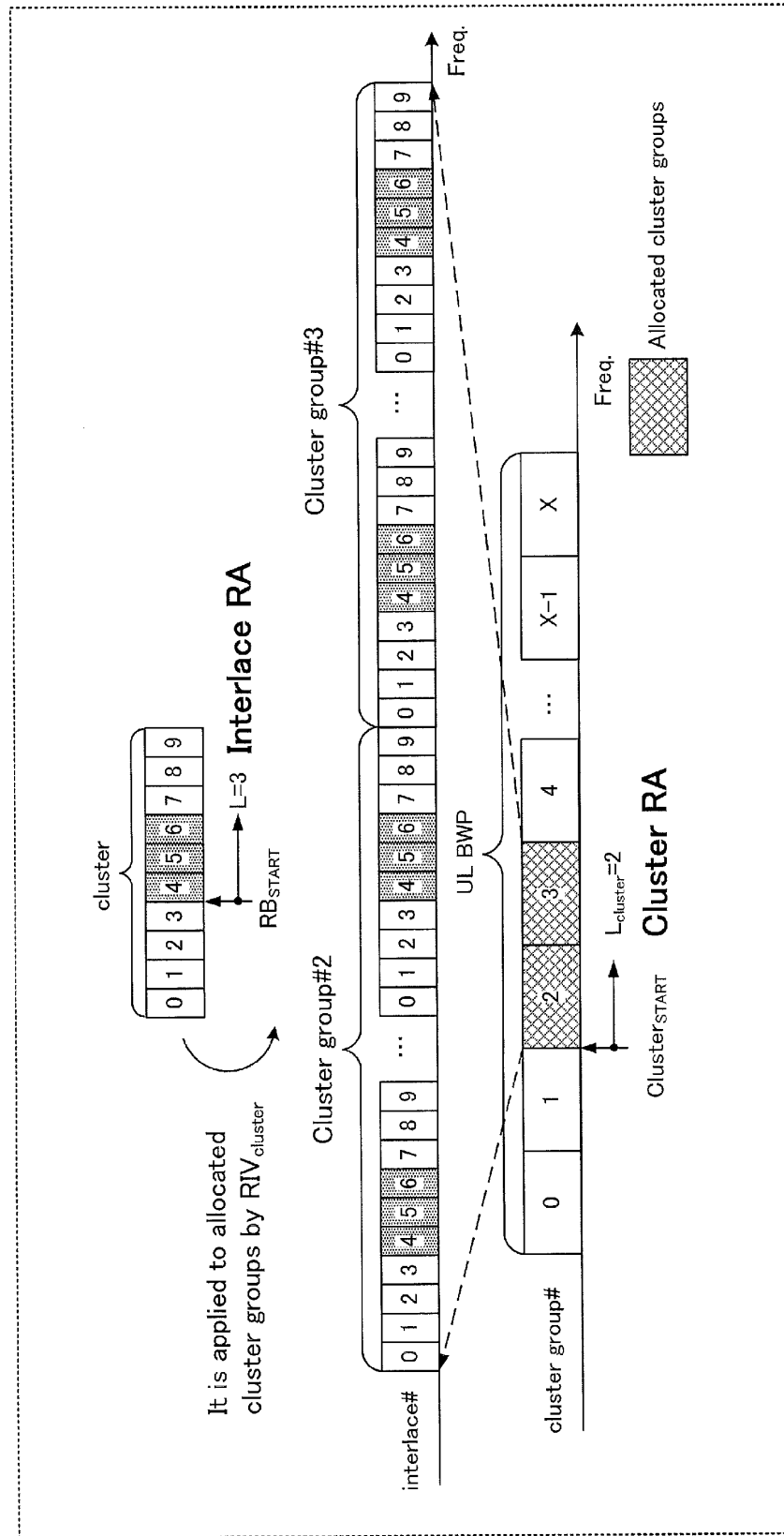
[図7]



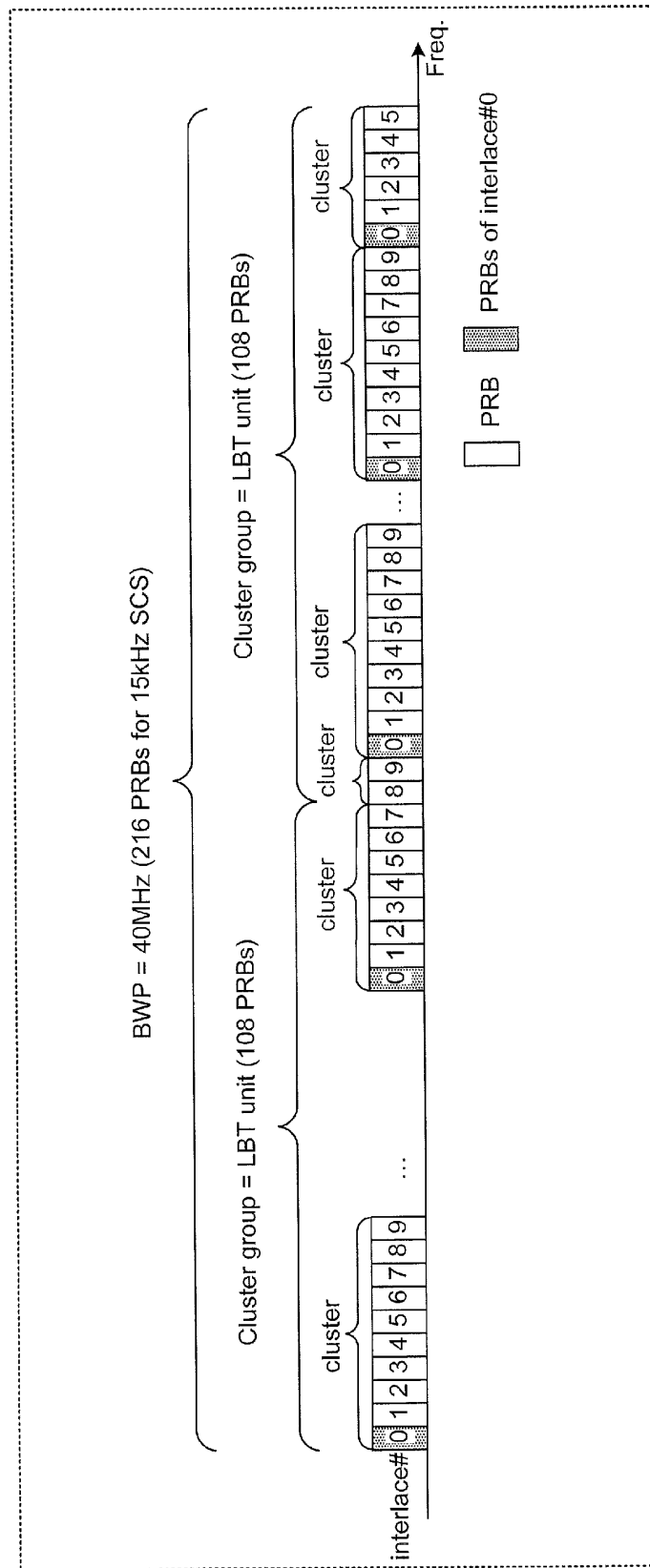
[図8]



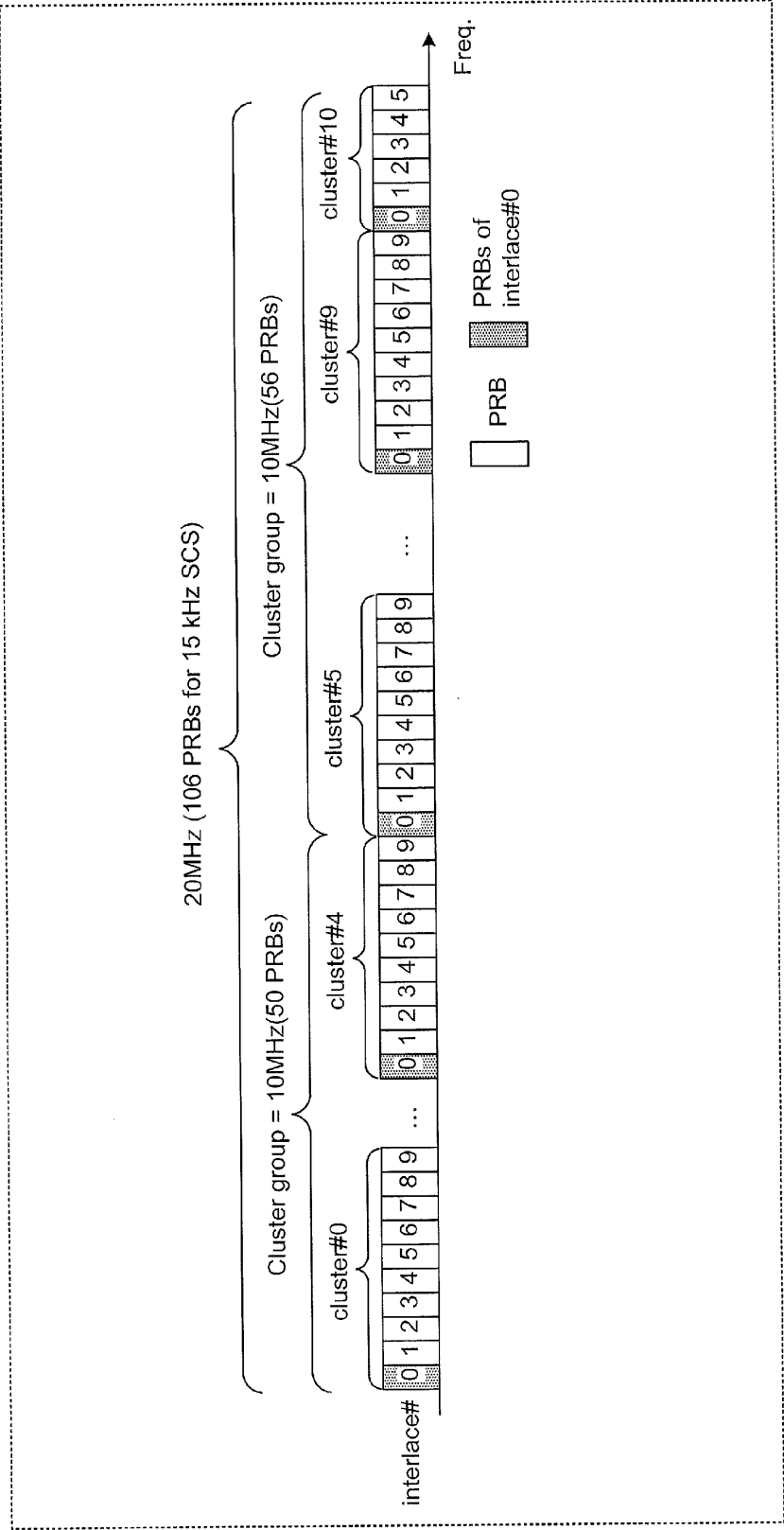
[図9]



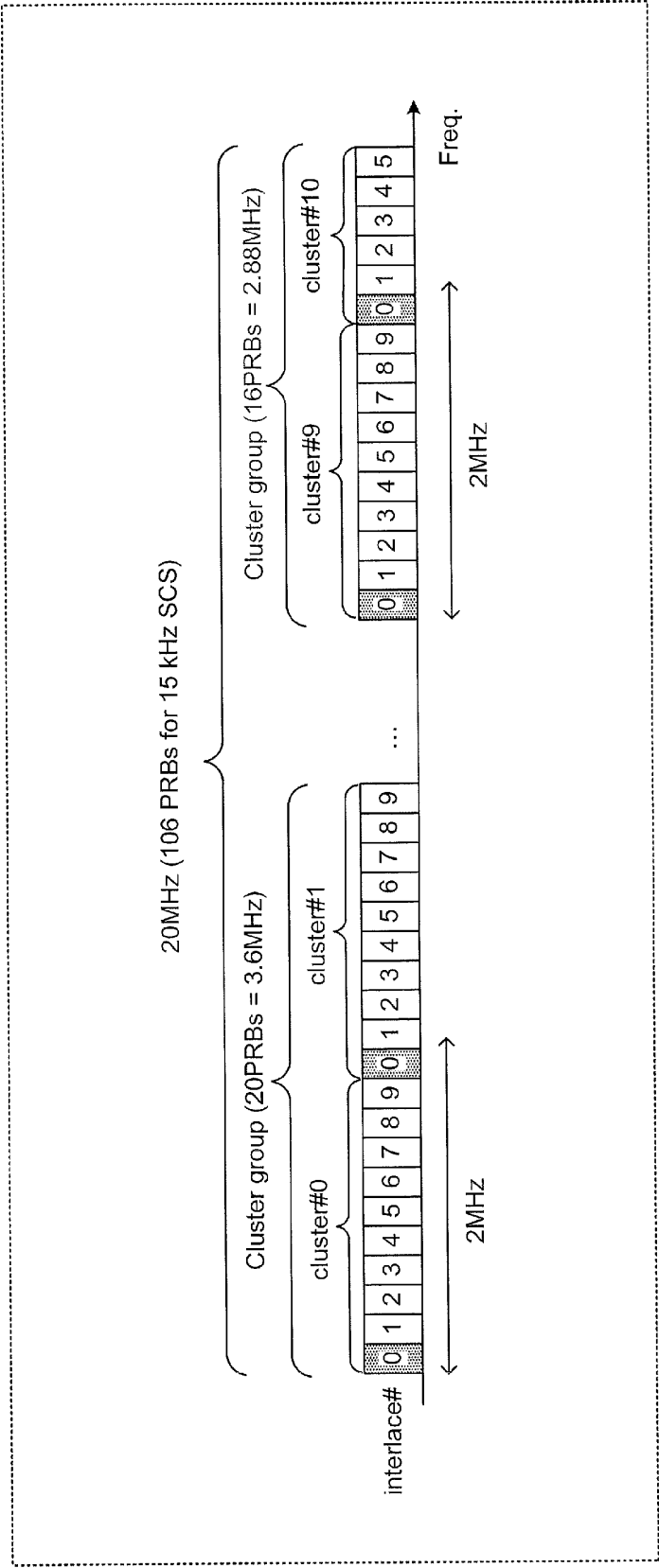
[図10]



[図11]



[図12]



[図13]

RIV	RBstart	L
0	0	1
1	1	1
2	2	1
3	3	1
⋮	⋮	⋮
⋮	⋮	⋮
⋮	⋮	⋮
53	3	6
54	4	6
55		
56		
57		
58		
59		
60		
61		
62		
63		

6bits

Contiguous interlace pattern for M=10, N=10 or 11

Remaining RIVs

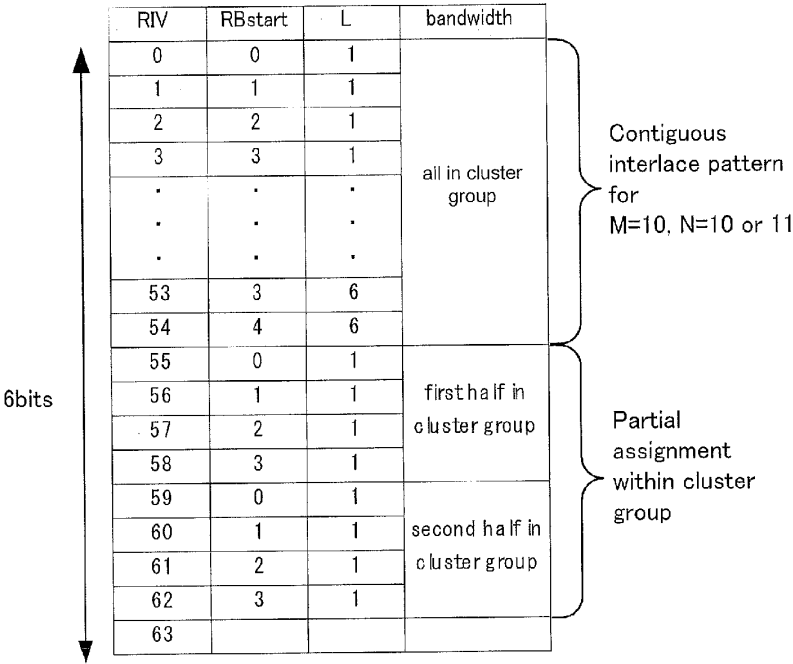
[図14]

	RIV	RBstart	L	M	
6bits	0	0	1	10	Contiguous interlace pattern for M=10, N=10 or 11
	1	1	1		
	2	2	1		
	3	3	1		
	.	.	.		
	.	.	.		
	53	3	6	12	Contiguous interlace pattern for M=12, N=9
	54	4	6		
	55	0	1		
	56	1	1		
	57	2	1		
	58	3	1		
	59	4	1		
	60	5	1		
	61	6	1		
	62	7	1		
63	no transmission				

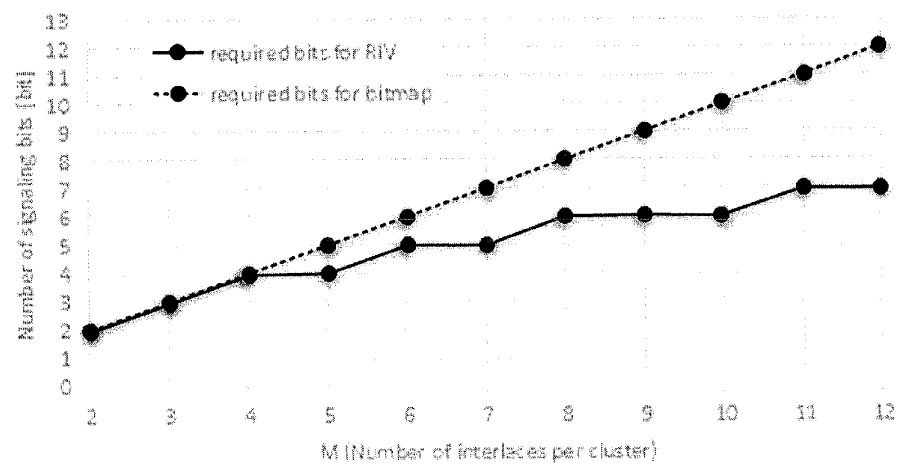
[図15]

	RIV	RBstart	L	M	
6bits	0	0	1	10	Contiguous interlace pattern for M=10, N=10 or 11 L = {1, 2, 4, 6, 8, 10}
	1	1	1		
	.	.	.		
	.	.	.		
	.	.	.		
	32	2	6		
	33	3	6		
	34	4	6		
	35	0	1	12	Contiguous interlace pattern for M=12, N=8 or 9 L = {1}
	36	1	1		
	37	2	1		
	.	.	.		
	.	.	.		
	.	.	.		
	45	10	1		
	46	11	1		
.	.	.			
.	.	.			
.	.	.			
62					
63					

[図16]



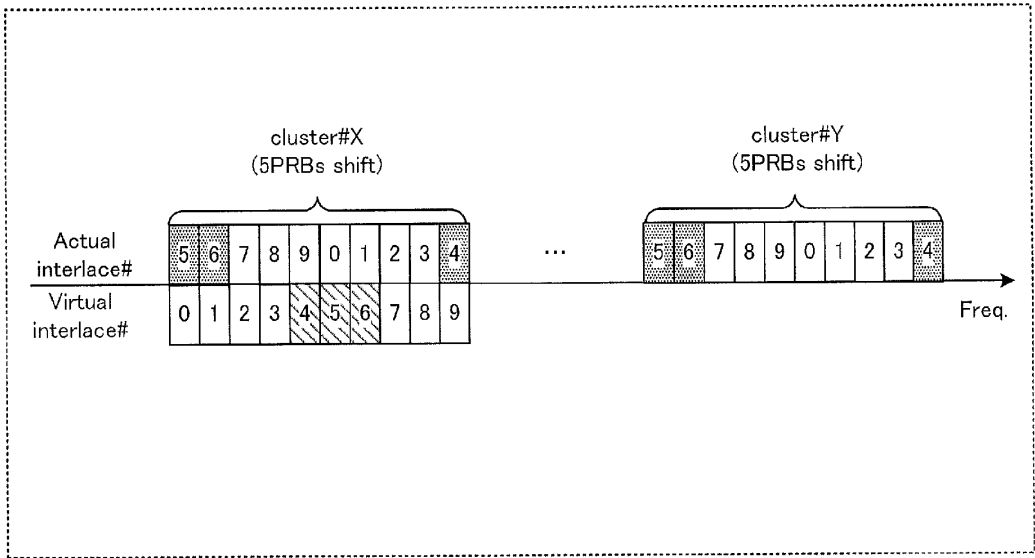
[図17]



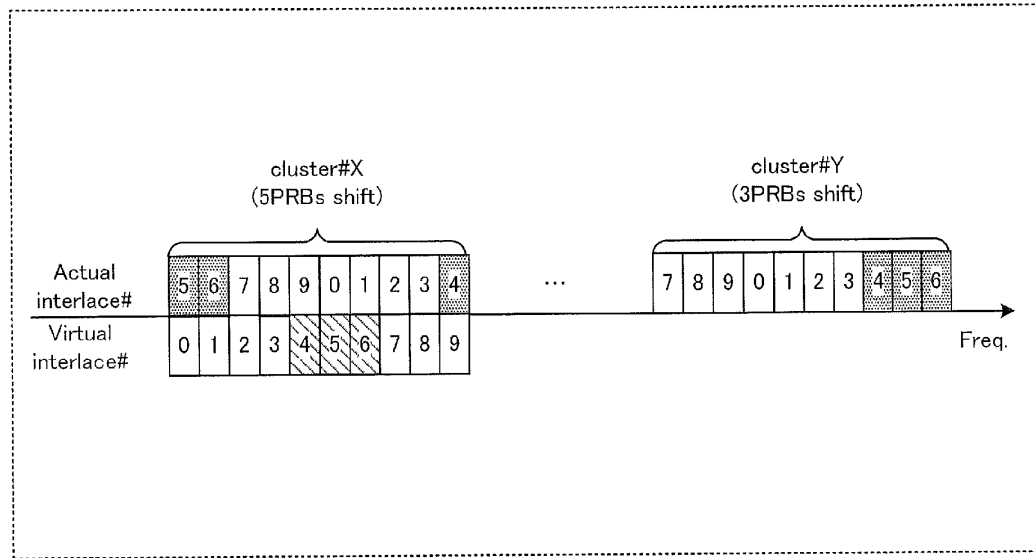
[図18]

SCS	M	Allocation method	Signaling bits
15	12	RIV base	7
	10		6
	8		6
30	6	Bitmap base	6
	5		
	4		
60	4	Bitmap base	4
	3		
	2		

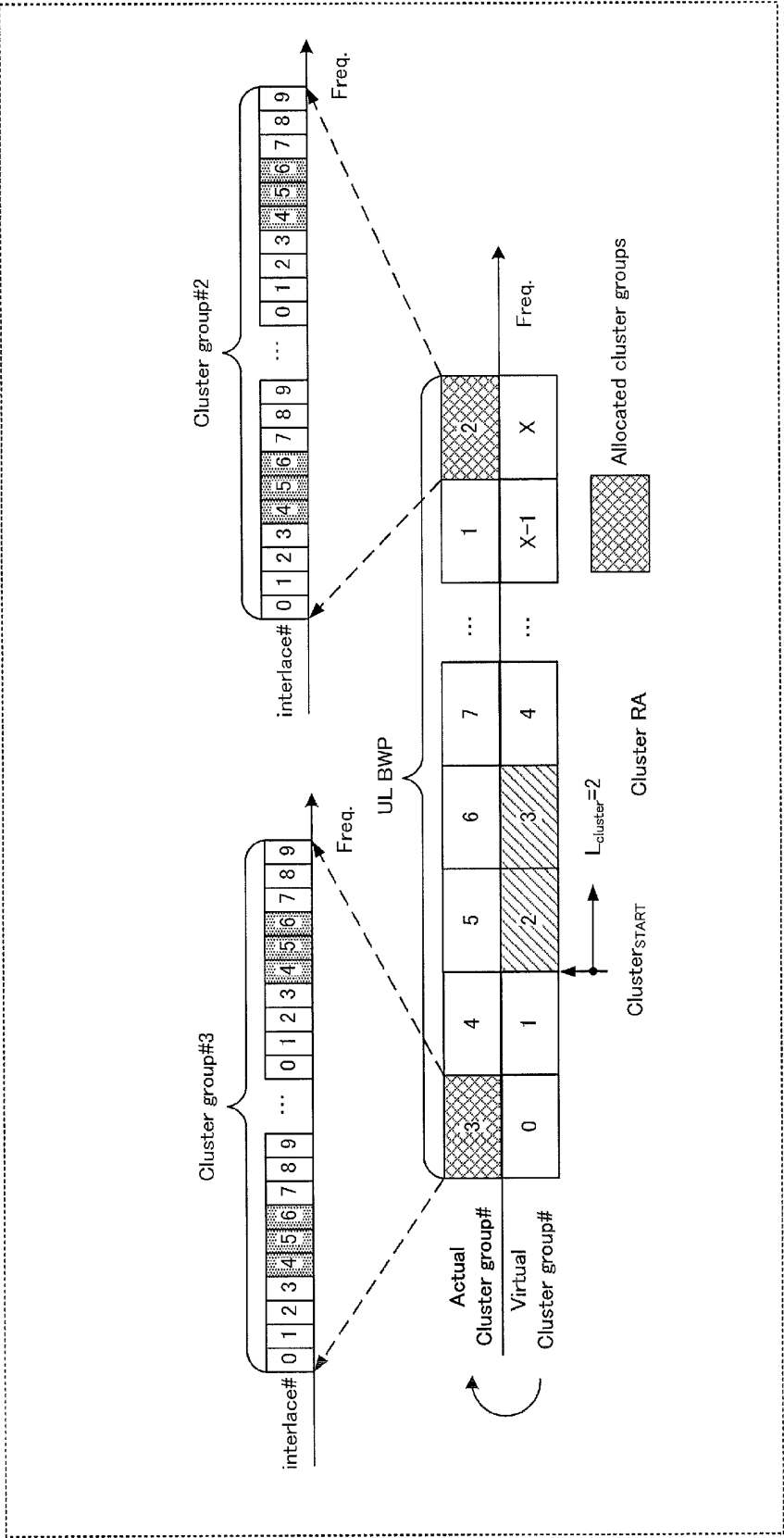
[図19]



[図20]



[図21]



[図22]

BWP [MHz]	SCS	Number of Cluster group	Allocation method	Signaling bits
20	10	2	Bitmap base	2
40		4		4
60		6	RIV base	5
80		8		6