

SEMICONDUCTOR APPARATUS

ABSTRACT

Provided is a semiconductor apparatus. The semiconductor apparatus comprises a memory chip and a temperature detection module, wherein the temperature detection module is used for detecting the temperature of the memory chip. The temperature detection module comprises a temperature detection unit, which comprises a temperature sensitive unit and an adjustable resistor unit, with the electrical conductivity of the temperature sensitive unit changing with changes in temperature, and the adjustable resistor unit being connected in parallel to the temperature sensitive unit, wherein the temperature detection unit is configured to be calibrated by means of adjusting a resistance value of the adjustable resistor unit. In the present invention, the temperature detection module is used to detect the temperature of the memory chip, thereby preventing the memory chip from being started and operated at a low temperature, shortening a write-in time and improving the stability of memory chip write-in. In addition, in the present invention, the temperature detection module has a simple circuit structure which makes same easy to implement, the temperature detection module occupies a small area, and the temperature detection unit is calibrated, thereby improving the detection accuracy of the temperature detection unit.

[Fig. 2]