

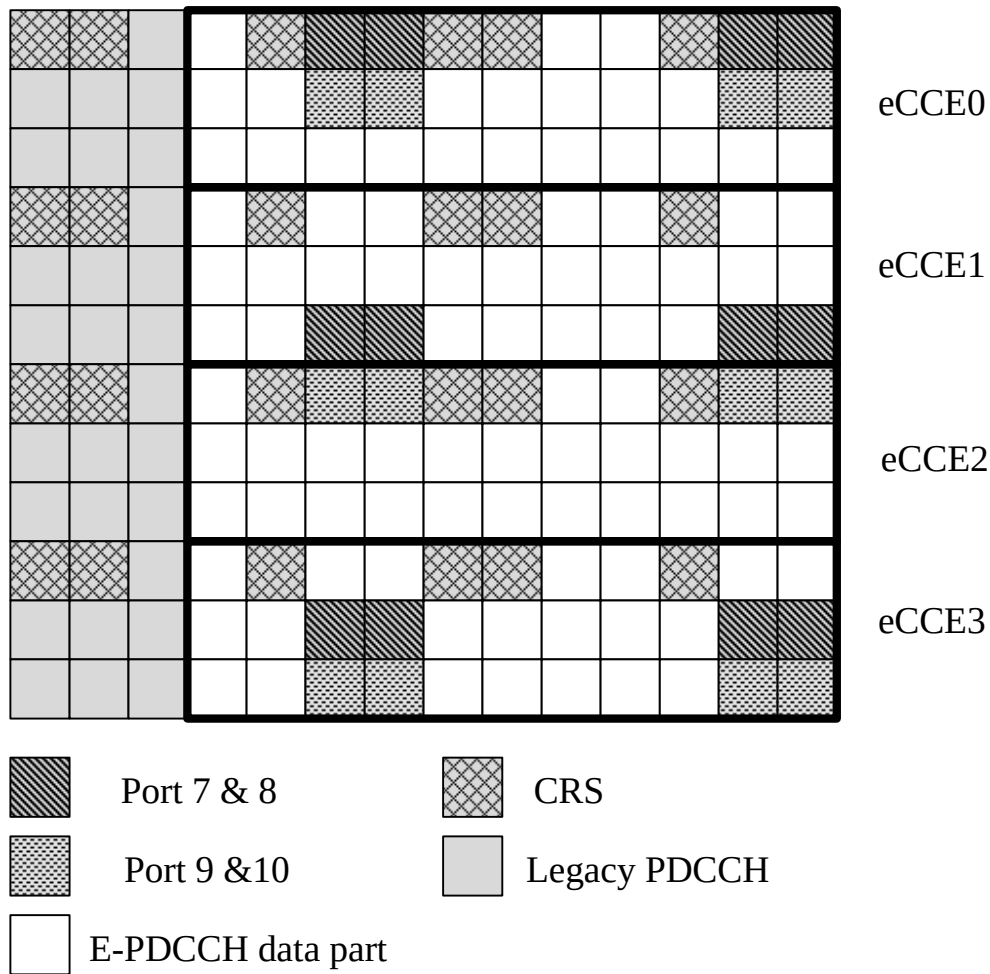


FIG. 1

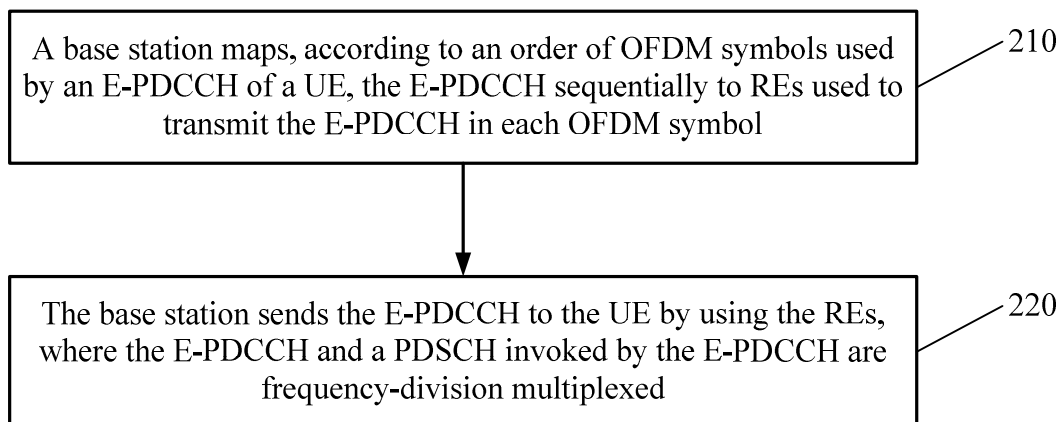


FIG. 2

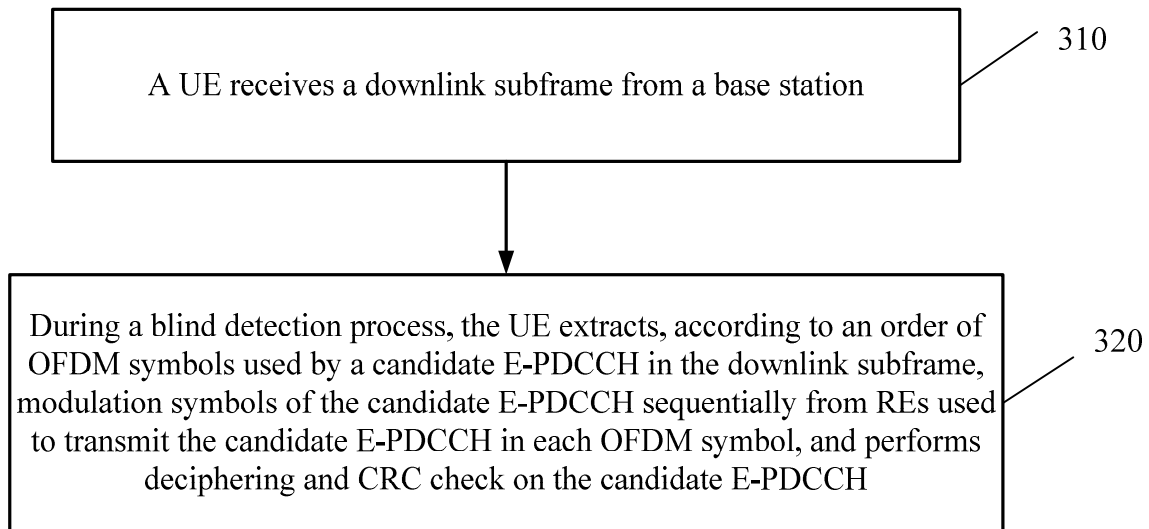


FIG. 3

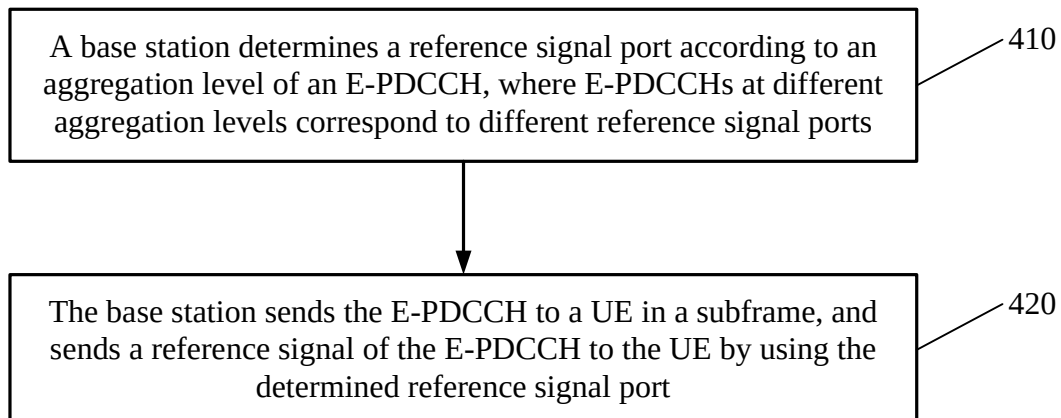


FIG. 4

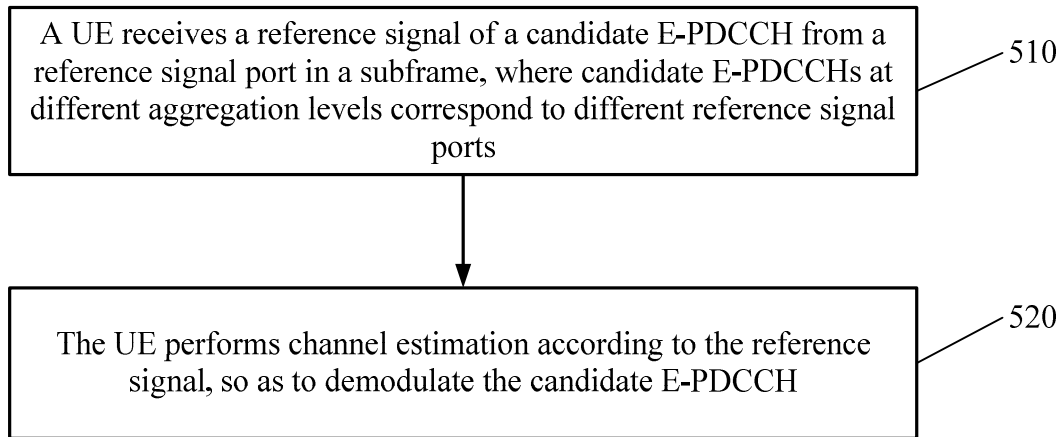


FIG. 5

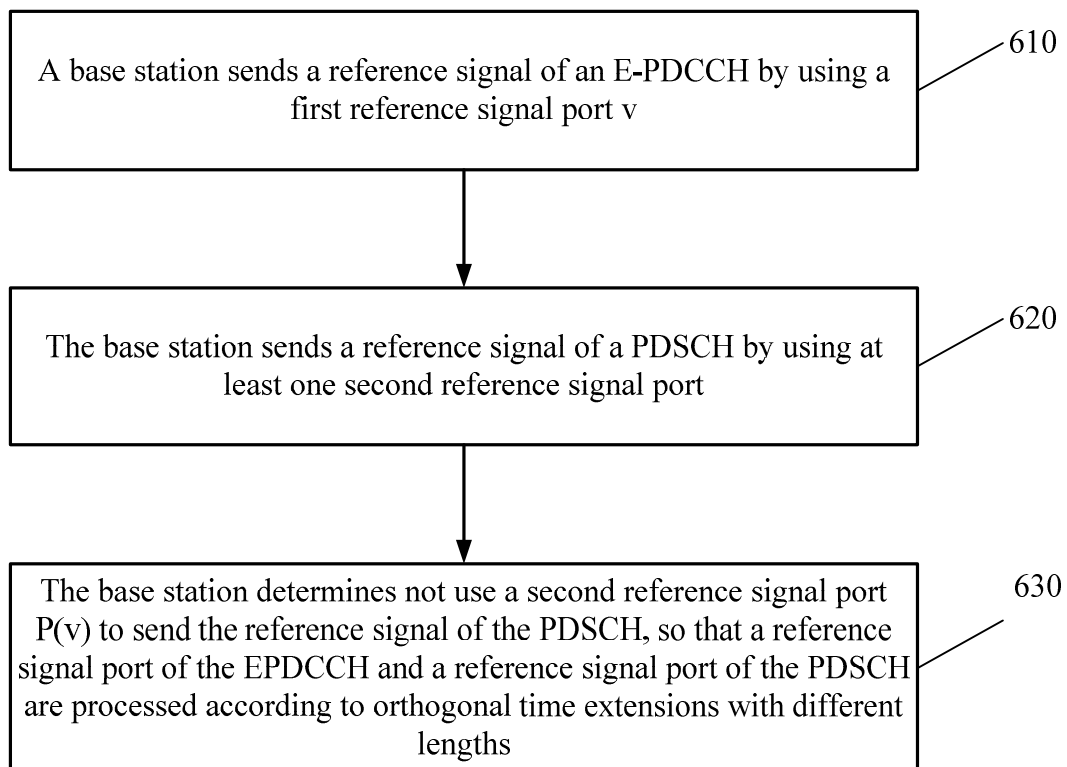


FIG. 6

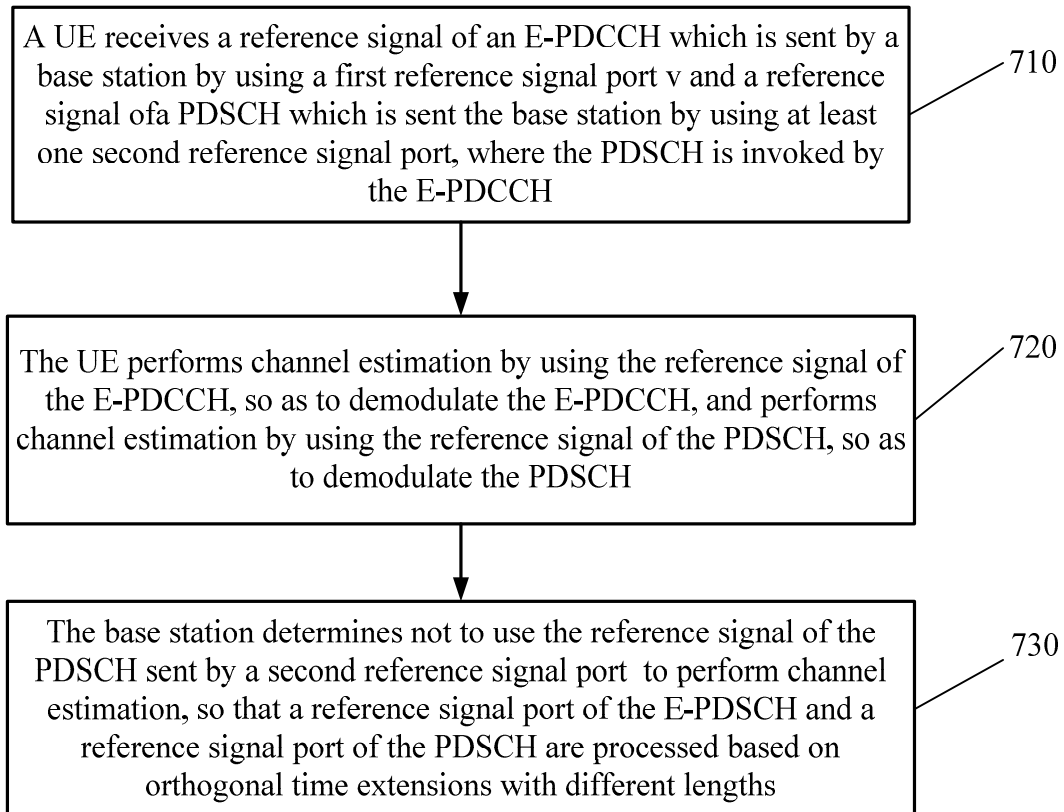


FIG. 7

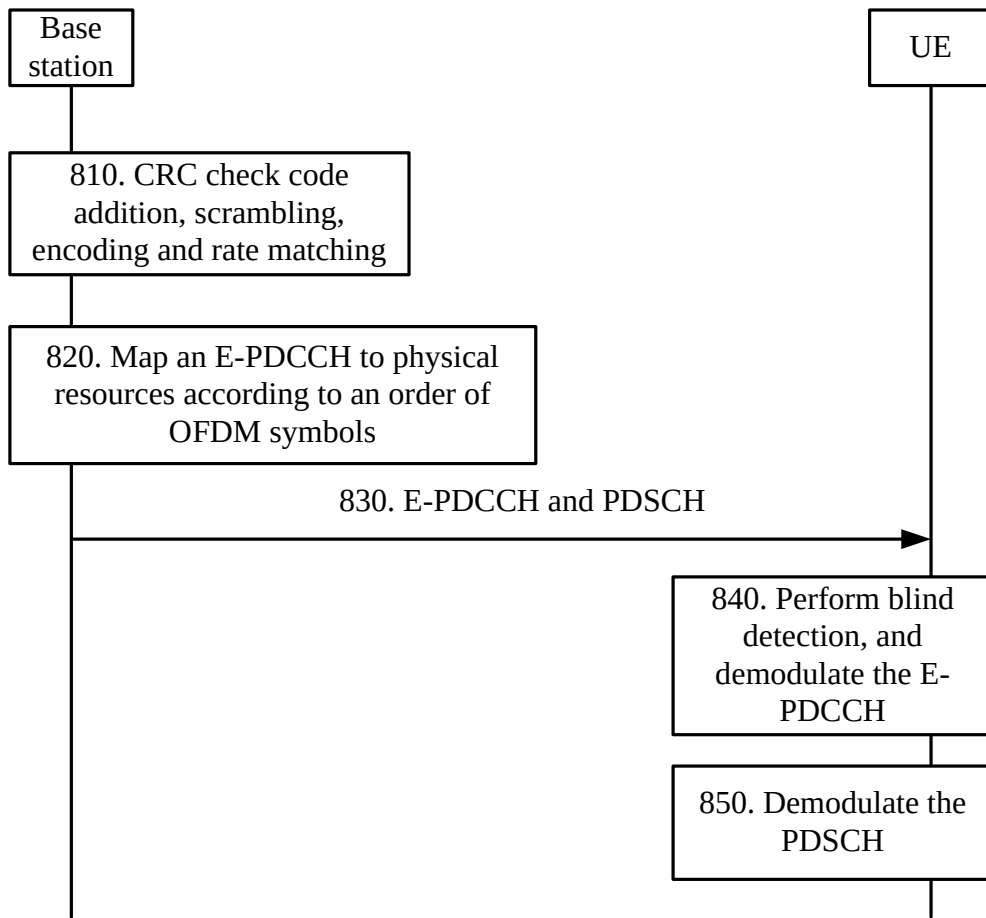


FIG. 8A

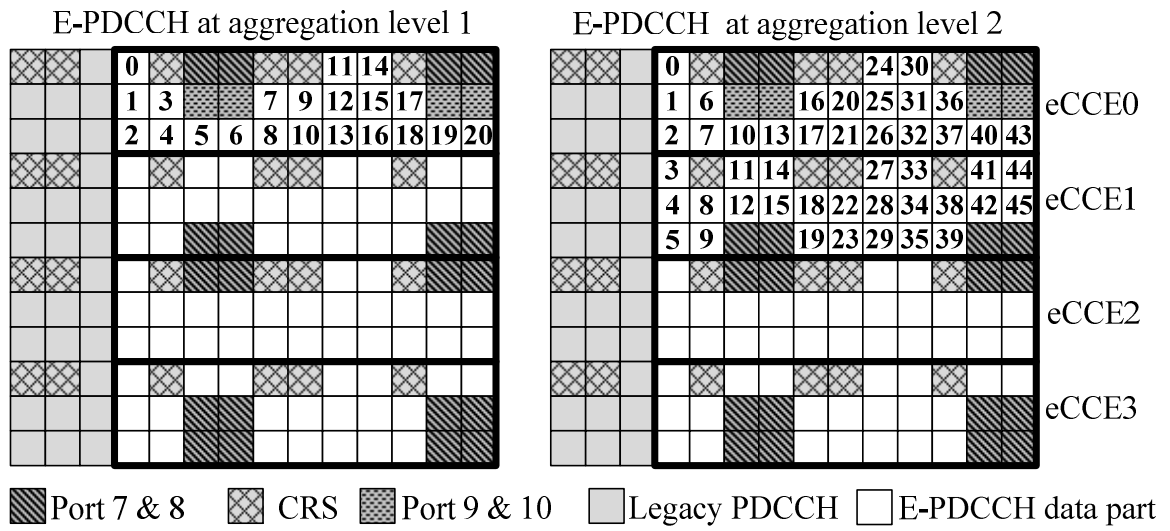


FIG. 8B

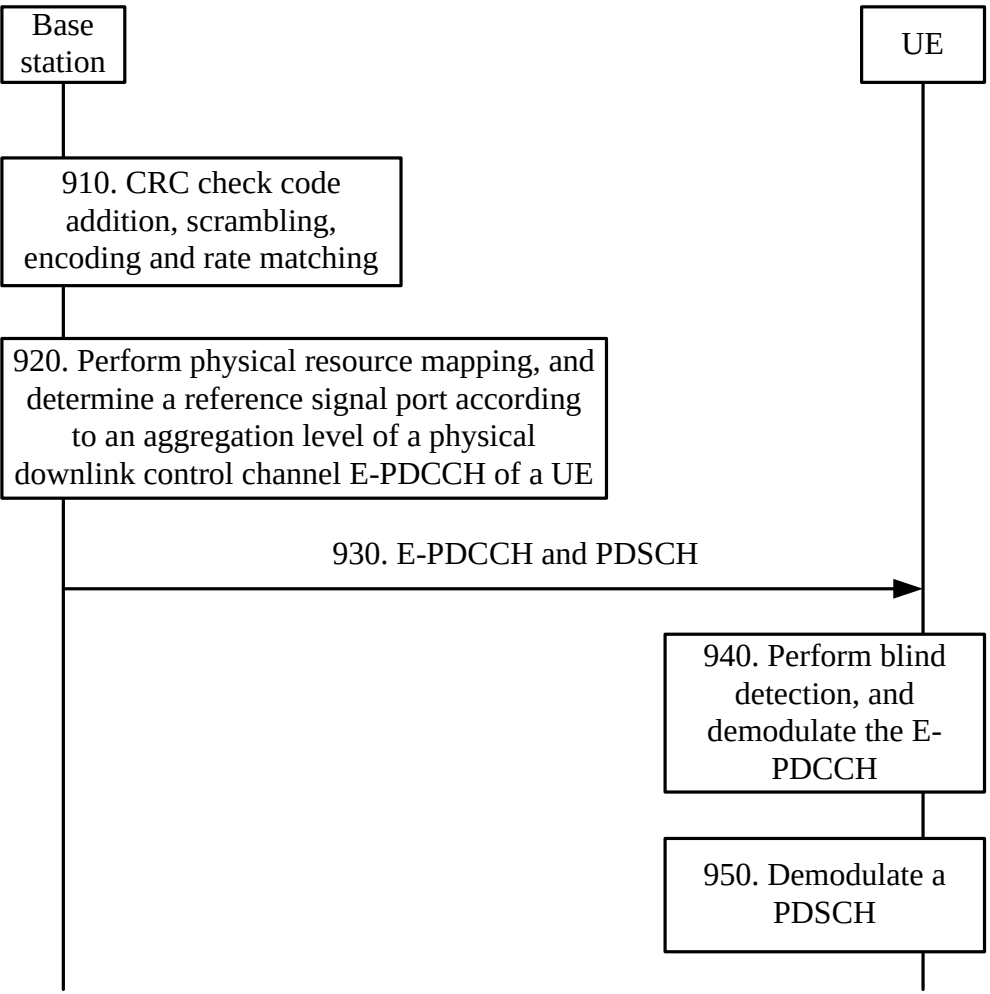


FIG. 9A

7	8	9	10	7	8	9	10
8	8	10	10	8	8	10	10
9	9	9	9	9	9	9	9
10	10	10	10	10	10	10	10
RB0				RB1			
RBG0							

FIG. 9B

7	8	9	10	7	8	9	10	7
8	8	10	10	8	8	10	10	
9	9	9	9	9	9	9	9	
10	10	10	10	10	10	10	10	
RB0			RB1			RB2		
RBG0								

FIG. 9C

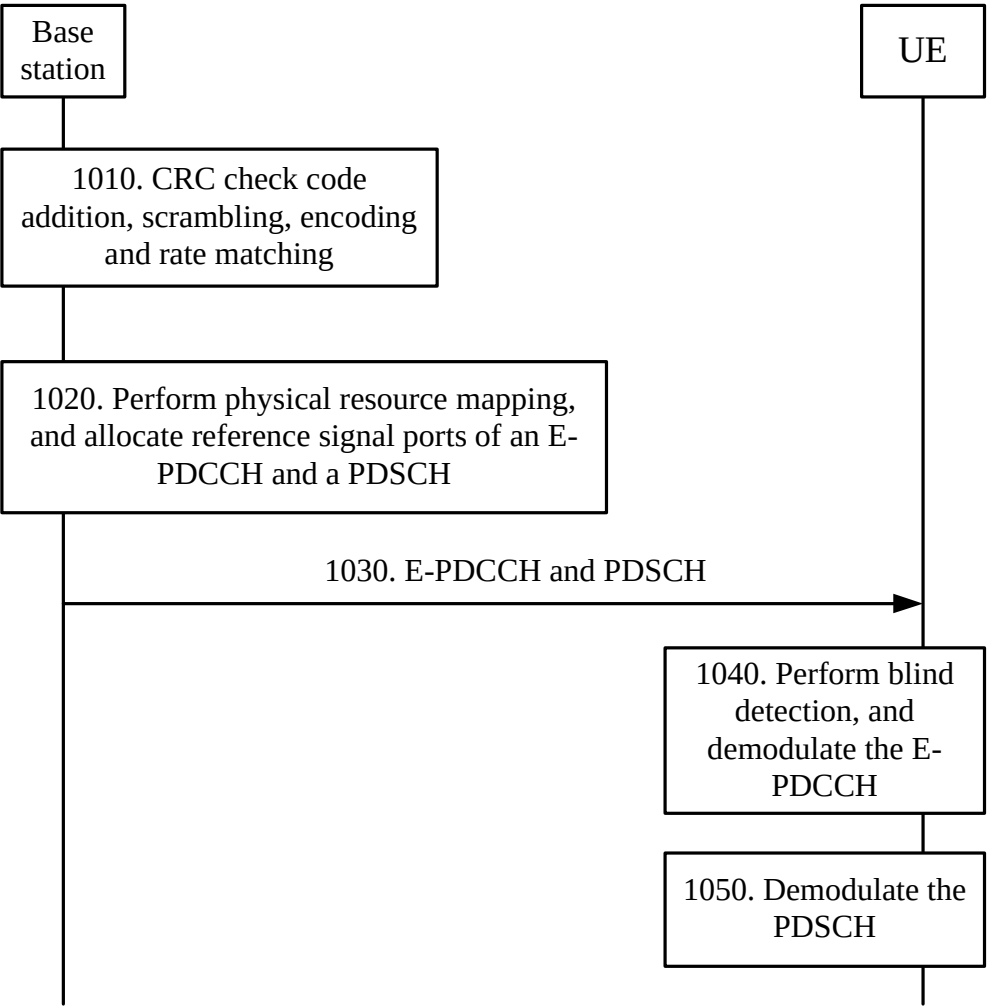


FIG. 10

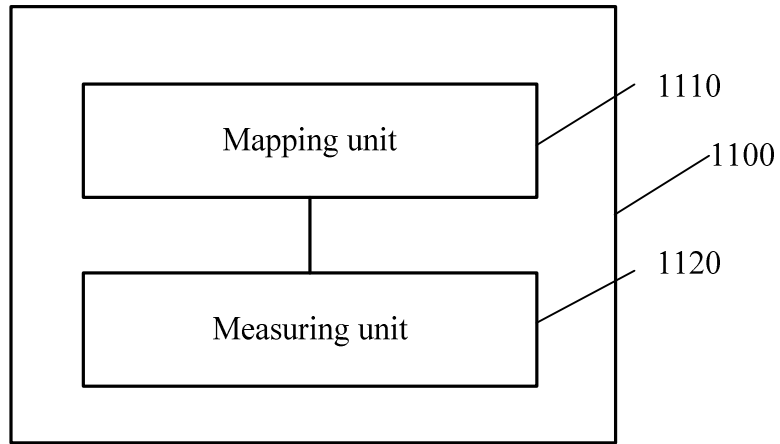


FIG. 11

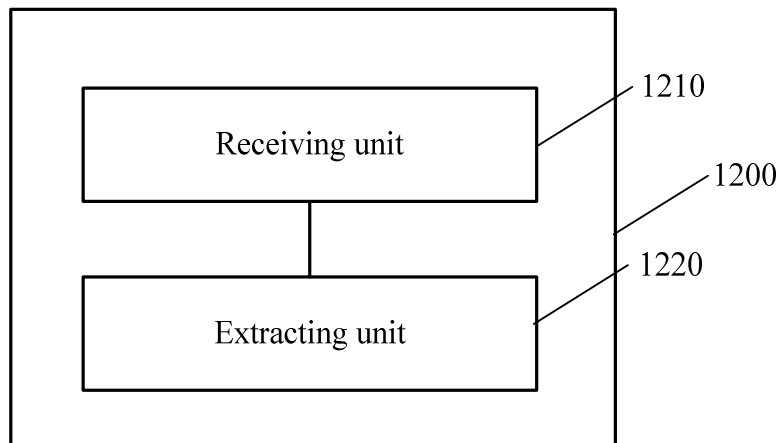


FIG. 12

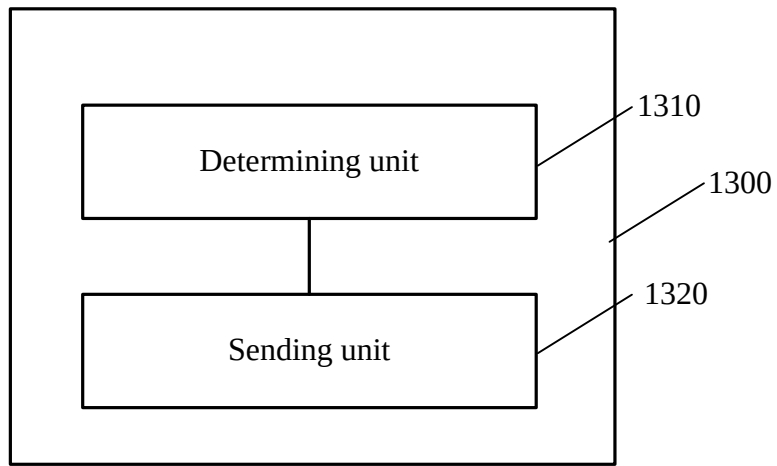


FIG. 13

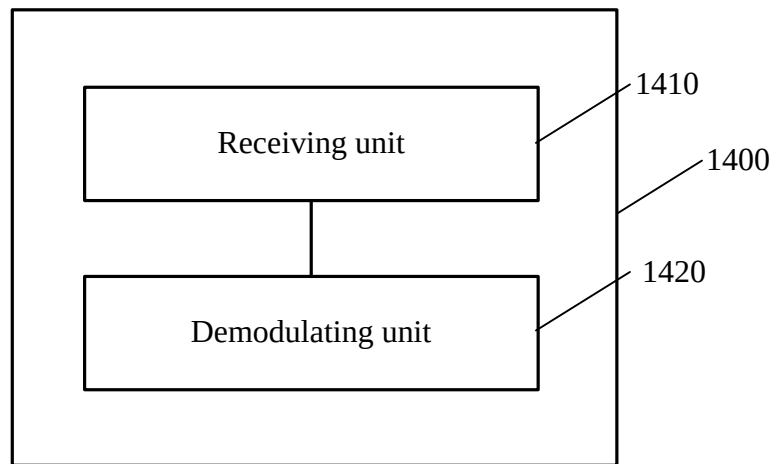


FIG. 14

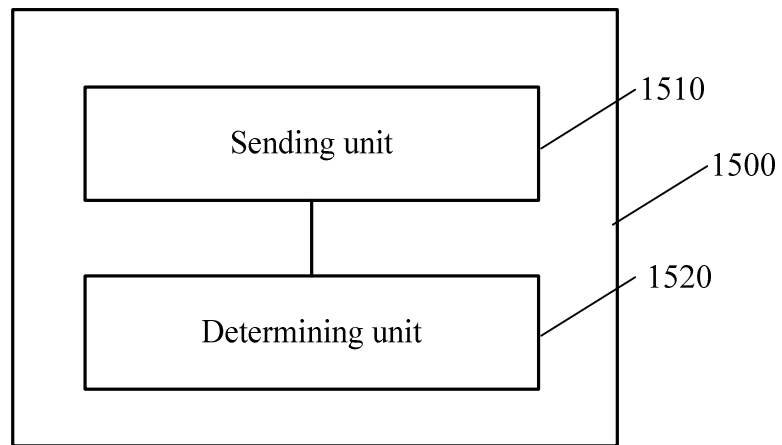


FIG. 15

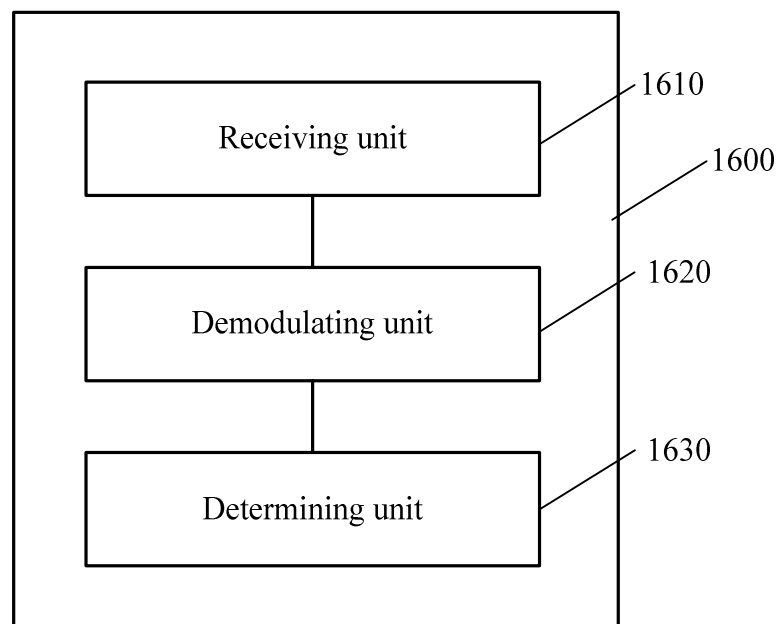
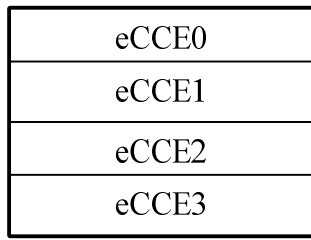
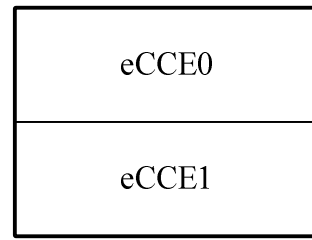


FIG. 16



One physical resource block pair
includes four eCCEs



One physical resource block pair
includes two eCCEs

FIG. 17

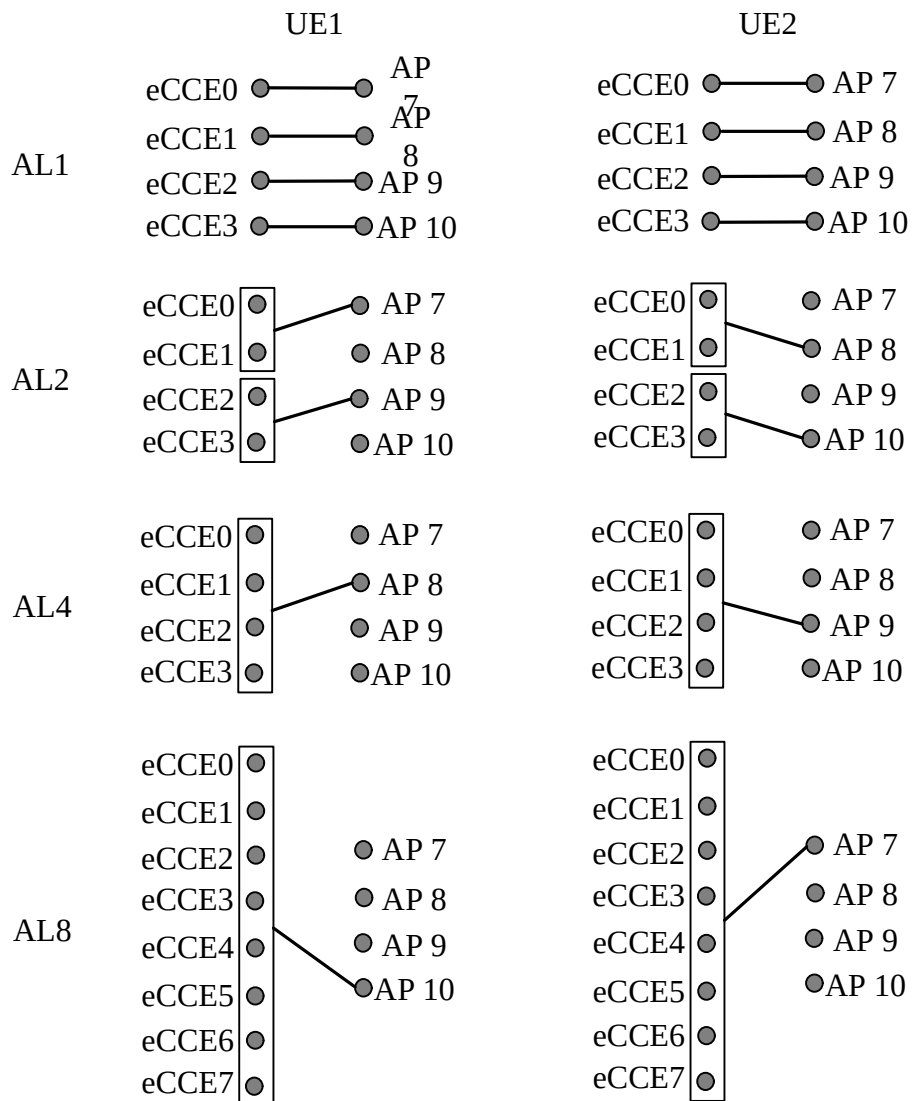


FIG. 18

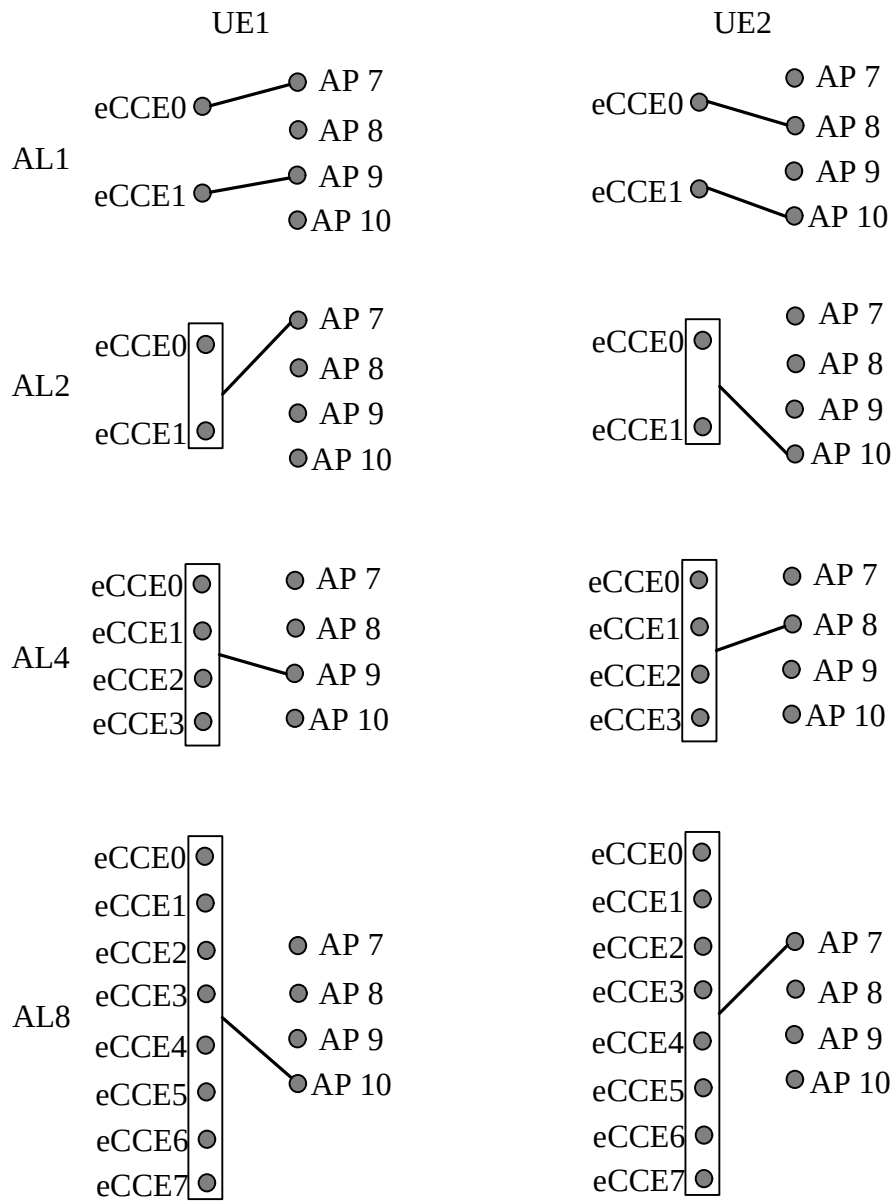


FIG. 19

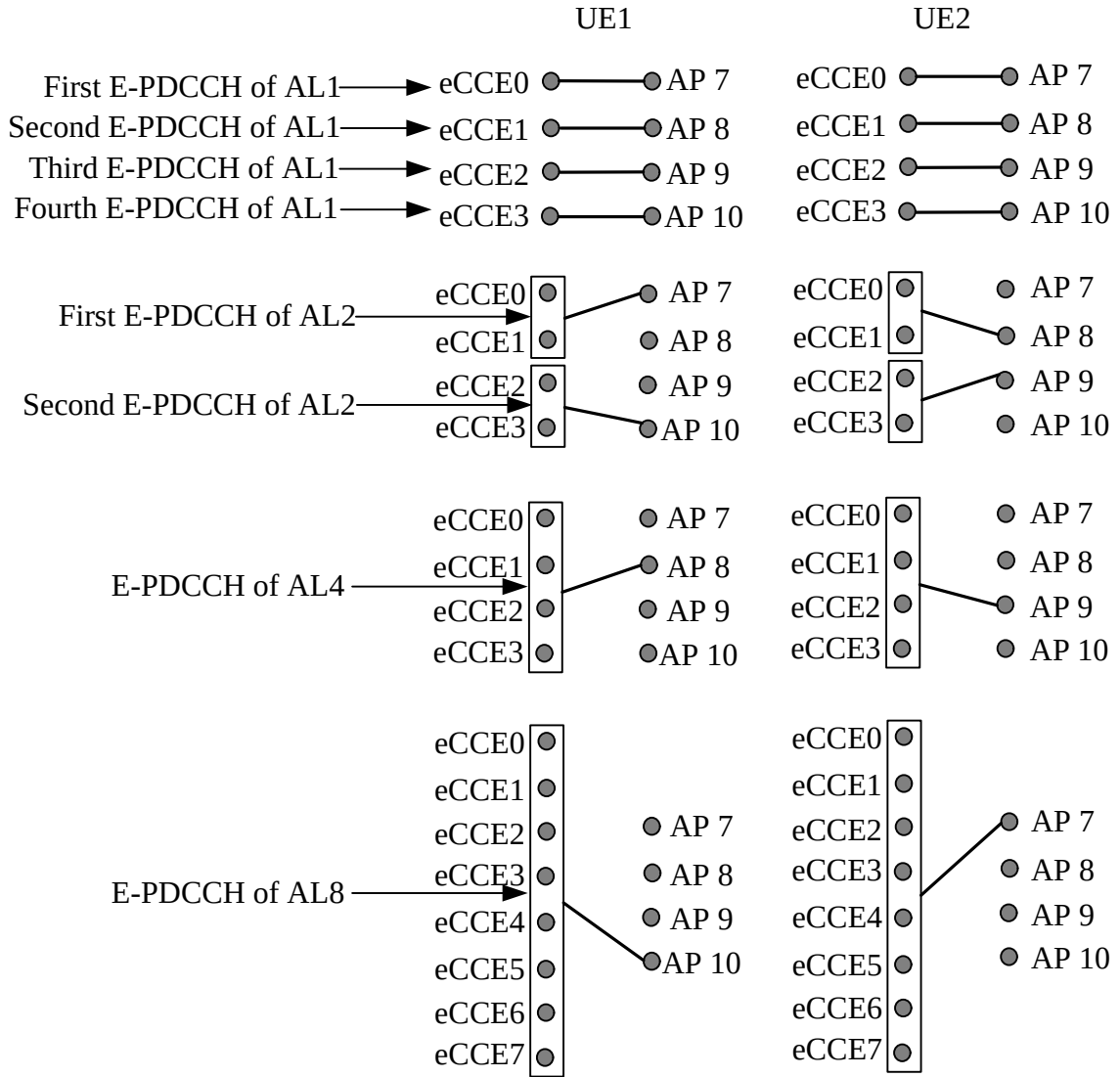


FIG. 20

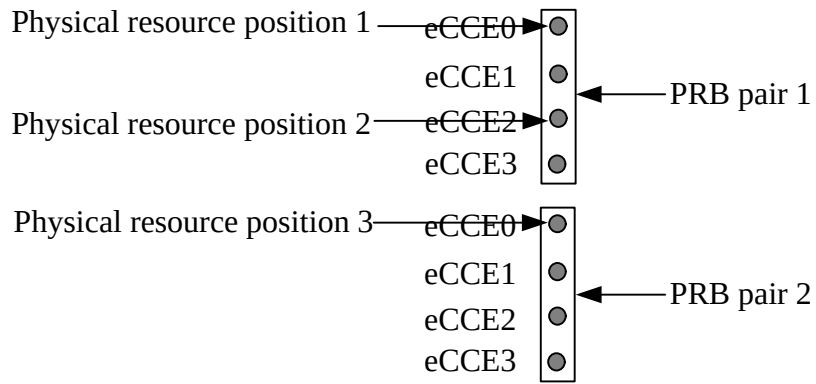


FIG. 21