

ABSTRACT

Embodiments of the present application provide a data receiving circuit, a data receiving system, and a memory device. The data receiving circuit includes: a first amplification module configured to: receive a data signal, a first reference signal, and a second reference signal; and when an enable signal is at a first level, in response to a sampling clock signal and on the basis of a feedback signal, select the data signal and the first reference signal for first comparison and output a first signal pair, or select the data signal and the second reference signal for second comparison and output a second signal pair; and a second amplification module configured to receive output signals of the first amplification module as an input signal pair, perform amplification processing on a voltage difference of the input signal pair, and output a first output signal and a second output signal as a result of the amplification processing. The embodiments of the present application at least facilitate reducing power consumption of the data receiving circuit while improving receiving performance of the data receiving circuit.