

Abstract**APPARATUS AND METHOD FOR DEINTERLEAVING**

The invention relates to an apparatus and method for implementing deinterleaving, in particular for a paging system. The memory elements of RAM are arranged into an array in such a manner that the number of elements in each row corresponds to the number of bits in a code word, and the number of rows corresponds to the number of code words. At a point before the memory, a series-parallel conversion is carried out, and each memory element has a multiplexed input so that the writing in the memory can be carried out row by row horizontally and the reading from the memory can be carried out vertically. If, for example, the memory has 30 x 9 elements, according to the invention 30 addresses are required for the writing and 9 addresses for the reading, i.e. a total of 39 addresses instead of the 270 addresses previously required.

Fig. 1