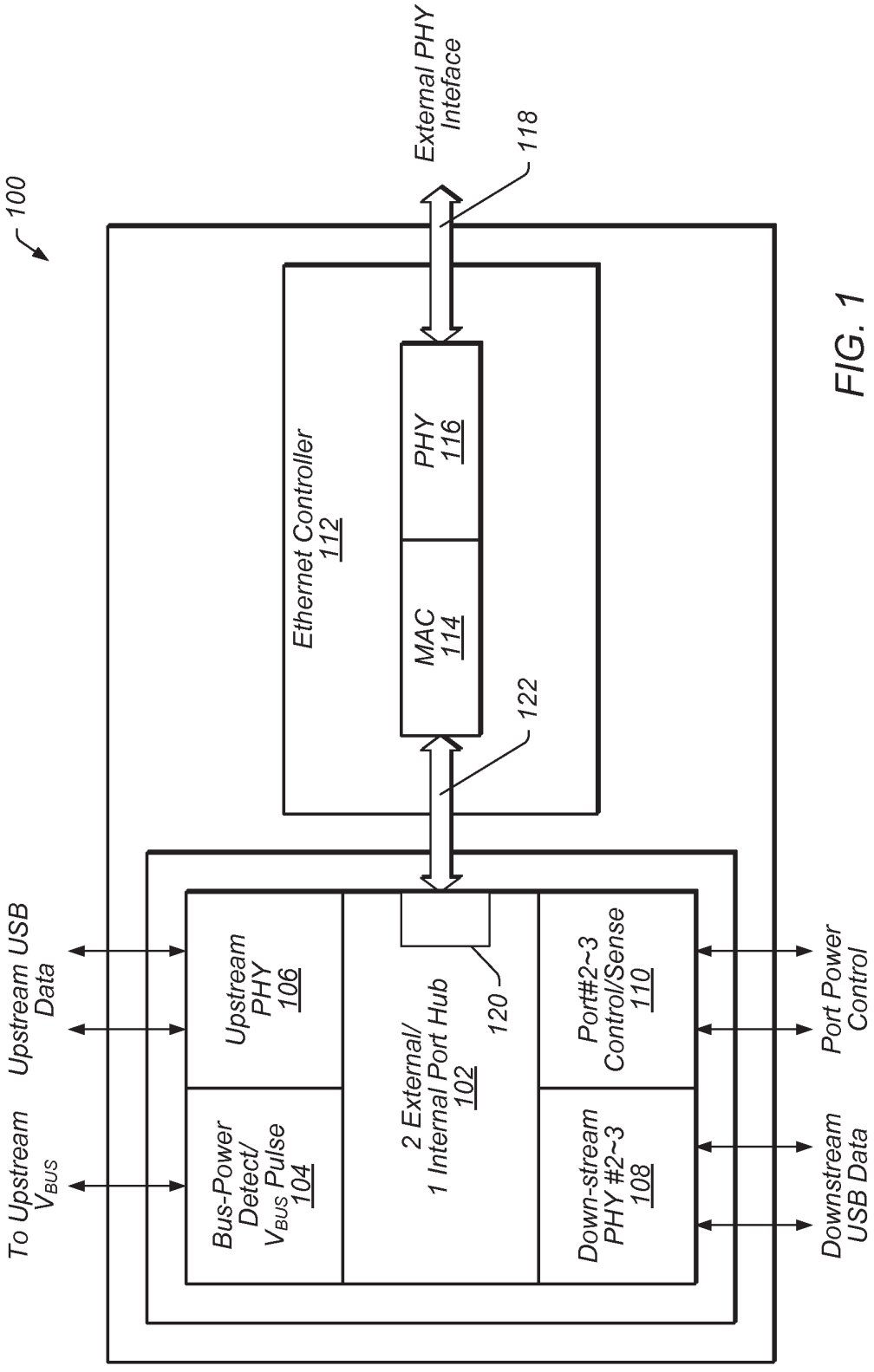

100
External PHY Inteface
118
Ethernet Controller 112
PHY 116
MAC 114
122
2 External/ 1 Internal Port Hub 102
120
Upstream PHY 106
Bus-Power Detect/ V_{BUS} Pulse 104
Port#2~3 Control/Sense 110
Down-stream PHY #2~3 108
Upstream USB Data
To Upstream V_{BUS}
Port Power Control
Downstream USB Data

FIG. 1

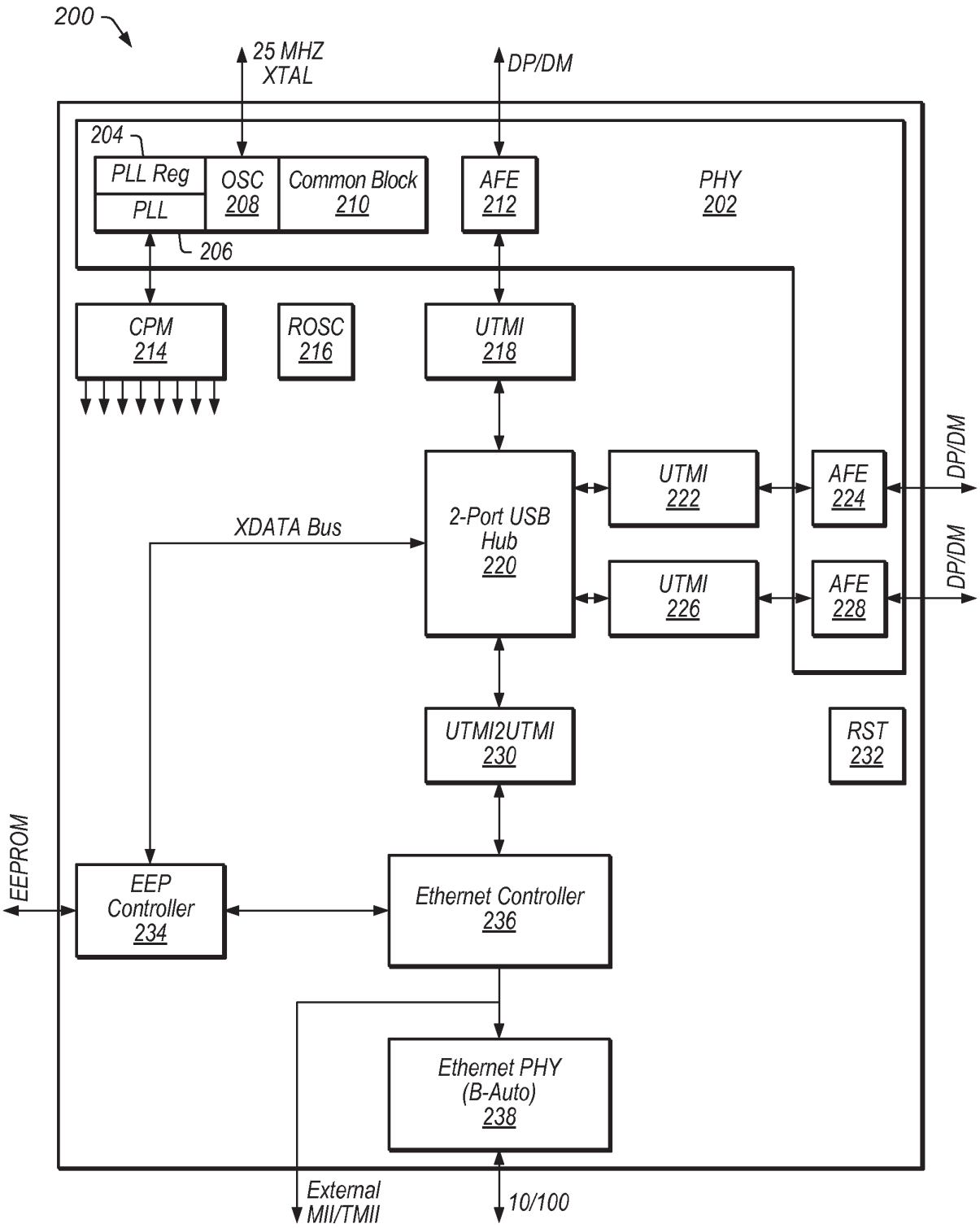

200
25 MHZ XTAL
DP/DM
204
PLL Reg
PLL
OSC 208
Common Block 210
AFE 212
PHY 202
206
CPM 214
ROSC 216
UTMI 218
2-Port USB Hub 220
UTMI 222
AFE 224
DP/DM
XDATA Bus
UTMI 226
AFE 228
DP/DM
UTMI2UTMI 230
RST 232
EEPROM
EEP Controller 234
Ethernet Controller 236
Ethernet PHY (B-Auto) 238
External MII/TMII
10/100

FIG. 2

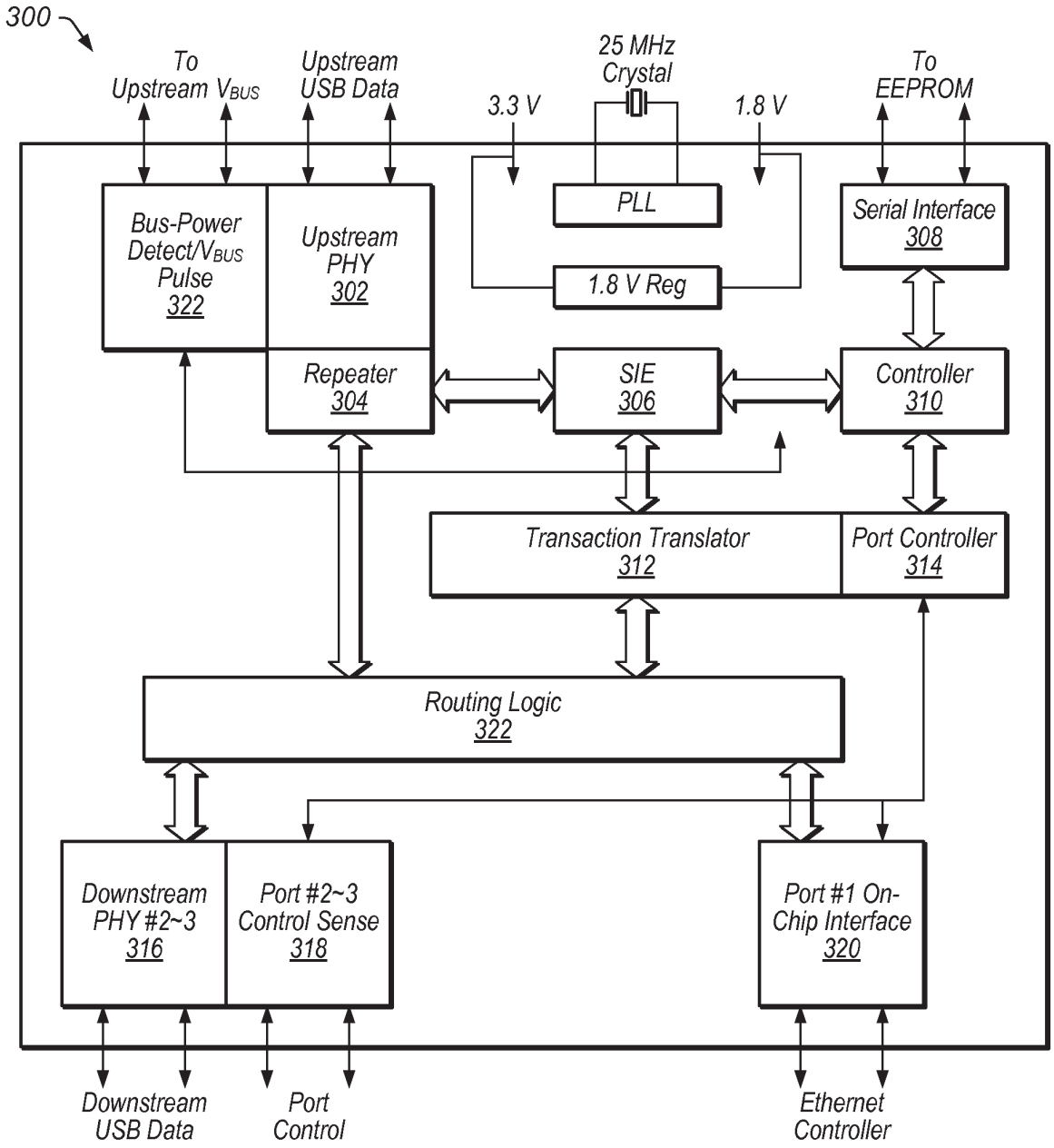

300
To Upstream V_{BUS}
Upstream USB Data
3.3 V
25 MHz Crystal
1.8 V
To EEPROM
Bus-Power Detect/V_{BUS} Pulse 322
Upstream PHY 302
PLL
1.8 V Reg
Serial Interface 308
Repeater 304
SIE 306
Controller 310
Transaction Translator 312
Port Controller 314
Routing Logic 322
Downstream PHY #2~3 316
Port #2~3 Control Sense 318
Port #1 On-Chip Interface 320
Downstream USB Data
Port Control
Ethernet Controller

FIG. 3