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ABSTRACT OF THE DISCLOSURE:

An address for a character generator ROM is composed of an upper readout address portion which is a character code loaded from a text memory for storing texts edited by a microcomputer and a lower readout address portion which is a value of a counter. Output data from the character generator ROM accessed by the above addresses are loaded in parallel by left-shift and right-shift registers. The output data is serialized by the left-shift register to form character pattern data, which in turn is superimposed on a video picture signal to be outputted to a VTR portion. In response to a self-image picture taking mode signal, one output is selected by a selector from the output of the left-shift register and the output of the right-shift register. The thus selected output is superimposed over the video picture signal to be delivered to a monitor screen driver circuit. The monitor screen driver circuit horizontally inverts the scanning direction in order to display a mirror-image of the picture on the monitor screen when the video camera is operated in the self-image picture taking state.

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