

図 1

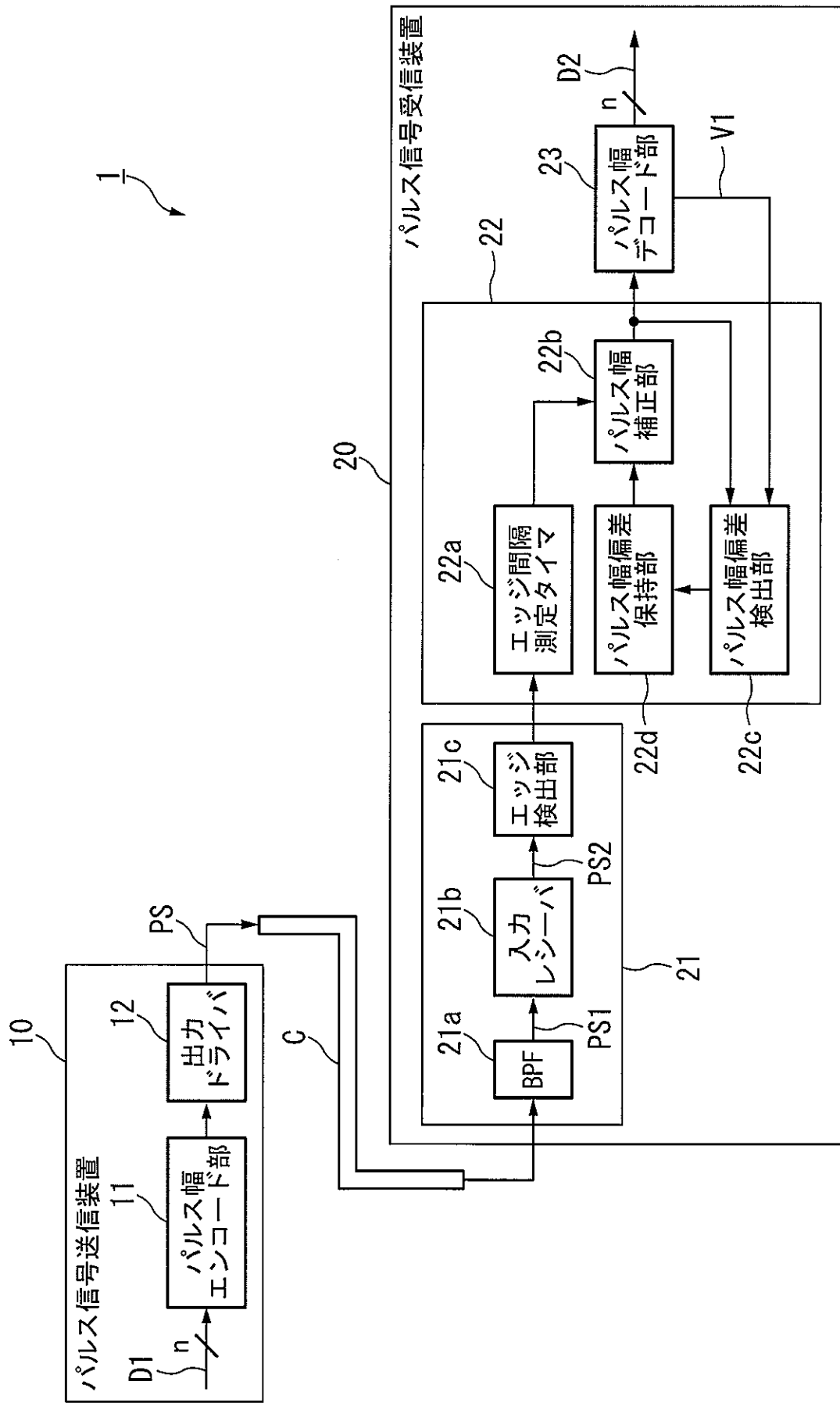


図 2

デジタル信号D1	パルス幅基準値[nsec]
00	600
01	700
10	800
11	900

図 3

	パルス幅W[nsec]	デジタル信号D2
R1 {	$550 \leq W < 650$	00
R2 {	$650 \leq W < 750$	01
R3 {	$750 \leq W < 850$	10
R4 {	$850 \leq W < 950$	11
	$W < 550, 950 \leq W$	ビットエラー

図 4

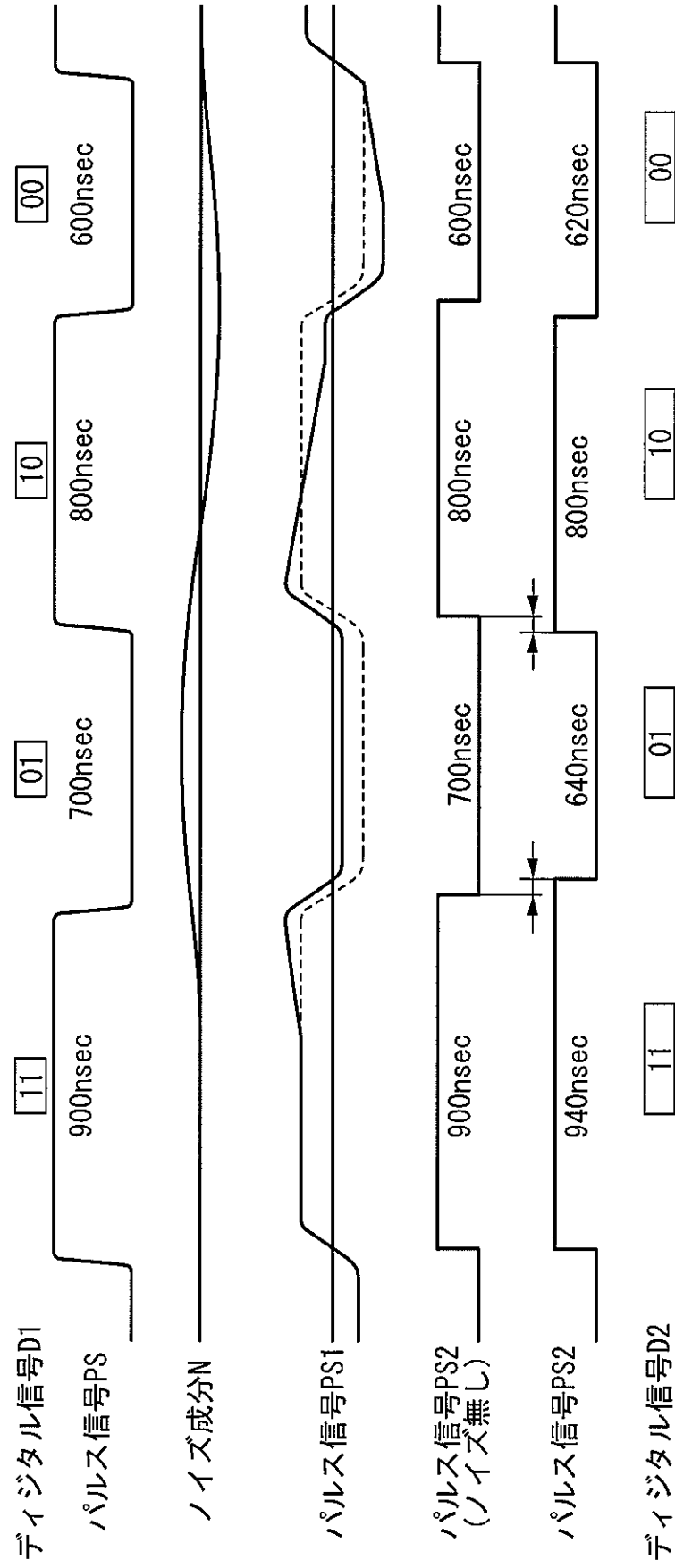


図 5

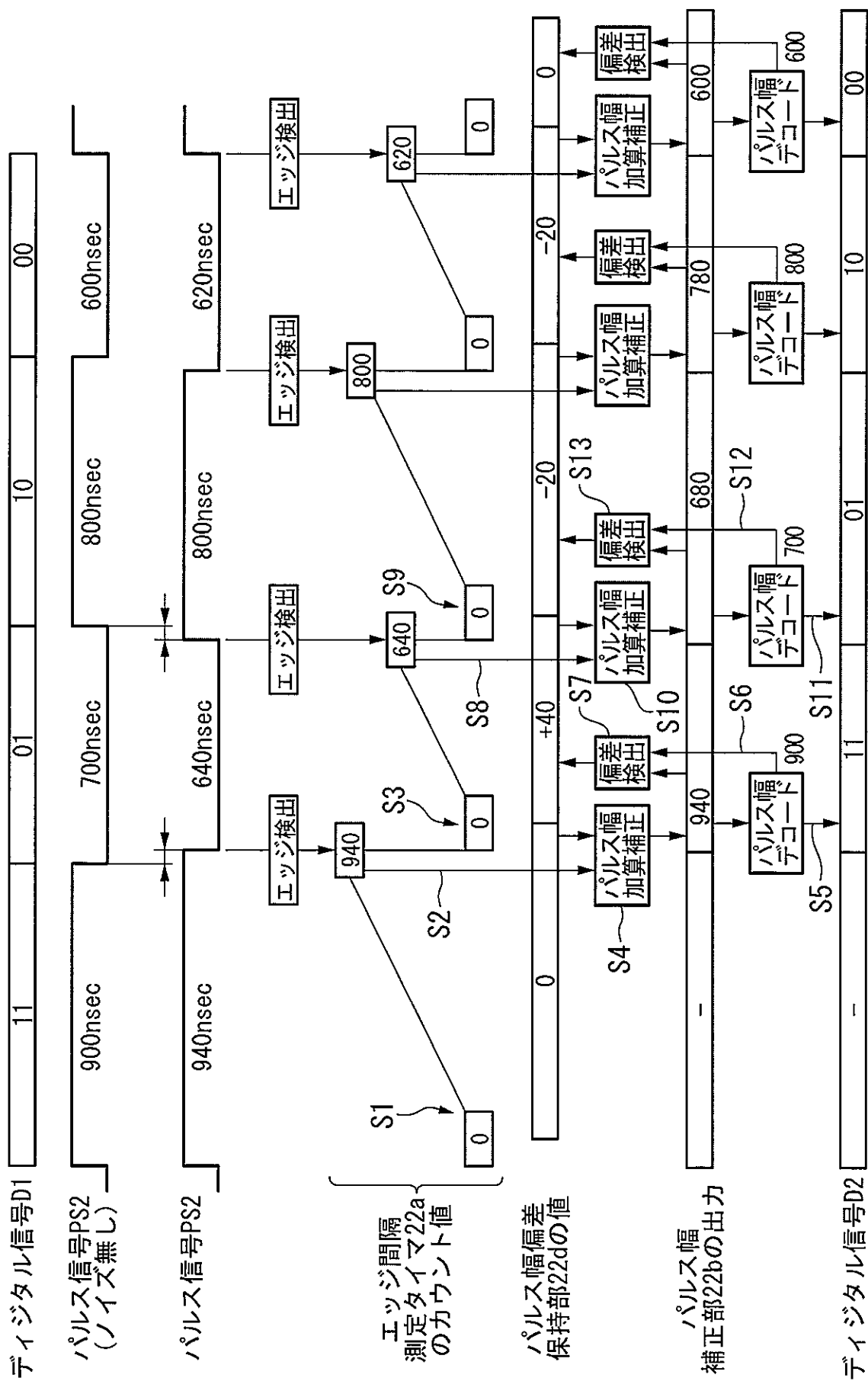


図6

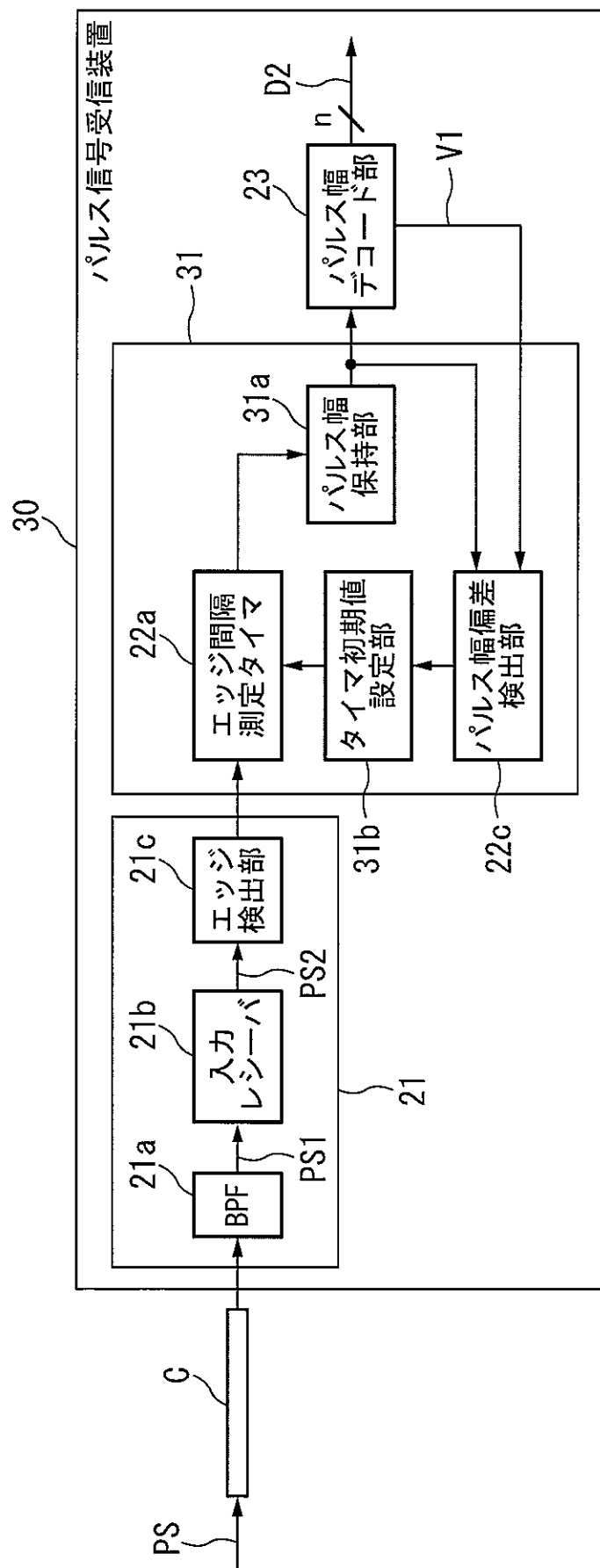


図 7

