

ABSTRACT OF THE DISCLOSURE

A synchronous rectifier type switching converter includes: a synchronous rectification circuit connected to a secondary side of a transformer; and a control circuit that performs ON-OFF controls of a primary side switching circuit and the synchronous rectification circuit at each sampling timing. The control circuit includes: a gate blocking means that, through comparison of a detection value of a reactor current detection sensor with a predetermined threshold value, outputs a gate blocking signal to stop operation of the synchronous rectification circuit when the detection value becomes not greater than the threshold value; and a correction means that, upon detecting that decrease of the reactor current is not less than a fixed level based on the detection value of the detection sensor, corrects either a present time sampling detection value of the detection sensor or the predetermined threshold value, referring to history of the detection values of the detection sensor when the gate blocking means compares the present time sampling detection value of the detection sensor with the predetermined threshold value.