

PROGRAM EXECUTION METHOD AND PROGRAM EXECUTION DEVICE
ABSTRACT OF THE DISCLOSURE

Disclosed is a program execution device capable of carrying out high-speed computation with a low amount of hardware and small program size, while permitting future expansion of the range of functions. General-purpose registers R0 ~ R4 are virtual registers and a specific computation is related to read/write processing to and from each of these registers R0 ~ R4. When instruction decoder 3 has designed an instruction, if it has been determined that the instruction is a data transfer instruction denoting access to any one of the general-purpose registers R0 ~ 4, the computation related to the register to be accessed is carried out. A expanded range of computation types can be related to different general-purpose registers.

FIGURE 1