

TITLE:
PIXEL CIRCUIT

ABSTRACT

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A pixel circuit (100) includes a first connection point (PAD1), a second connection point (PAD2), a first transistor (T1), a second transistor (T2), a third transistor (T3), a fourth transistor (T4), a fifth transistor (T5), a sixth transistor (T6), a seventh transistor (T7), an eighth transistor (T8), a ninth transistor (T9), and a first capacitor (capacitor). A first control signal (S1), a second control signal (S2), an emission control signal (EM1), a system high voltage (VDD), a system low voltage (VSS), a first reference voltage (DC_1), and a second reference voltage (DC_2) received by first transistor, second transistor, third transistor, fourth transistor, fifth transistor, sixth transistor, seventh transistor, the eighth transistor, and the ninth transistor are set to electrically test the first connection point, the second connection point, the first transistor, the second transistor, the third transistor, the fifth transistor, the sixth transistor, the seventh transistor, and the first capacitor.

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FIG. 1