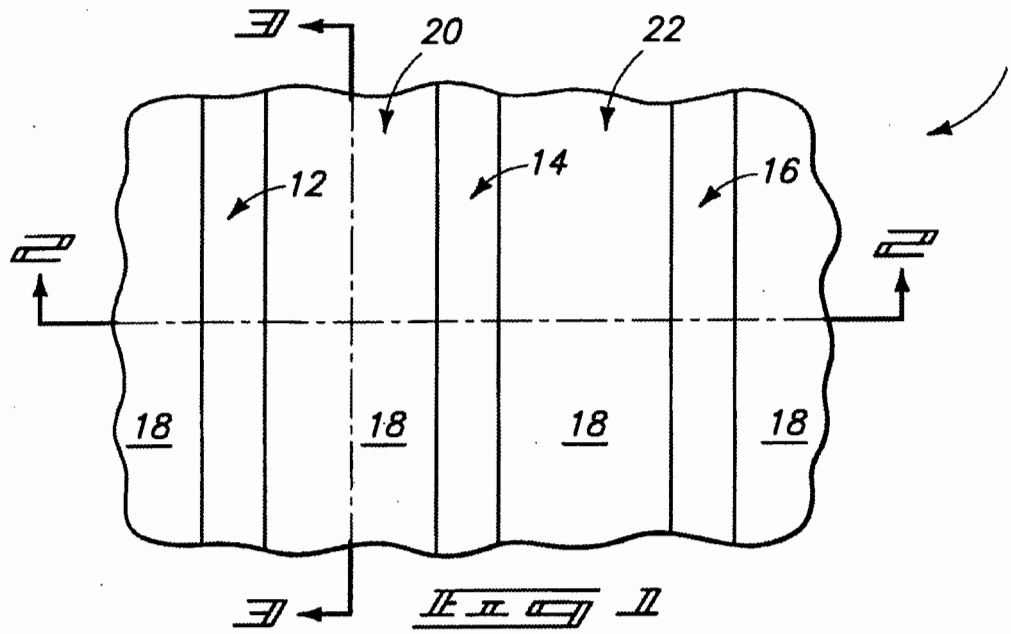

3
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22
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14
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2
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Fig 1

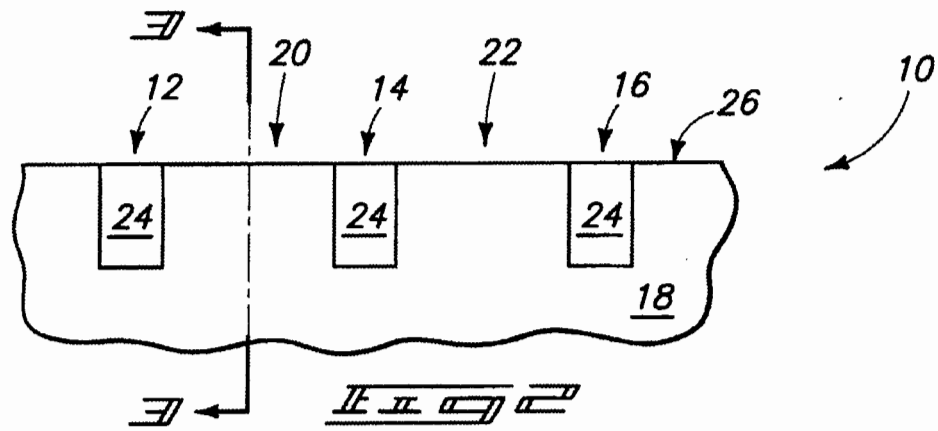
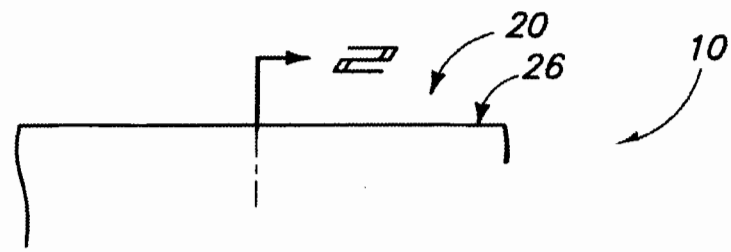

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Fig 2


20
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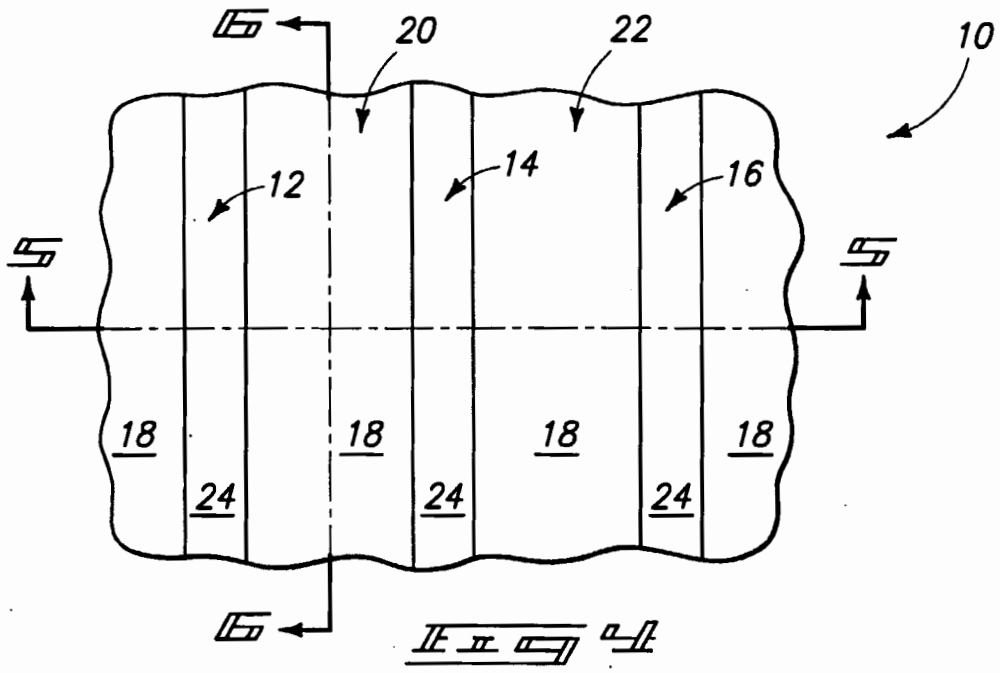


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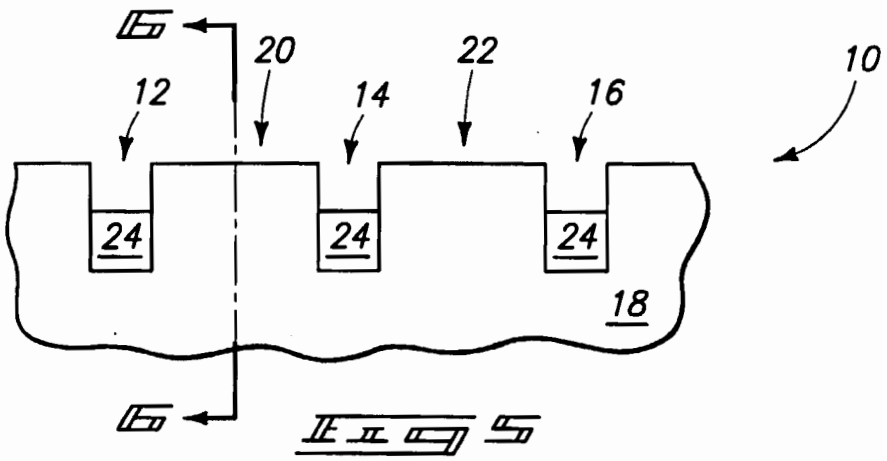


Fig 5

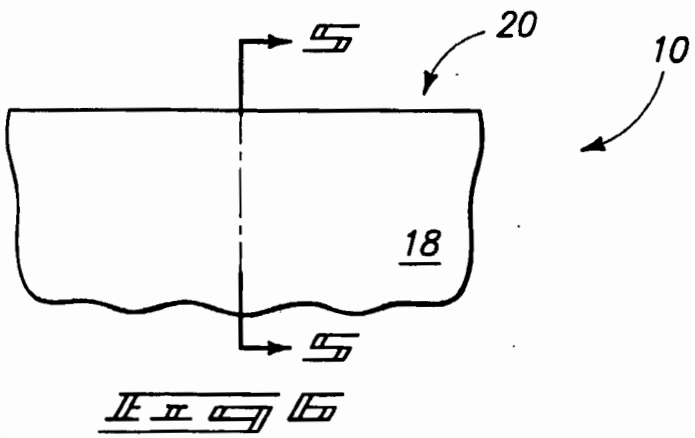


Fig 6

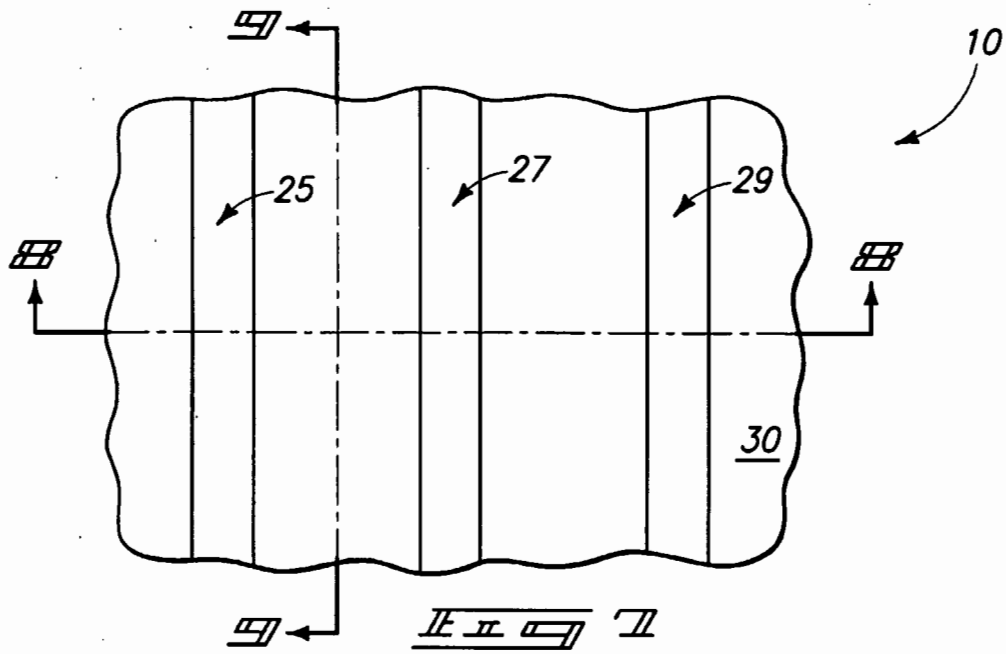


Fig 11

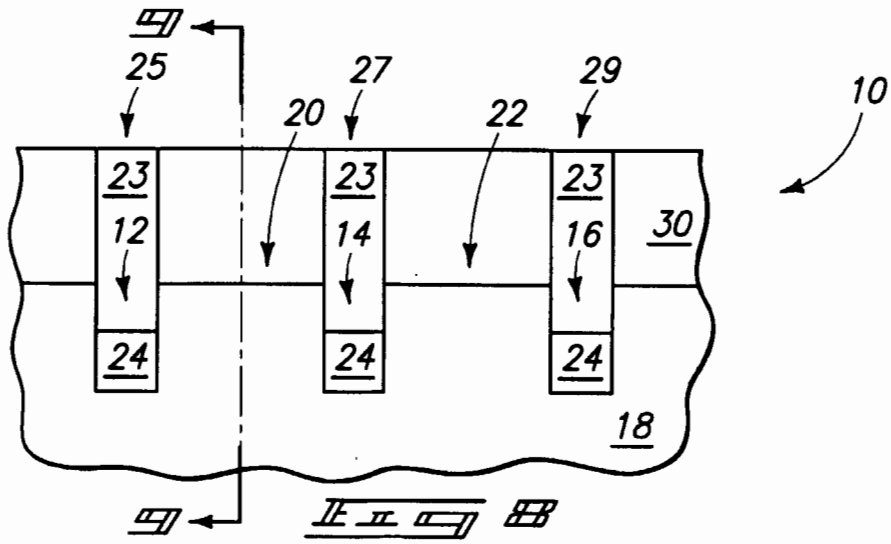


Fig 8

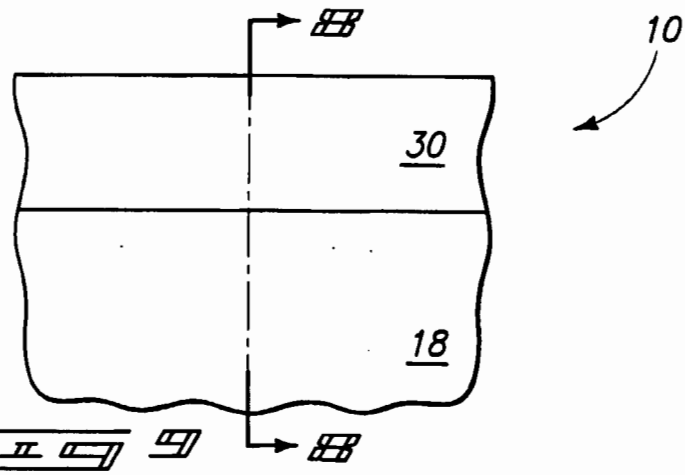


Fig 9

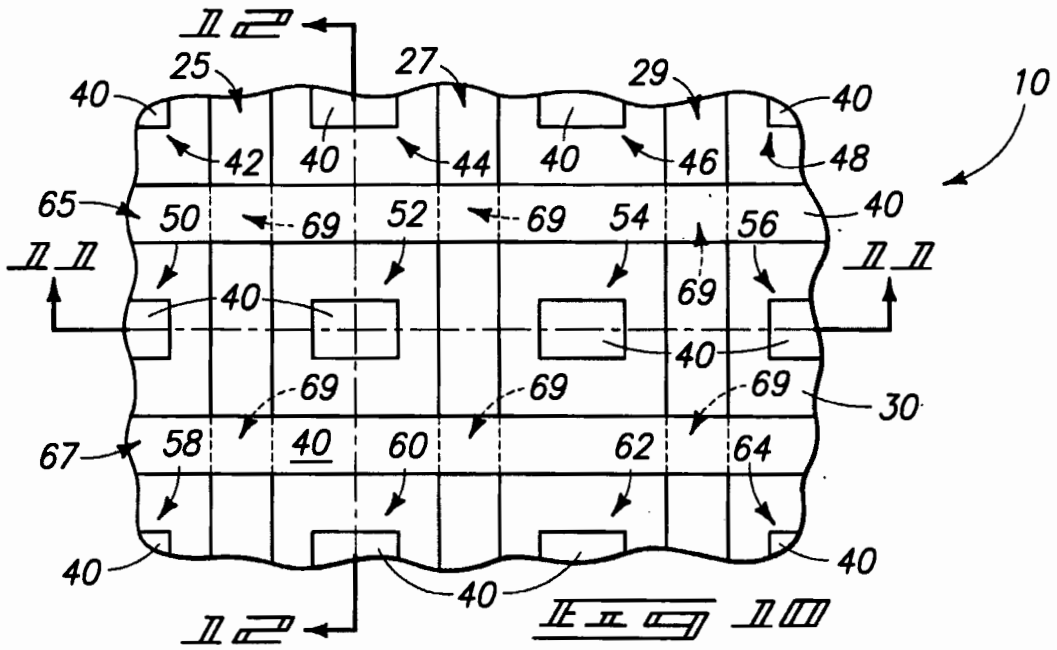


Fig 1

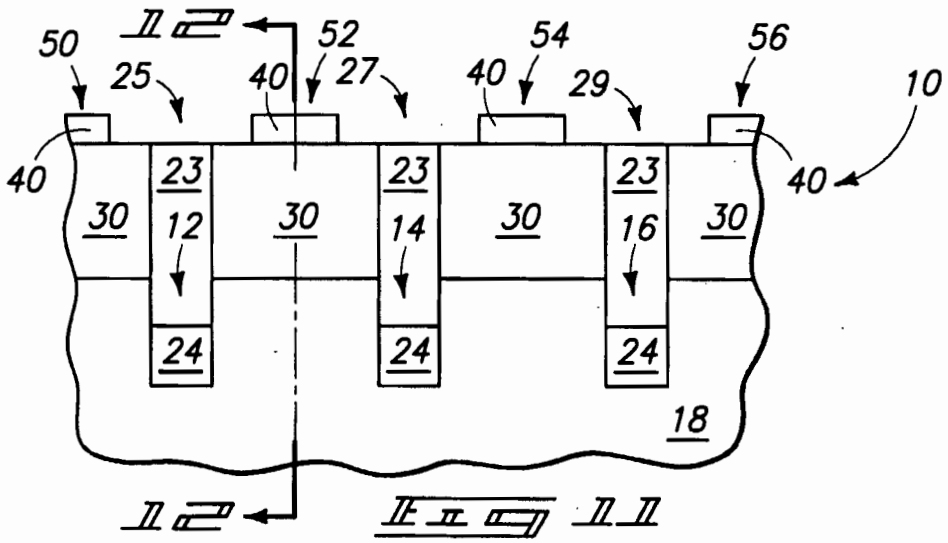


Fig 2

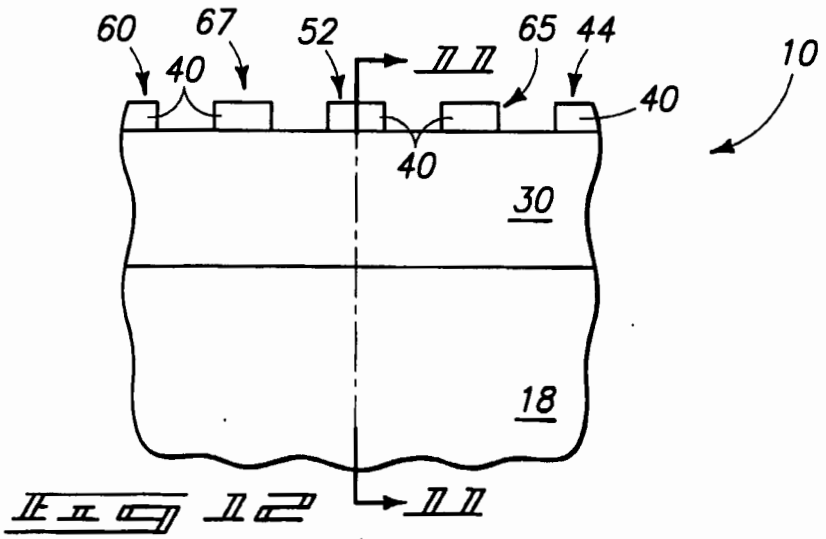
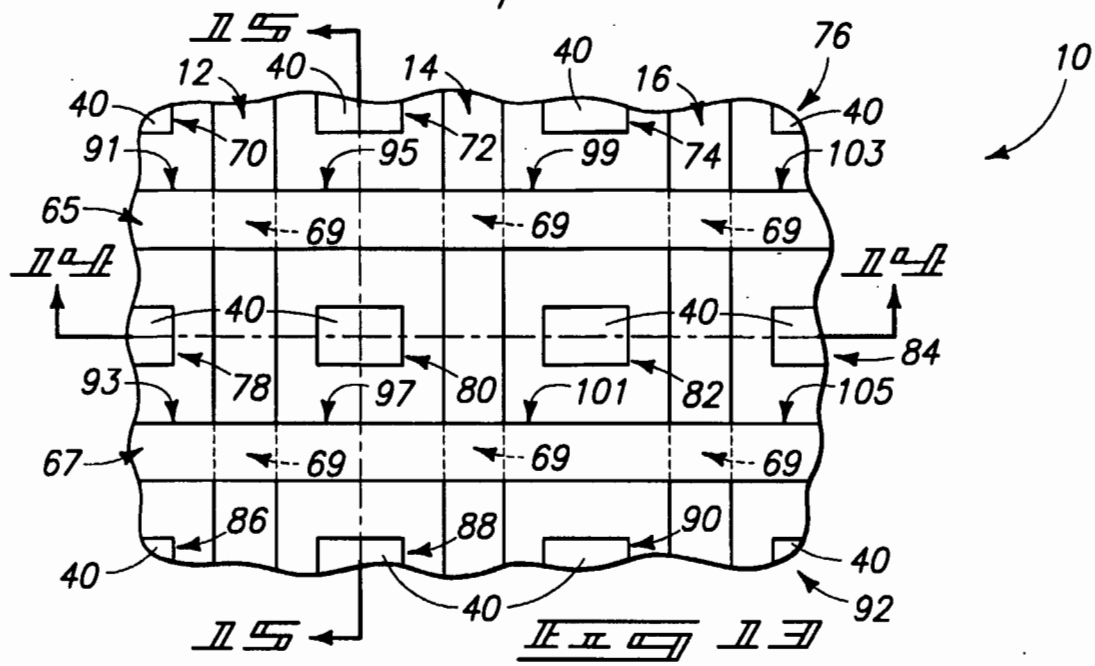
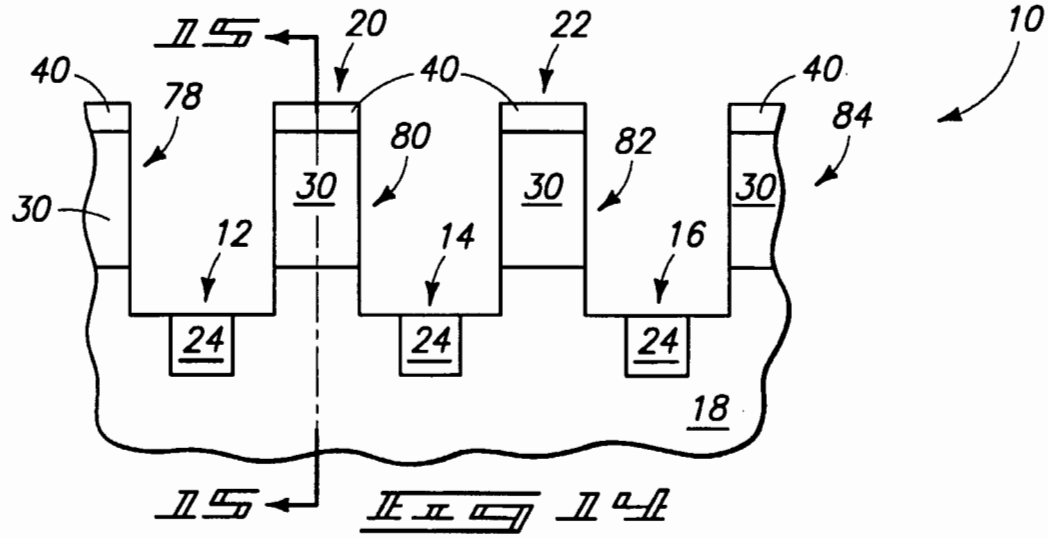
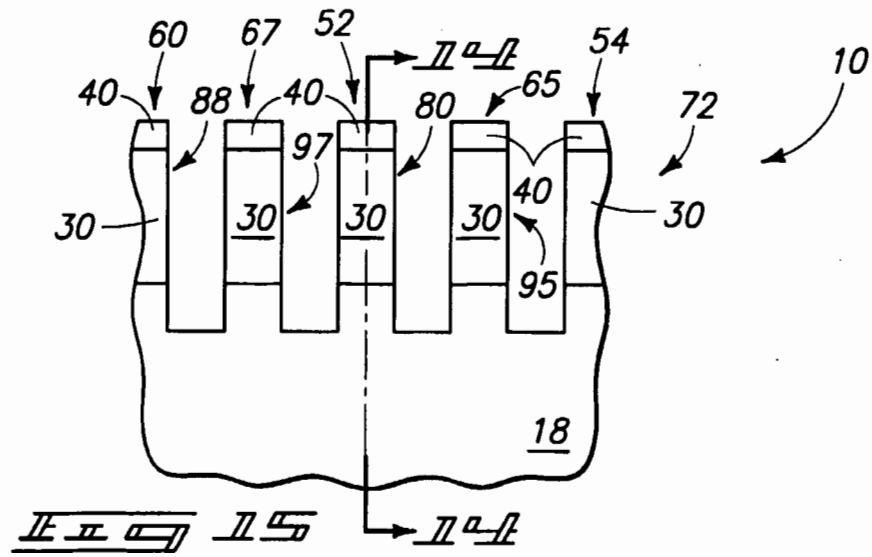


Fig 3


II
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II
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II a

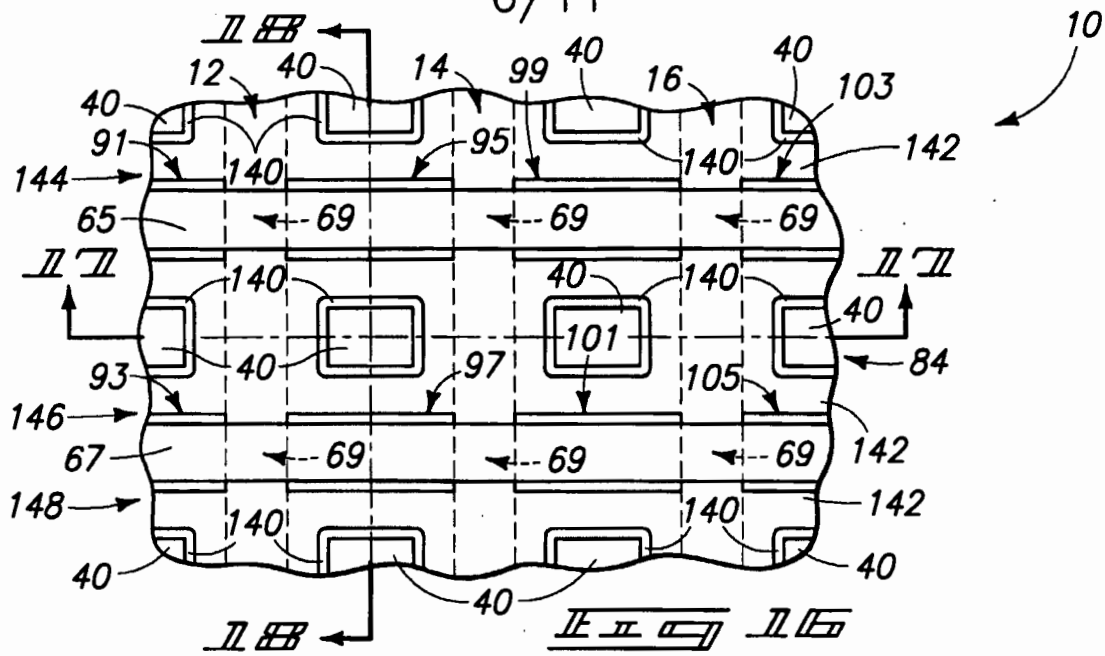


Fig 16

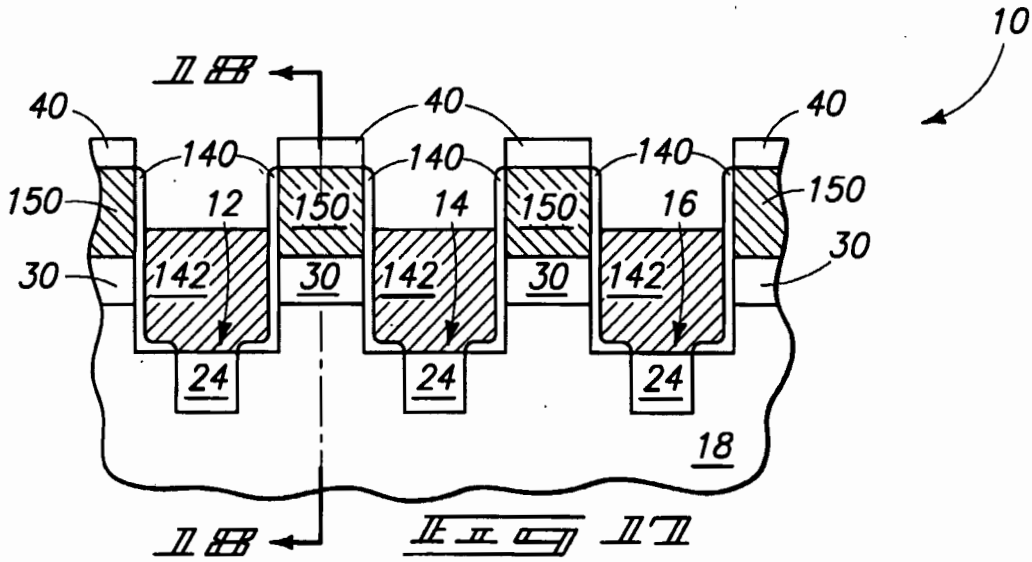


Fig 17

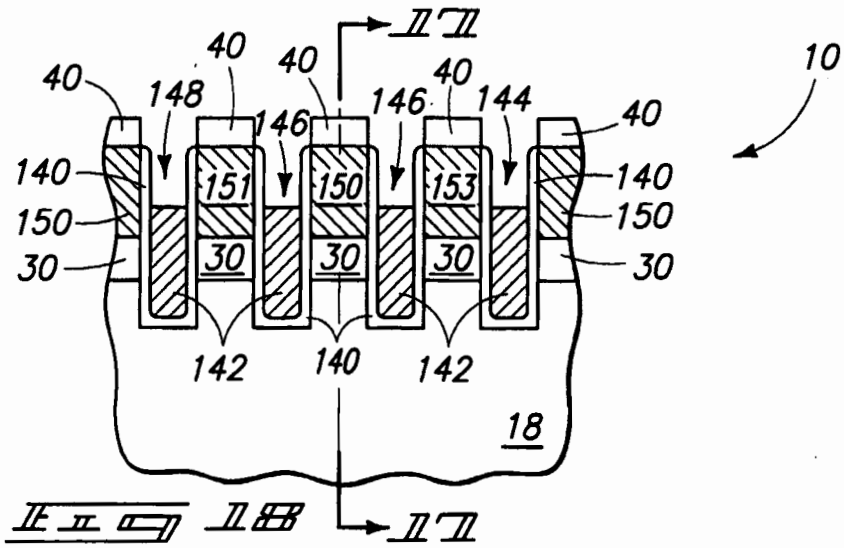


Fig 18

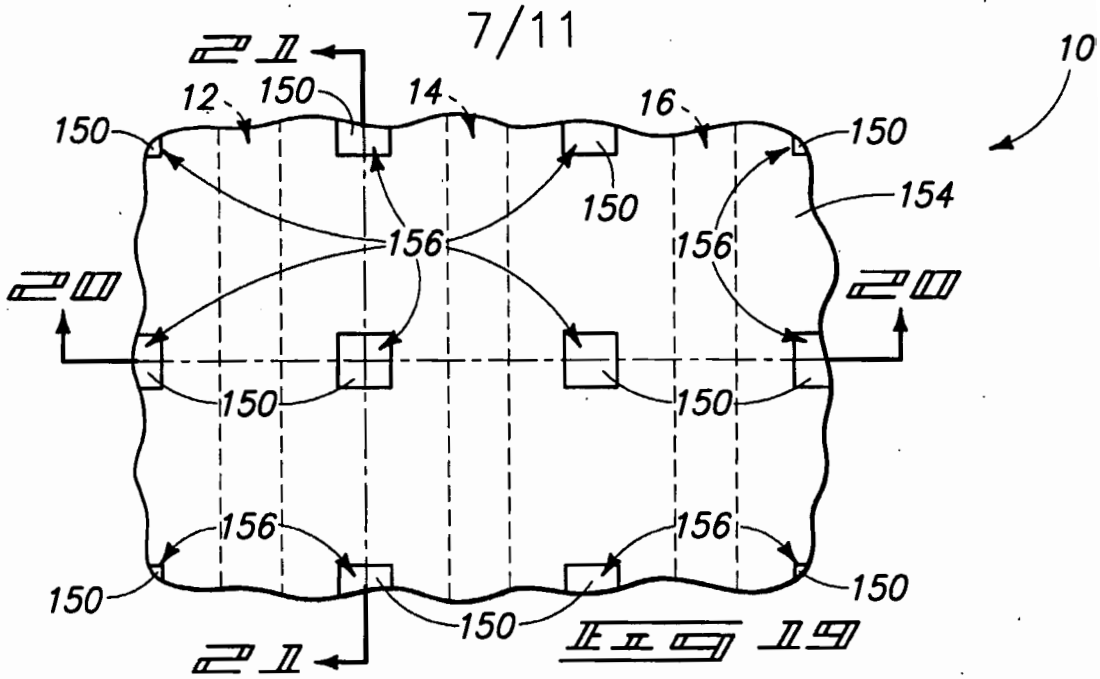


Fig 19

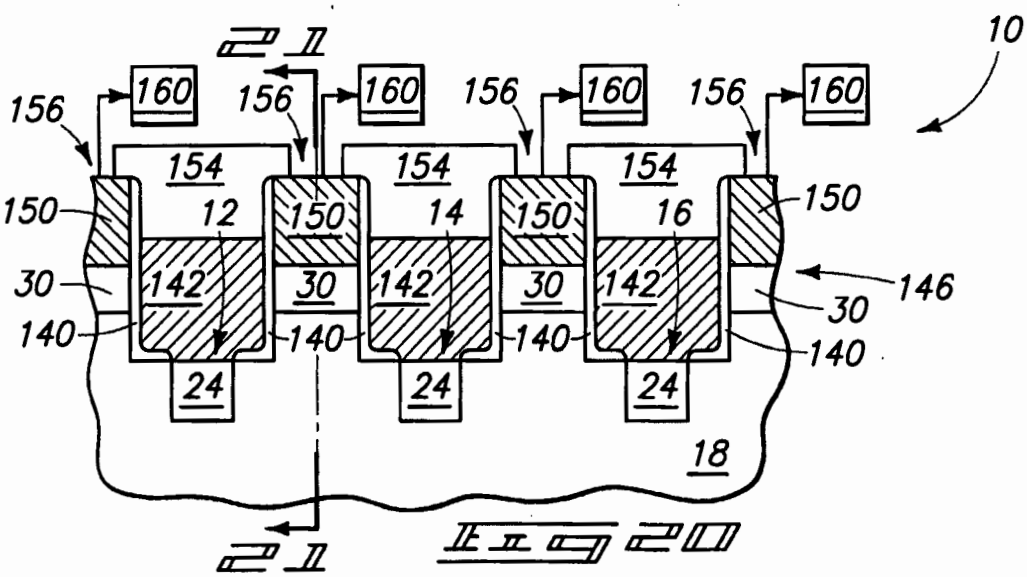


Fig 20

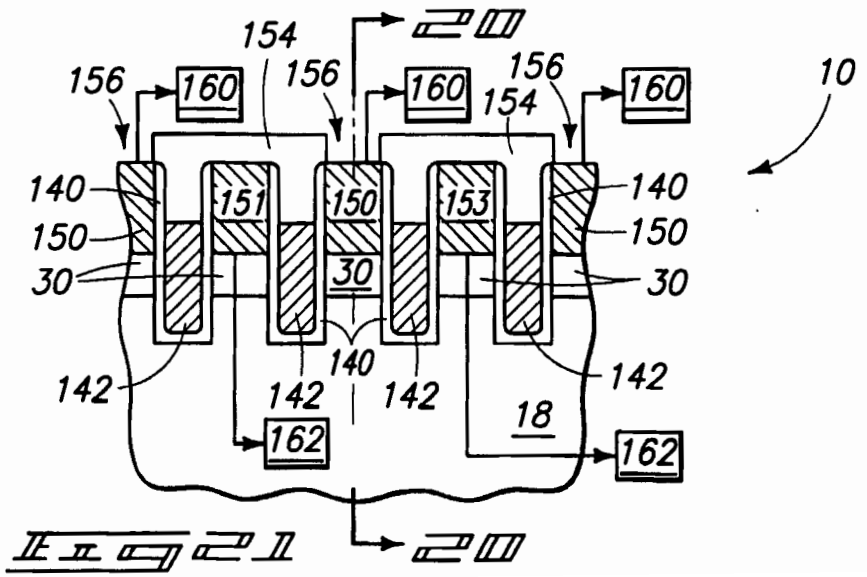


Fig 21

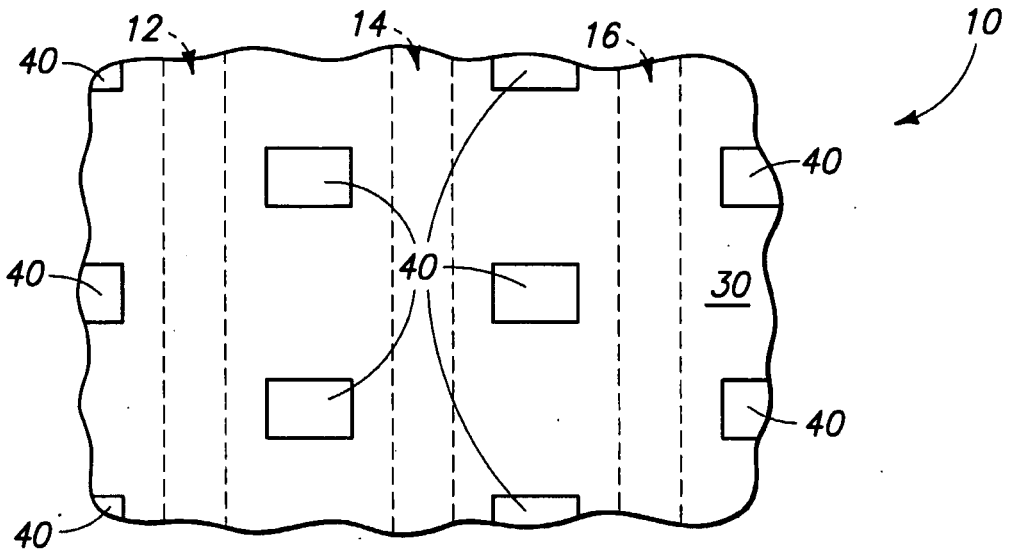


Fig 22

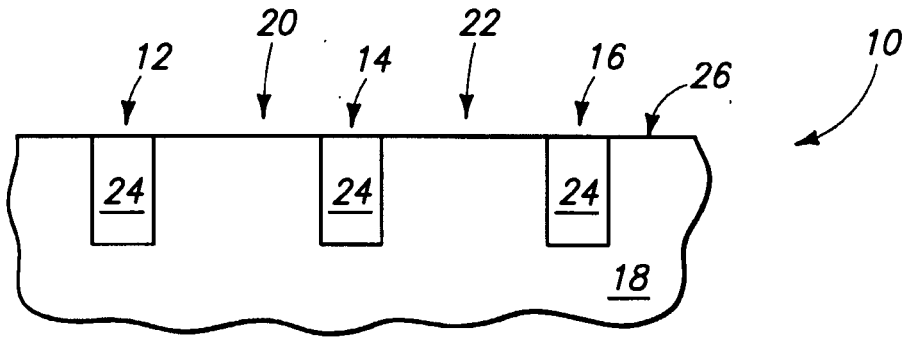


Fig 23

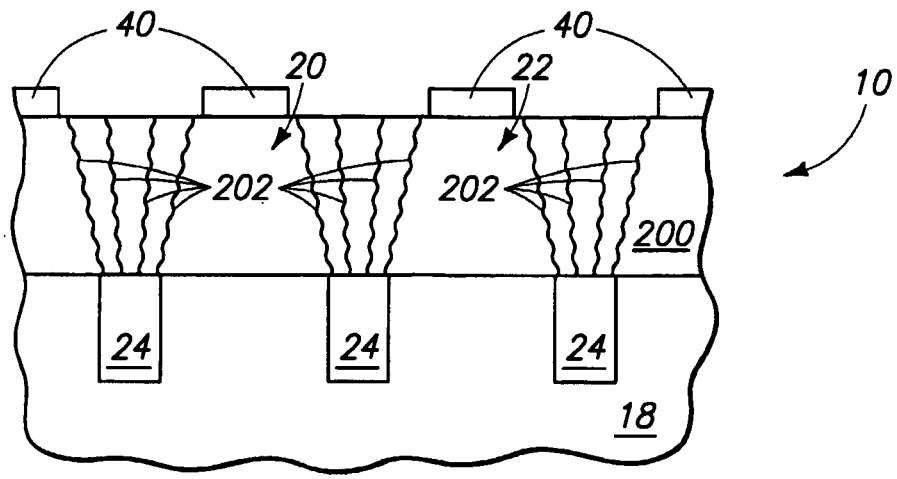


Fig 24

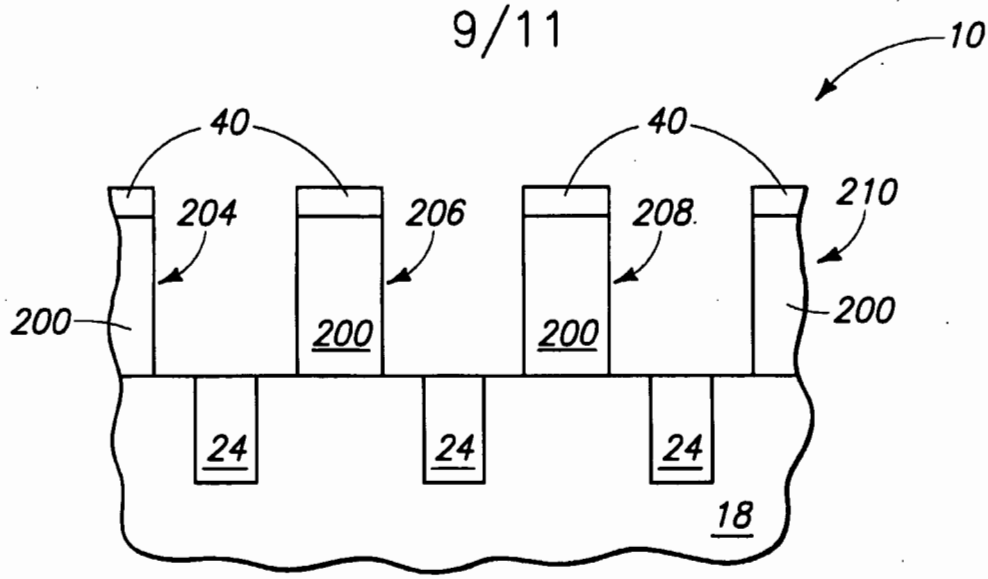


Fig 25

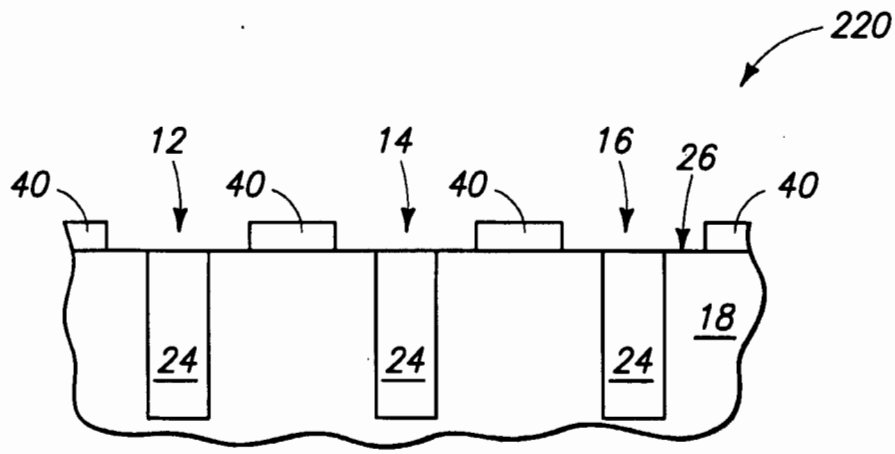


Fig 26

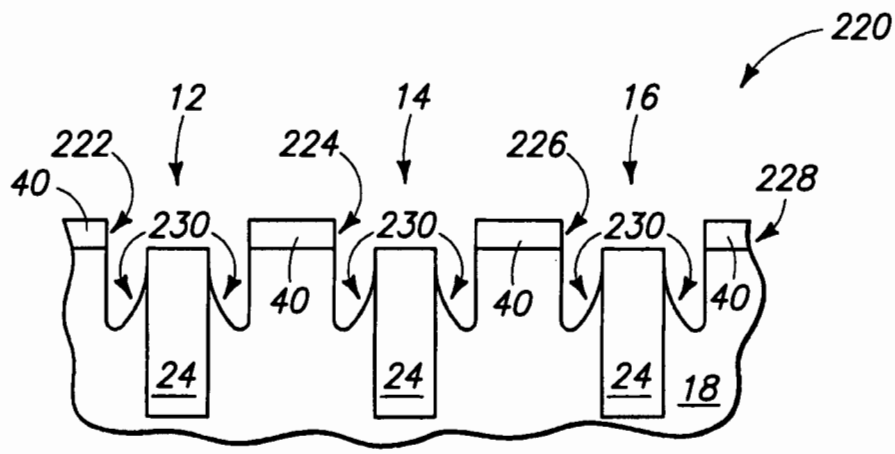


Fig 27

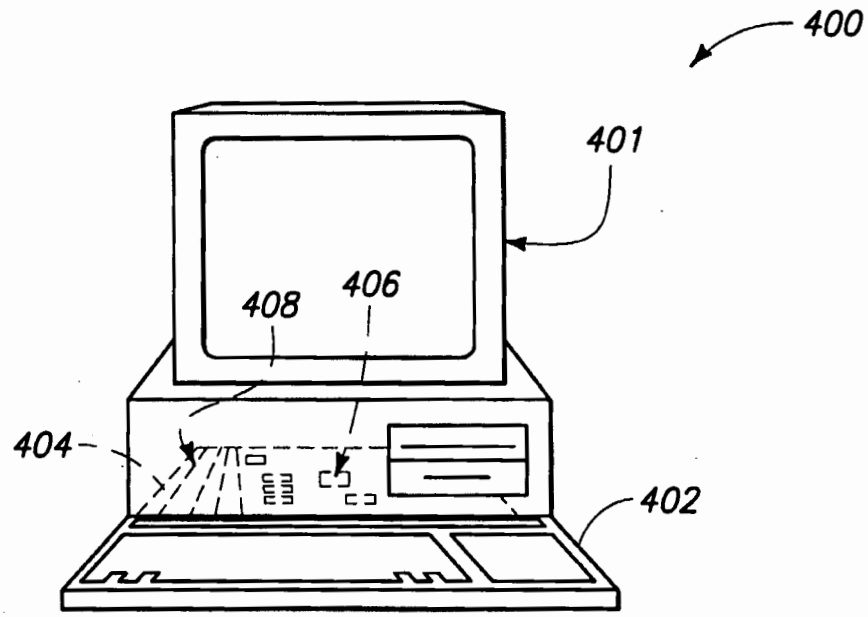


Fig 28

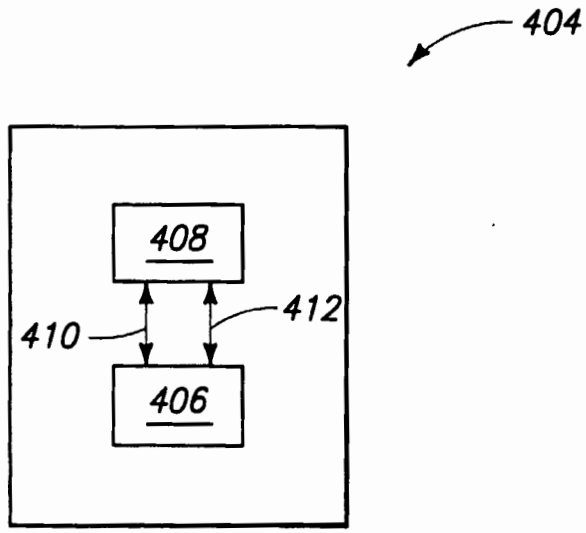


Fig 29

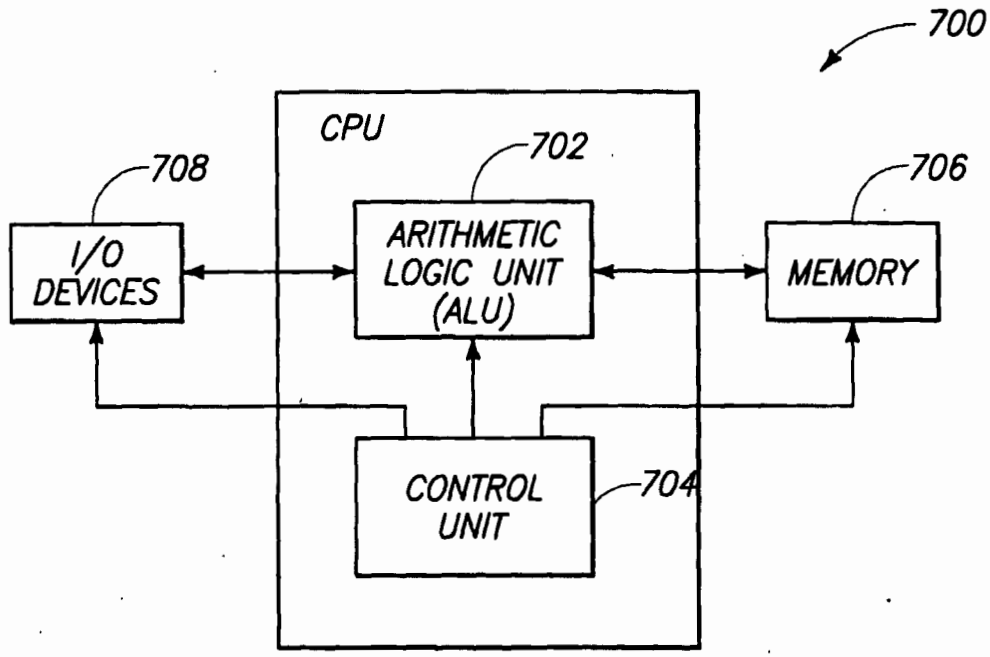

700
CPU
702
708
706
I/O DEVICES
ARITHMETIC LOGIC UNIT (ALU)
MEMORY
CONTROL UNIT
704

Fig 30

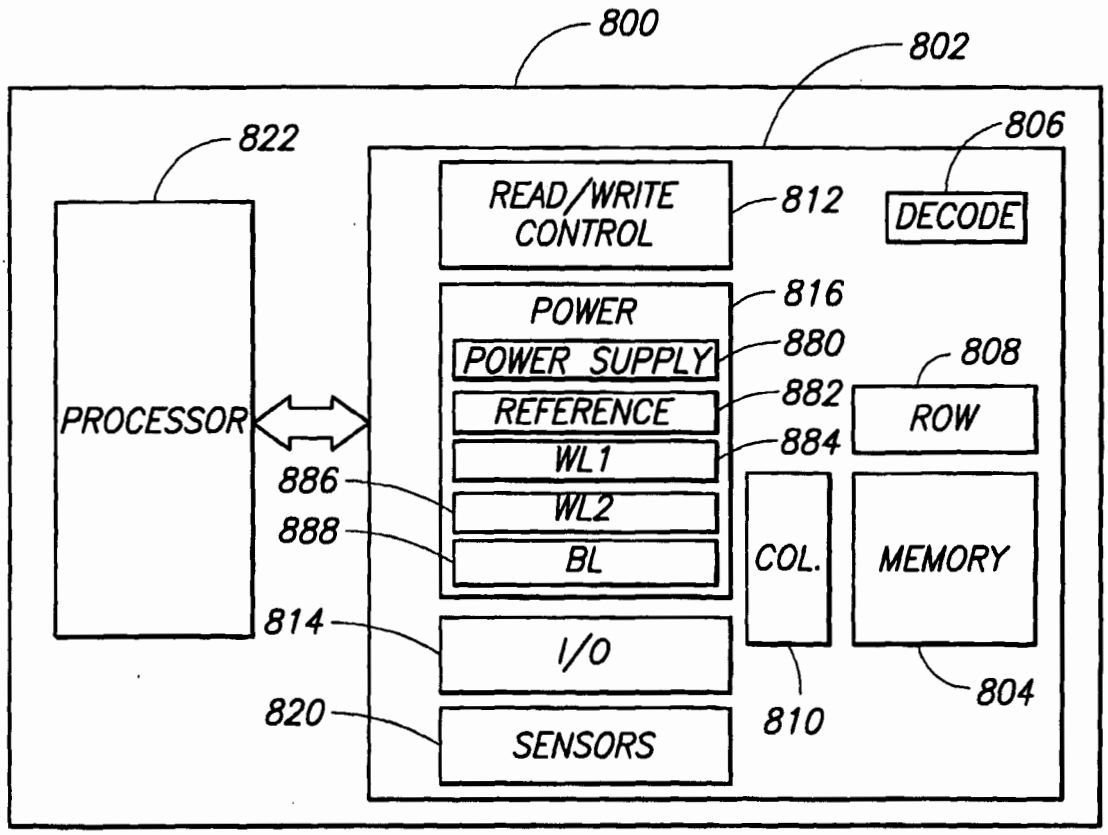

800
802
822
806
READ/WRITE CONTROL
812
DECODE
816
POWER
POWER SUPPLY
880
808
882
REFERENCE
ROW
PROCESSOR
884
WL1
886
WL2
888
BL
COL.
MEMORY
814
I/O
804
820
SENSORS
810

Fig 31