

WRITE REQUEST PROCESSING METHOD, PROCESSOR, AND COMPUTER

ABSTRACT

Embodiments of the present invention provide a write request processing method, a processor,
5 and a computer. A first computer is connected to a second computer, respective operating systems
run on the first computer and the second computer respectively, the first computer includes a first
processor, the first processor is connected to a second processor of the second computer by using a
system bus, the first computer includes a first memory address space, a second memory address
10 space of the second computer is a mirror address space of the first memory address space, and the
first processor mirrors data written into the first memory address space to the second memory
address space by using the system bus, which can reduce mirroring operation latency and improves
IOPS performance of a system.

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FIGURE 2